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March 23, 1984

Dr. T. R. Vance
New Products Program Manager
IBM Corporation
East Fishkill Facility
Route 52
Hopewell Junction, N.Y. 12533

Dear Dr. Vance:

It is a pleasure to write in support of the Jack Morton Award to Robert Kerwin, Donald Klein and John Sarace for their invention of the self aligned silicon gate field effect transistor.

Certainly Intel's success to date is very heavily based on the application of this invention and subsequent extensions. The vast majority of the MOS devices made in the world today utilize the self aligned silicon gate structure. Thus, in a real respect, an industry whose earnings approach \$10B has grown from the use of this clever combination of materials and processes. The importance of the invention has clearly been established.

If I can supply any more details that would be useful, please let me know.

Sincerely yours,

A handwritten signature in blue ink, appearing to read "Gordon E. Moore". The signature is stylized and written over a faint, large watermark of the word "Intel" in the background.

Gordon E. Moore
Chairman of the Board

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