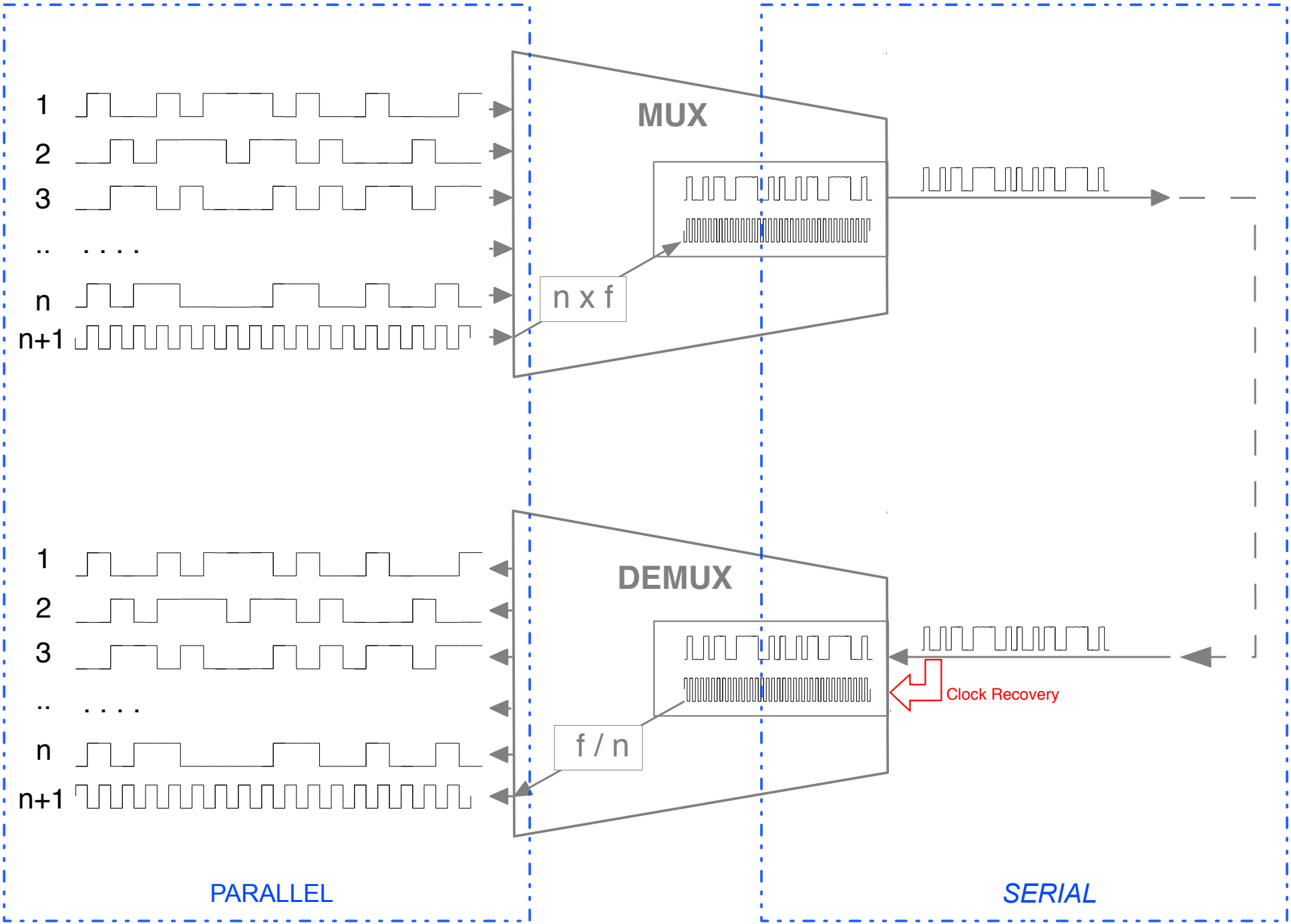
1
2
3
..
n
n+1
MUX
n x f
1
2
3
..
n
n+1
DEMUX
f / n
Clock Recovery
PARALLEL
SERIAL