

CMOS RCA (H.1)

20151215

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References

Some Figures from the following sites

[1] <http://pages.hmc.edu/harris/cmosvlsi/4e/index.html>

Weste & Harris Book Site

[2] en.wikipedia.org

[3] Digital Integrated Circuits : A Design Perspective,

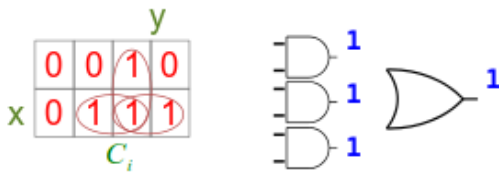
Jan M. Rabaey,

(<http://bwracs.eecs.berkeley.edu/Classes/lcBook/>)

[4] Digital Electronics and Design with VHDL

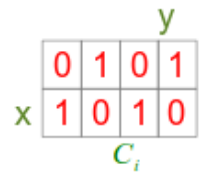
Pedroni

Boolean Algebra



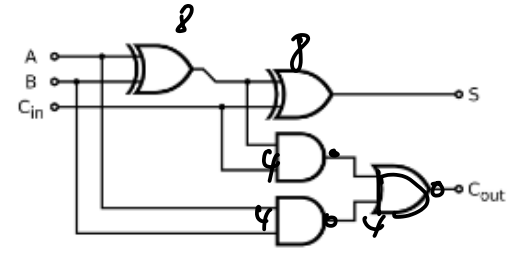
$$C_o = yC_i + xC_i + xy$$

$$\begin{aligned} C_o &= (x + y)C_i + xy \\ &= (\bar{x}y + x\bar{y} + xy)C_i + xy \\ &= (\bar{x}y + x\bar{y})C_i + xy(C_i + 1) \\ &= (x \oplus y)C_i + xy \end{aligned}$$



$$S = \bar{x}\bar{y}C_i + \bar{x}y\bar{C}_i + x\bar{y}\bar{C}_i + xyC_i$$

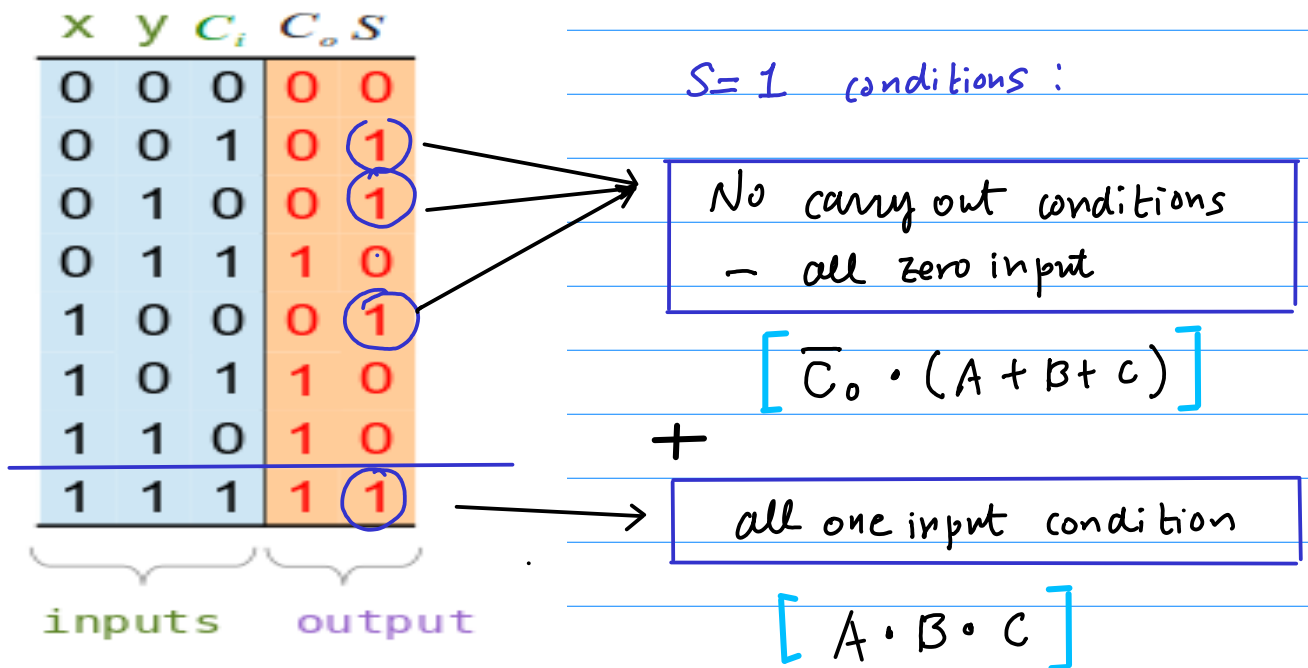
$$\begin{aligned} S &= (\bar{x}\bar{y} + xy)C_i + (\bar{x}y + x\bar{y})\bar{C}_i \\ &= (\overline{x \oplus y})C_i + (x \oplus y)\bar{C}_i \\ &= (x \oplus y) \oplus C_i \end{aligned}$$



12
16
28

$$C_o = AC_i + BC_i + AB$$

$$S = \underbrace{ABC_i}_{\text{three '1's}} + \underbrace{A\bar{B}\bar{C}_i + \bar{A}\bar{B}C_i + \bar{A}B\bar{C}_i}_{\text{only one '1'}}$$

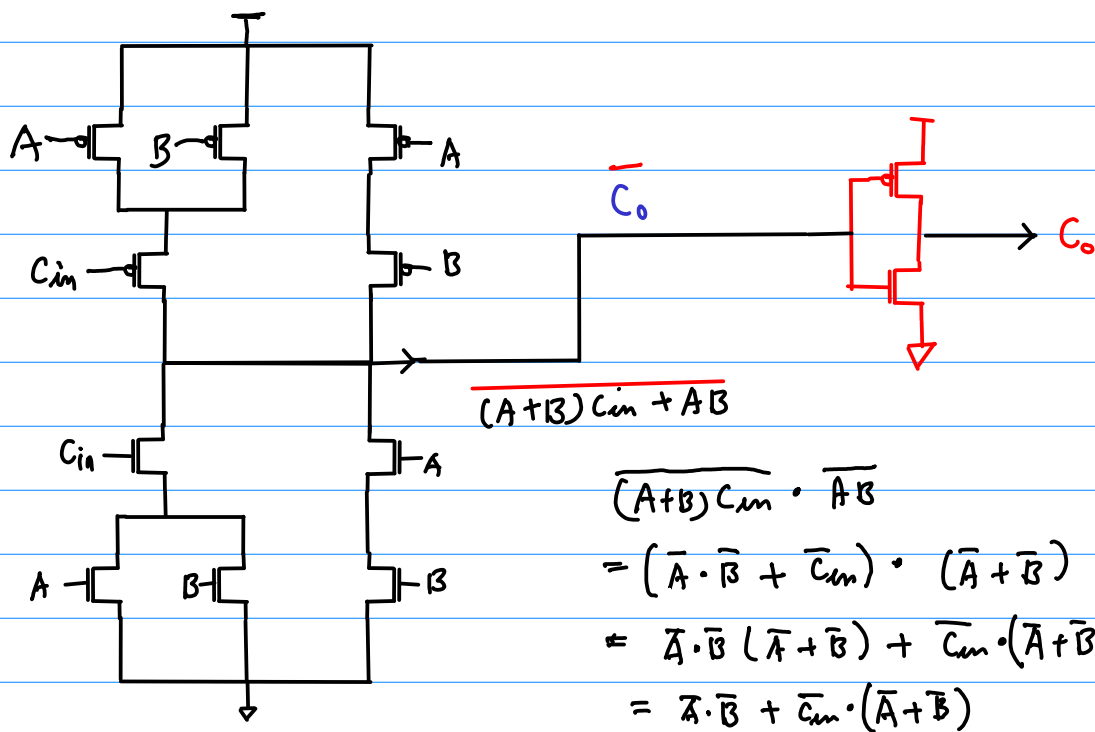
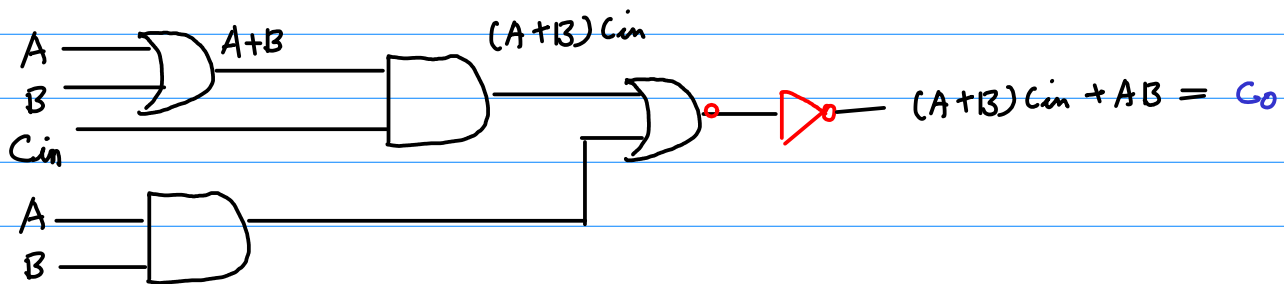
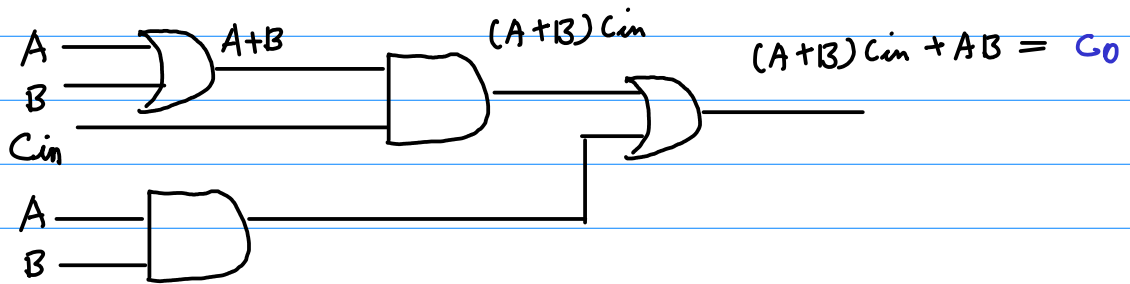


$$C_o = AC_i + BC_i + AB$$

$$S = \bar{C}_o \cdot (A + B + C) + A \cdot B \cdot C$$

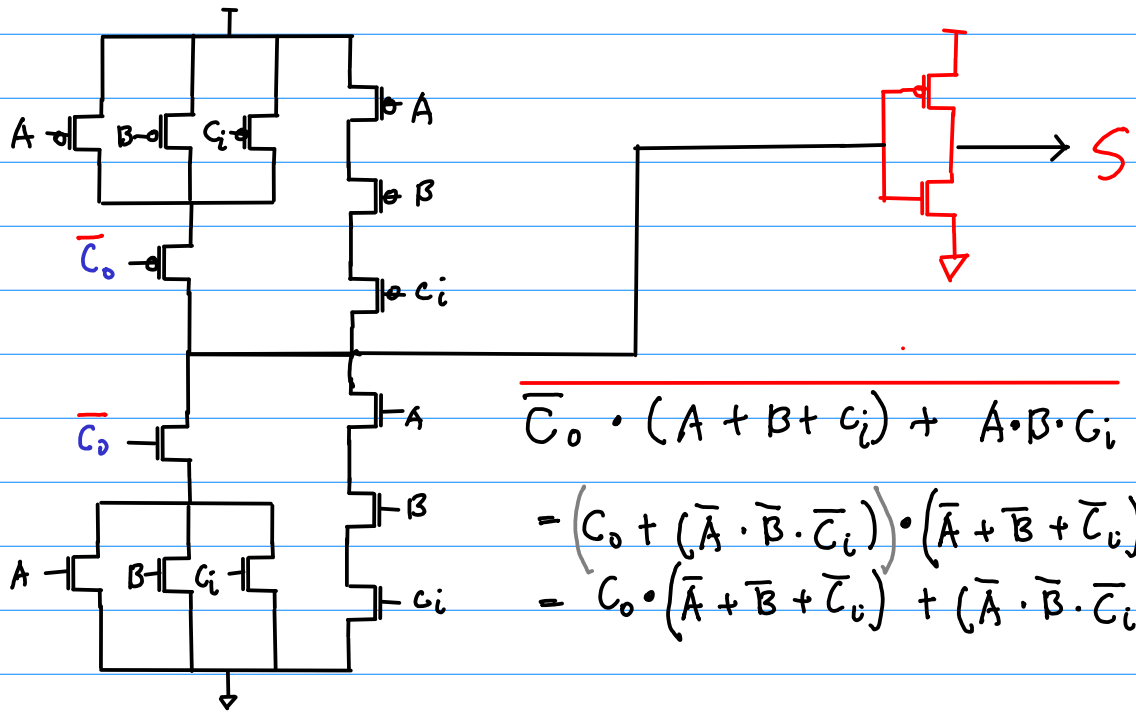
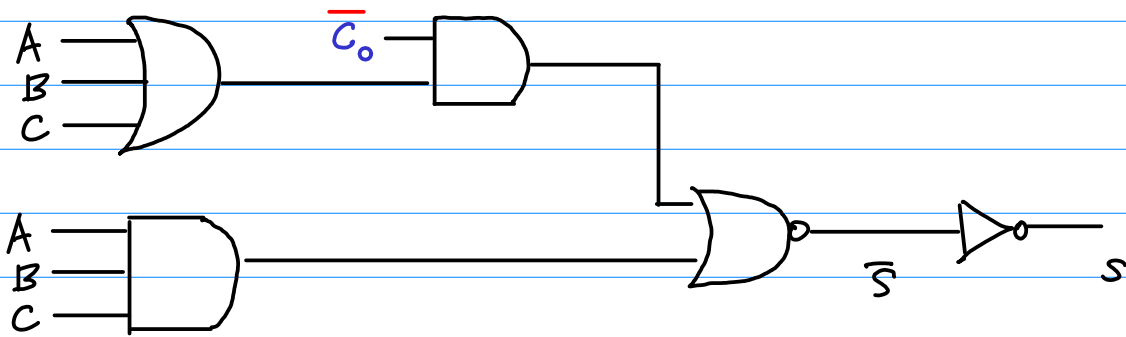
$$C_o = A C_i + B C_i + A B$$

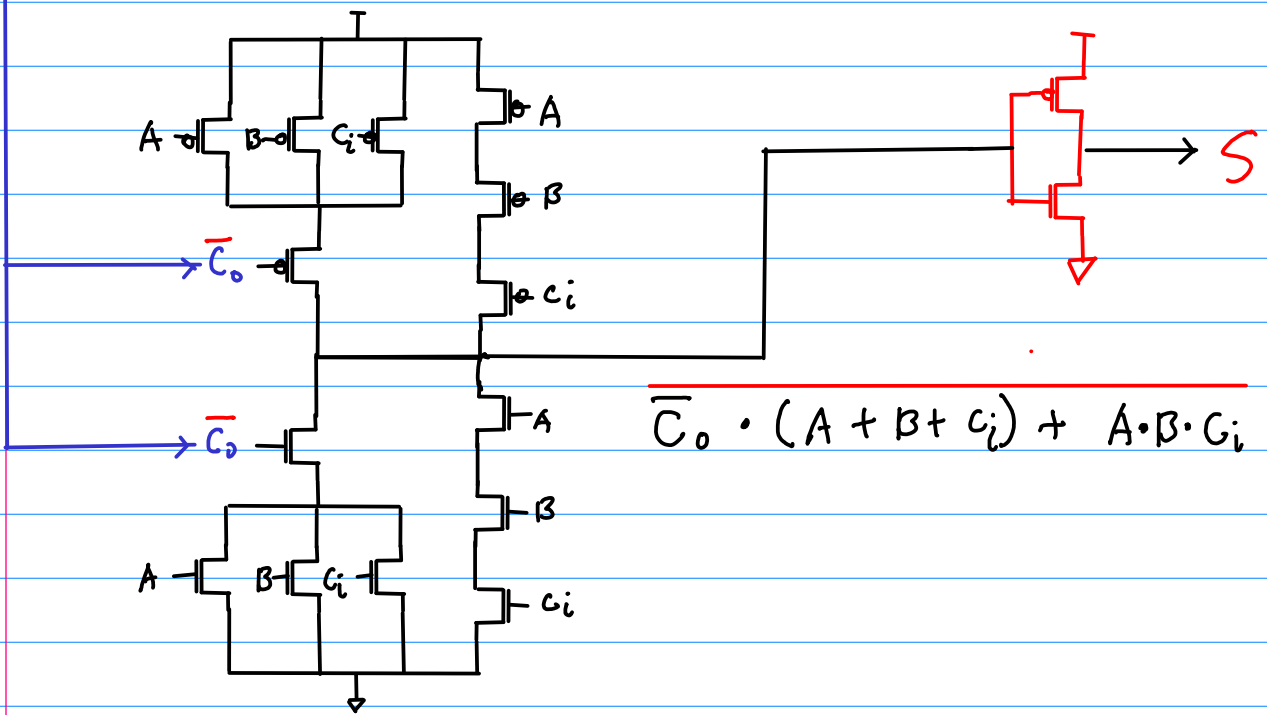
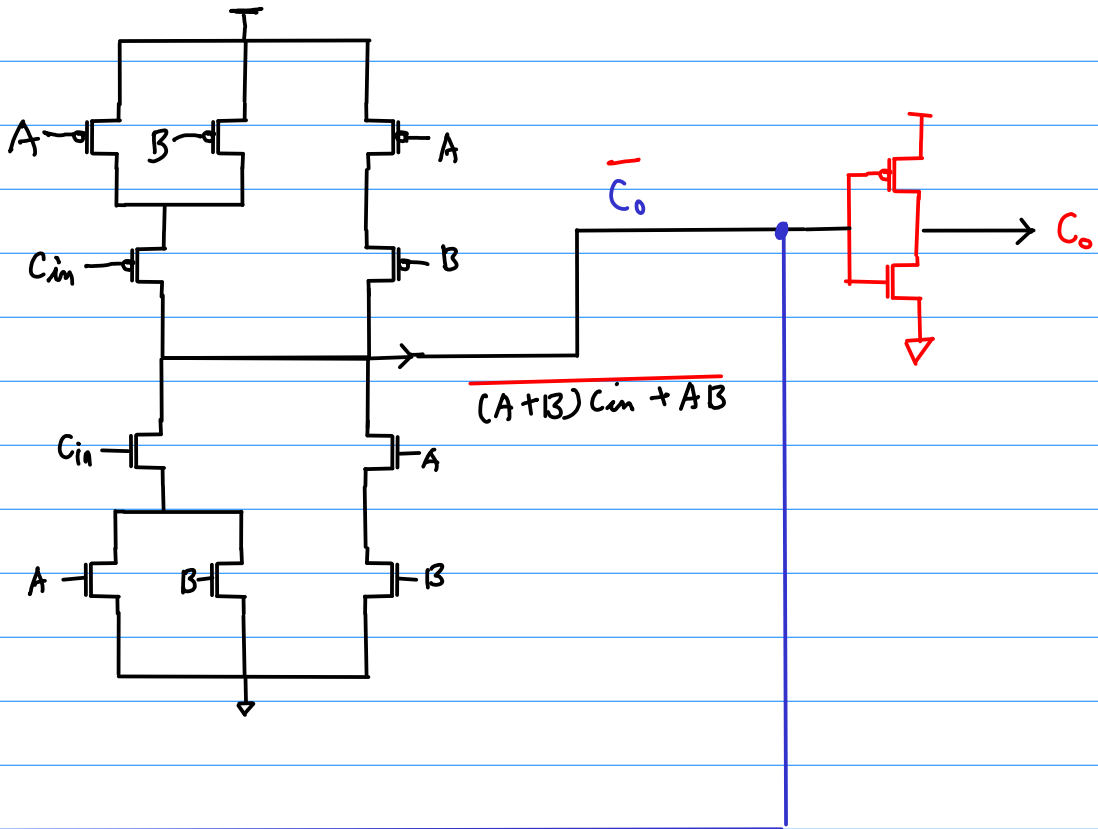
$$S = \bar{C}_o \cdot (A + B + C_i) + A \cdot B \cdot C_i$$



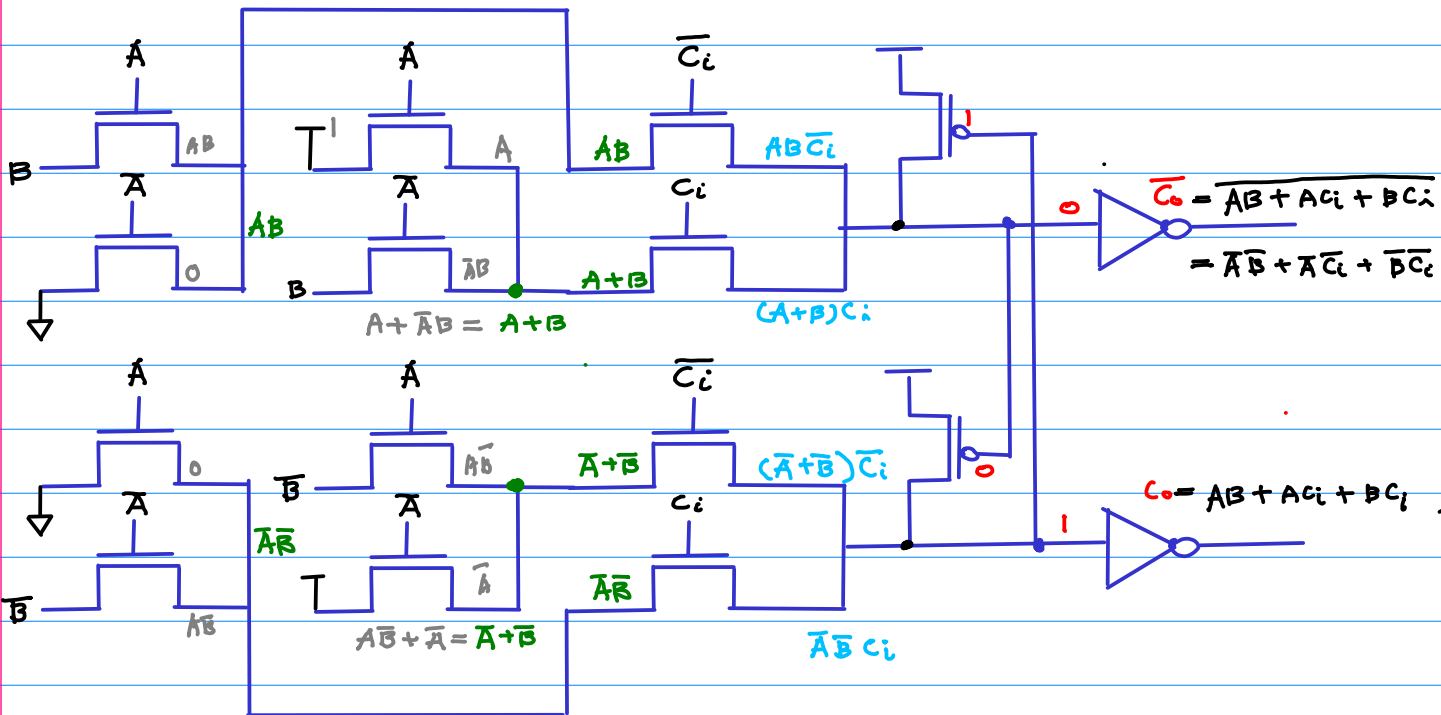
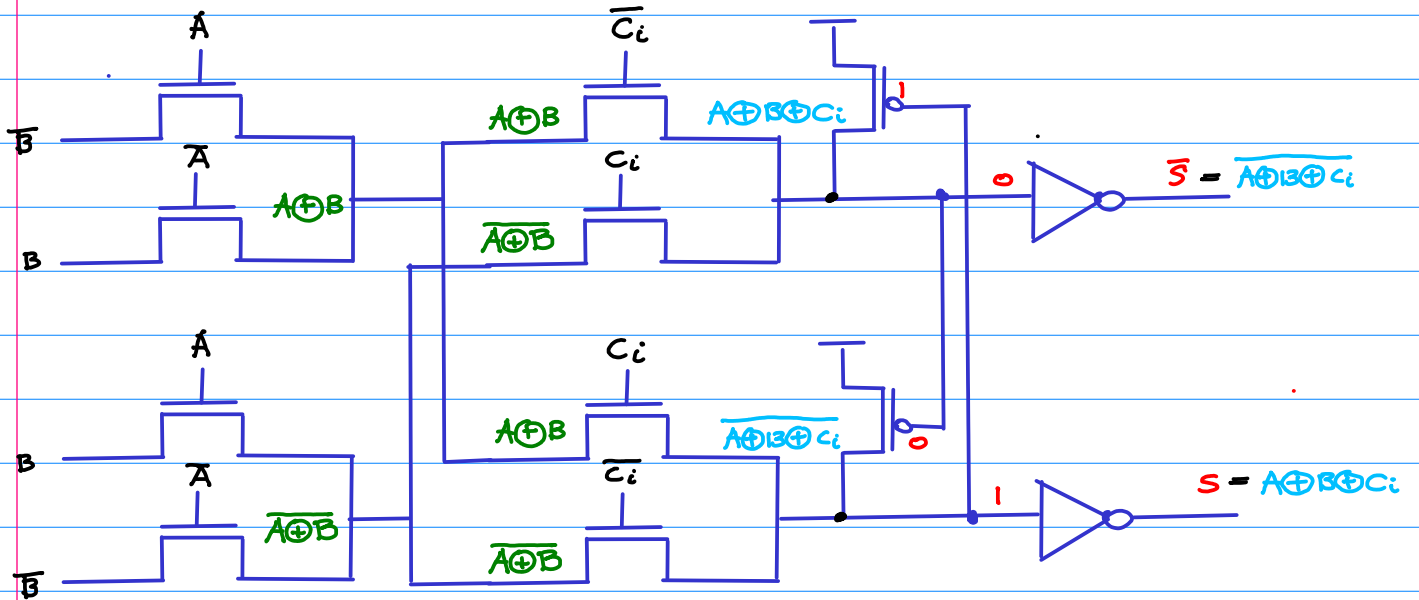
$$C_o = A c_i + B c_i + A B$$

$$S = \overline{C_o} \cdot (A + B + c_i) + A \cdot B \cdot c_i$$





CPL FA



$$AB\bar{C}_i + (A+B)C_i$$

	AB		
C_i	00	01	11
0			1
1		1	

	AB		
C_i	00	01	11
0	1	1	1
1	1		

CPL (Carry Propagate Adder)

or RCA (Ripple Carry Adder)

