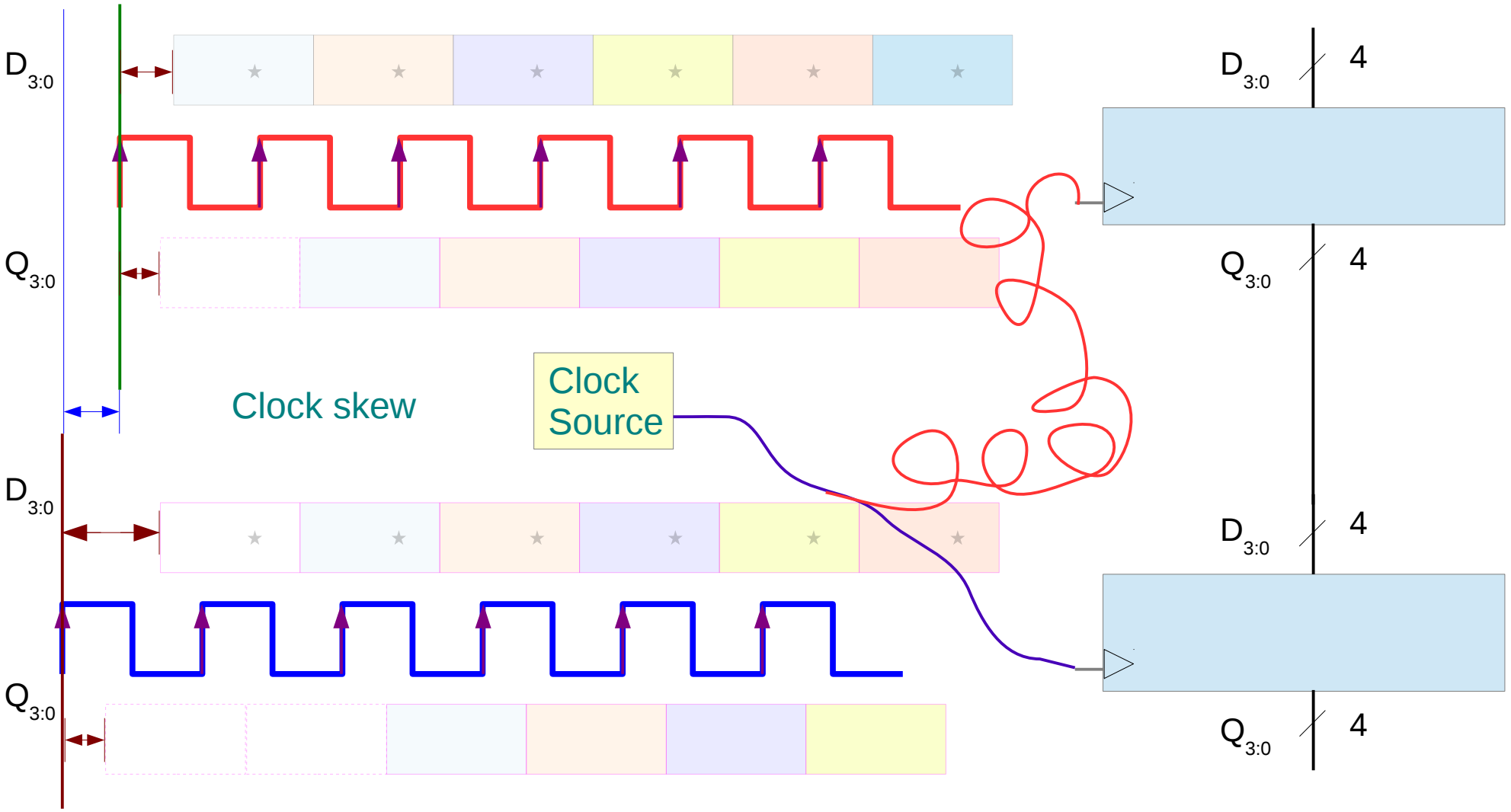
Clock Skew

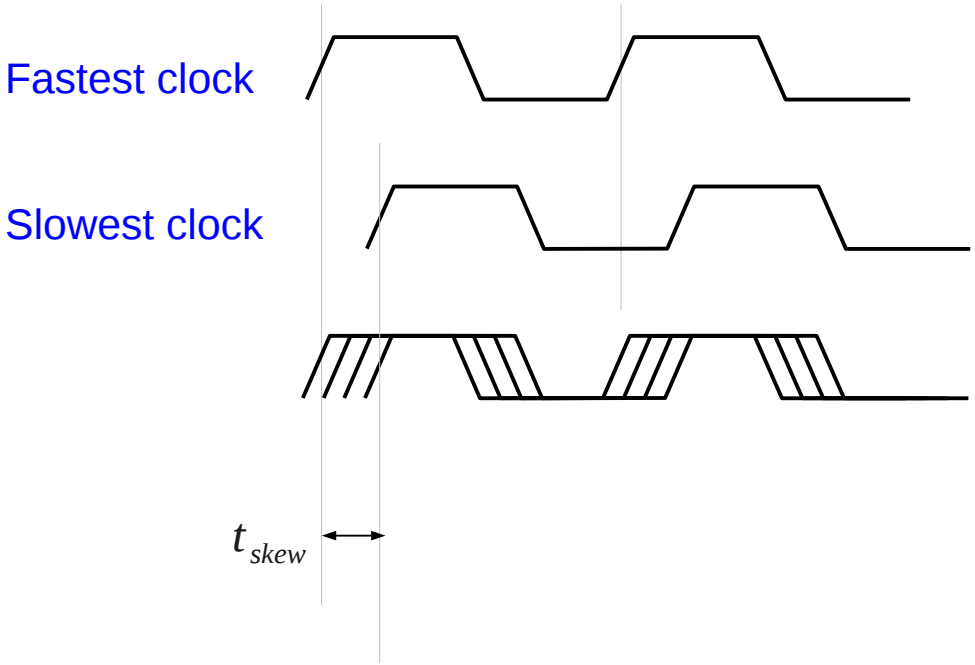
Please send corrections (or suggestions) to youngwlim@hotmail.com.

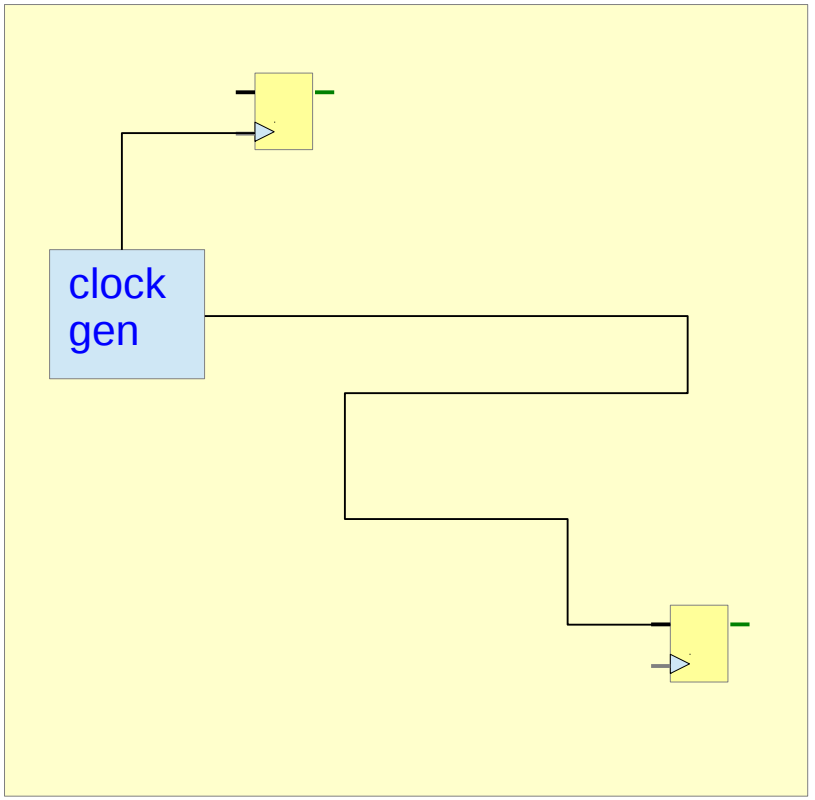
This document was produced by using OpenOffice and Octave.

Clock Skew

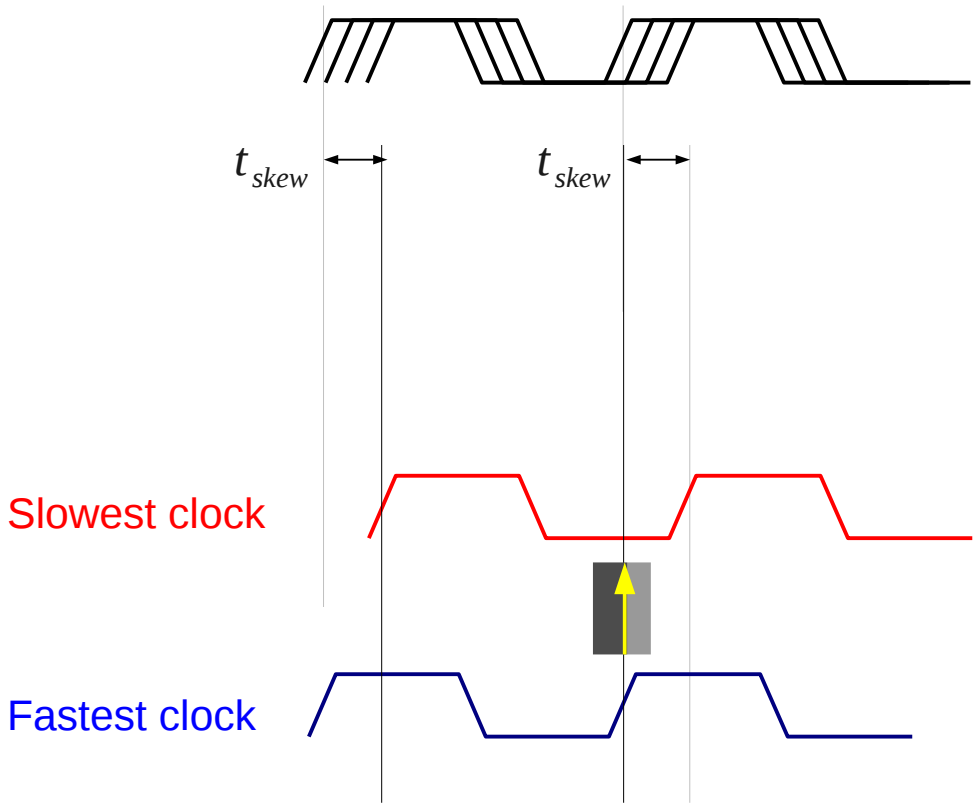


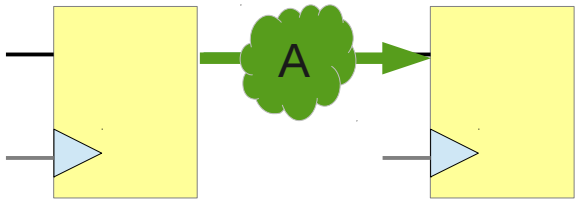
Clock Skew





Setup Time with Clock Skew

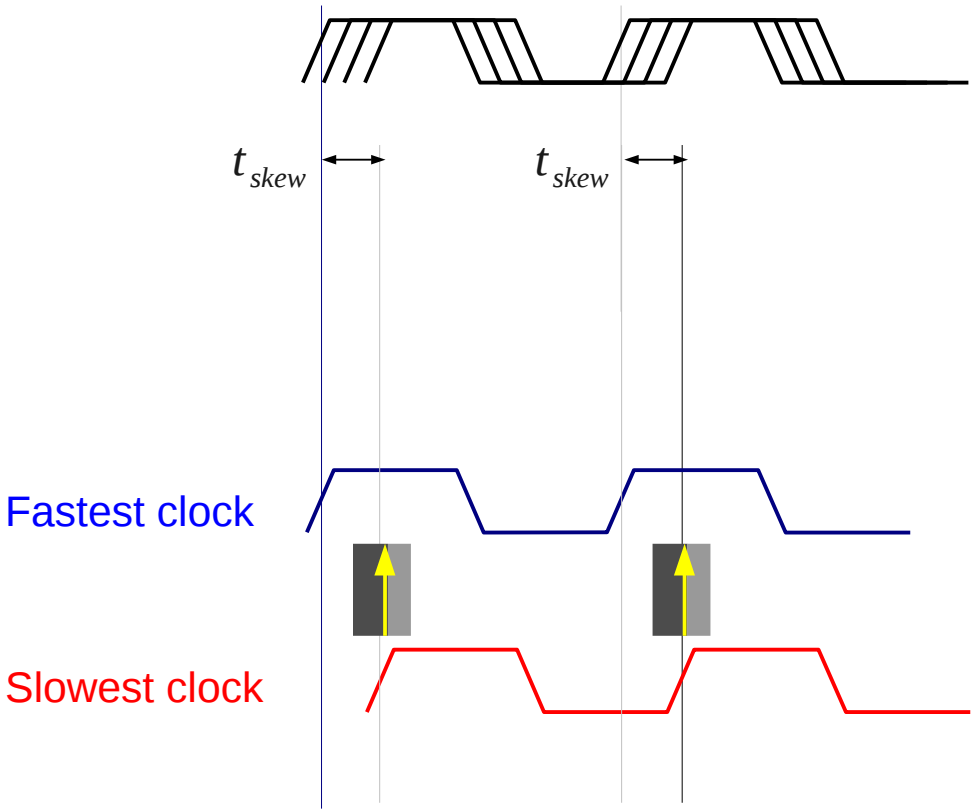


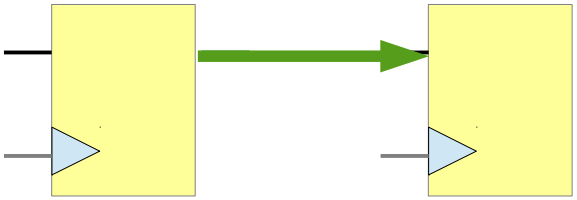


Slowest clock

Fastest clock

Hold Time with Clock Skew





Fastest clock

Slowest clock

Setup Time Constraints with Clock Skew

Hold Time Constraints with Clock Skew

Resolution Time

References

[1] http://en.wikipedia.org/
[2] M. M. Mano, C. R. Kime, “Logic and Computer Design Fundamentals”, 4th ed.
[3] J. Stephenson, Understanding Metastability in FPGAs. Altera Corporation white paper. July 2009.