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a planar graph is a graph that can be embedded in the plane, i.e., it can be <u>drawn</u> on the plane in such a way that its edges <u>intersect</u> only at their <u>endpoints</u>.

it can be drawn in such a way that no edges cross each other. Such a drawing is called a **plane graph** or **planar embedding** of the graph. (**planar representation**)

A **plane graph** can be defined as a planar graph with a mapping from every <u>node</u> to a <u>point</u> on a <u>plane</u>, and from every <u>edge</u> to a <u>plane curve</u> on that plane, such that the extreme points of each curve are the points mapped from its <u>end</u> nodes, and all curves are <u>disjoint</u> except on their extreme points.

#### **Planar Graph Examples**



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https://en.wikipedia.org/wiki/Planar\_graph

#### Planar Graph (7A)

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#### **Planar Representation**



### A planar bipartite graph







Bipartite graph but <u>not</u> complete bipartite graph K<sub>3,3</sub> Planar Graph

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## Non-planar Graph K<sub>3,3</sub>



no where  $v_6$ 





Non-planar

Discrete Mathematics, Rosen

### Non-planar graph examples $- K_{s}$



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## Non-planar graph examples $- K_{3,3}$



## Non-planar graph examples – embedding $K_{3,3}$



### Subdivision and Smoothing



### Homeomorphism

two graphs  $G_1$  and  $G_2$  are **homeomorphic** if there is a graph **isomorphism** from some **subdivision** of  $G_1$ to some **subdivision** of  $G_2$  homeo (identity, sameness) iso (equal)





homeomorphic graphs

https://en.wikipedia.org/wiki/Planar\_graph

#### Homeomorphism Examples



## Homeomorphism and Isomorphism





https://en.wikipedia.org/wiki/Planar\_graph

## Embedding on a surface

subdividing a graph preserves planarity.

Kuratowski's theorem states that

a finite graph is **planar** if and only if it contains **no** subgraph **homeomorphic** to  $K_5$  (complete graph on five vertices) or  $K_{3,3}$  (complete bipartite graph on six vertices, three of which connect to each of the other three).

In fact, a graph homeomorphic to  $K_5$  or  $K_{3,3}$  is called a Kuratowski subgraph.



A finite graph is **planar** if and only if it does <u>not</u> contain a **subgraph** that is a **subdivision** of the complete graph  $K_5$  or the complete bipartite graph  $K_{33}$  (utility graph).

A **subdivision** of a graph results from inserting vertices into edges (changing an edge •——• to •—•) <u>zero or more times</u>.



An example of a graph with no  $\sim$   $K_5$  or  $K_{3,3}$  subgraph. However, it contains a subdivision of  $K_{3,3}$  and is therefore non-planar.

#### Kuratowski's Theorem



# Homeomorphic to $K_{3,3}$





# Non-planar graphs: $K_6$ and $K_{3,3}$



Planar Graph (7A)

Young Won Lim 6/20/18 **Euler's formula** states that if a **finite**, **connected**, **planar graph** is drawn in the plane without any edge intersections, and **v** is the number of **vertices**, **e** is the number of **edges** and **f** is the number of **faces** (regions bounded by edges, including the outer, infinitely large region), then

v – e + f = 2

#### Euler's Formula Examples



Planar Graph (7A)

Young Won Lim 6/20/18 In a finite, connected, simple, planar graph,

any **face** (except possibly the outer one) is bounded by <u>at least three</u> **edges** and

every edge touches at most two faces;

using Euler's formula, one can then show that these graphs are **sparse** in the sense that if  $v \ge 3$ :



e ≤ 3 v − 6

#### **Corollary 1 Examples**



Planar Graph (7A)

Young Won Lim 6/20/18 In a finite, connected, simple, planar graph,

Every vertex has a **degree** not exceeding **5**.

deg(v) ≤ 5

#### **Corollary 2 Examples**



Planar Graph (7A)

Young Won Lim 6/20/18 the dual graph of a plane graph G is a graph that has a **vertex** for each **face** of G.

The dual graph has an **edge** whenever two **faces** of G are <u>separated</u> from each other by an **edge**,

and a **self-loop** when the <u>same</u> **face** appears on <u>both</u> <u>sides</u> of an **edge**.

each **edge e** of G has a corresponding <u>dual</u> <u>edge</u>, whose <u>endpoints</u> are the <u>dual vertices</u> corresponding to the **faces** on <u>either side</u> of **e**.



### **Dipoles and Cycles**





## Self-loop in a dual graph





a **self-loop** when the <u>same</u> **face** appears on <u>both</u> <u>sides</u> of an **edge**.

https://www.math.hmc.edu/~kindred/cuc-only/math104/lectures/lect17-slides-handout.pdf

Hamiltonian Cycles (3A)



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Faces of G
Edges of G
Dual of a plane graph
Cut edge of G
distinct faces of G with multiple
common boundary edges

https://en.wikipedia.org/wiki/Hamiltonian\_path



a **cut** is a **partition** of the **vertices** of a graph into two disjoint **subsets**.

Any **cut** determines a **cut-set** the **set** of **edges** that have one endpoint in <u>each</u> <u>subset</u> of the partition.

These edges are said to **cross** the cut.

In a connected graph, each **cut-set** determines a <u>unique</u> **cut**, and in some cases cuts are identified with their **cut-sets** rather than with their **vertex** partitions.

https://en.wikipedia.org/wiki/Cut\_(graph\_theory)

A cut is minimum if the size or weight of the cut is not larger than the size of any other cut.

the size of this cut is 2, and there is no cut of size 1 because the graph is bridgeless.



https://en.wikipedia.org/wiki/Cut\_(graph\_theory)

### Maximum Cut

A cut is maximum if the size of the cut is not smaller than the size of any other cut.

the size of the cut is equal to 5, and there is no cut of size 6, or |E| (the number of edges), because the graph is not bipartite (there is an odd cycle).



https://en.wikipedia.org/wiki/Cut\_(graph\_theory)

The concept of duality applies as well to **infinite graphs** embedded in the plane as it does to **finite graphs**.

When all faces are bounded regions surrounded by a cycle of the graph, an **infinite planar** graph embedding can also be viewed as a **tessellation** of the plane, a covering of the plane by closed disks (the **tiles** of the **tessellation**) whose interiors (the **faces** of the **embedding**) are disjoint open disks.



#### **Dual Logic Graph**



http://www.cse.psu.edu/~kxc104/class/cmpen411/11s/lec/C411L06StaticLogic.pdf

## **Stick Layout**



http://www.cse.psu.edu/~kxc104/class/cmpen411/11s/lec/C411L06StaticLogic.pdf

#### **CMOS** Transistors and Stick Layout



https://en.wikipedia.org/wiki/CMOS

### Single-Strip Stick Graph and Logic Graph





http://www.cse.psu.edu/~kxc104/class/cmpen411/11s/lec/C411L06StaticLogic.pdf

## Stick Graph and Logic Diagram



http://www.cse.psu.edu/~kxc104/class/cmpen411/11s/lec/C411L06StaticLogic.pdf



#### Hamiltonian Cycles (3A)

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### Stick Graph and Logic Diagram



#### **Eulerian Trail**

http://www.cse.psu.edu/~kxc104/class/cmpen411/11s/lec/C411L06StaticLogic.pdf

#### **Eulerian Circuit**

#### References

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