

Instruction Set Architecture Overview (1A)

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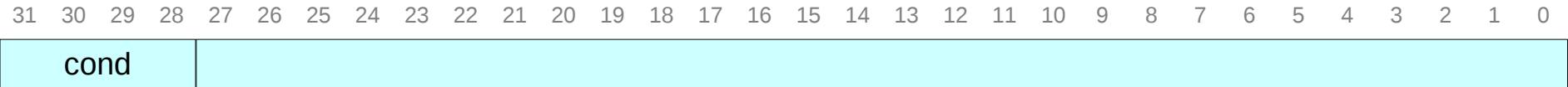
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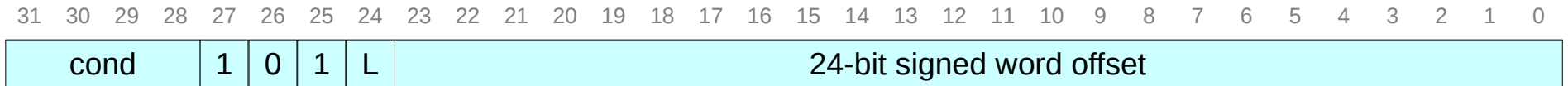
Based on

ARM System-on-Chip Architecture, 2nd ed, Steve Furber

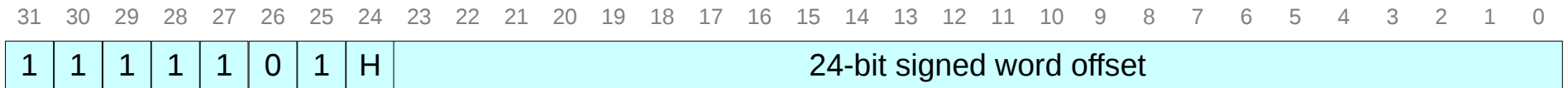
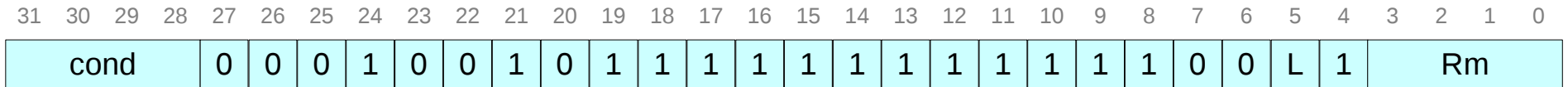
Condition Code



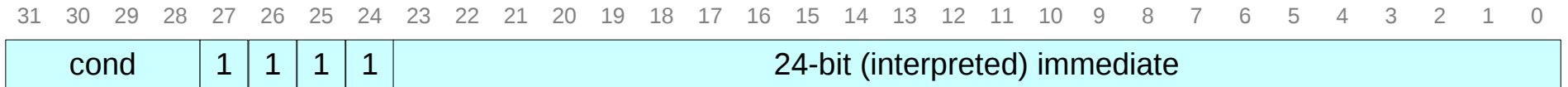
Branch and Branch with Link (B, BL)



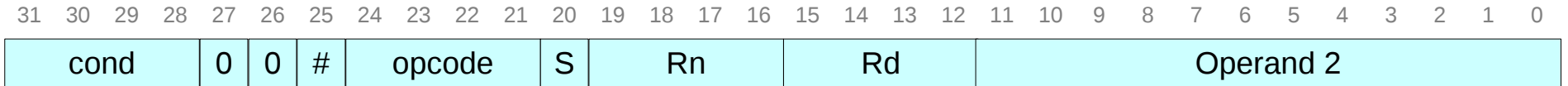
Branch, Branch with Link and eXchange (BX, BLX)



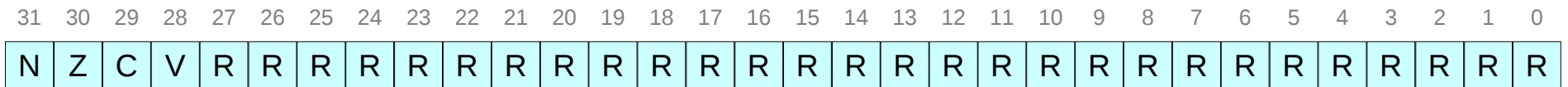
SWI (Software Interrupt)



Data Processing Instructions



ARM Exception Handling



References

- [1] <ftp://ftp.geoinfo.tuwien.ac.at/navratil/HaskellTutorial.pdf>
- [2] <https://www.umiacs.umd.edu/~hal/docs/daume02yaht.pdf>