Booth Encoding

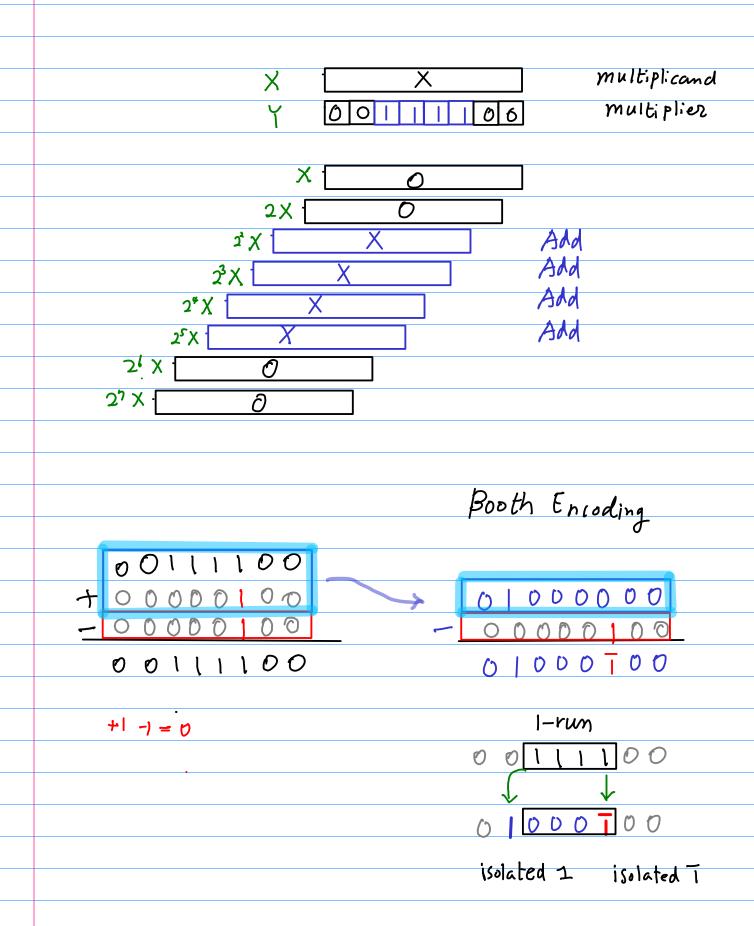
20161005

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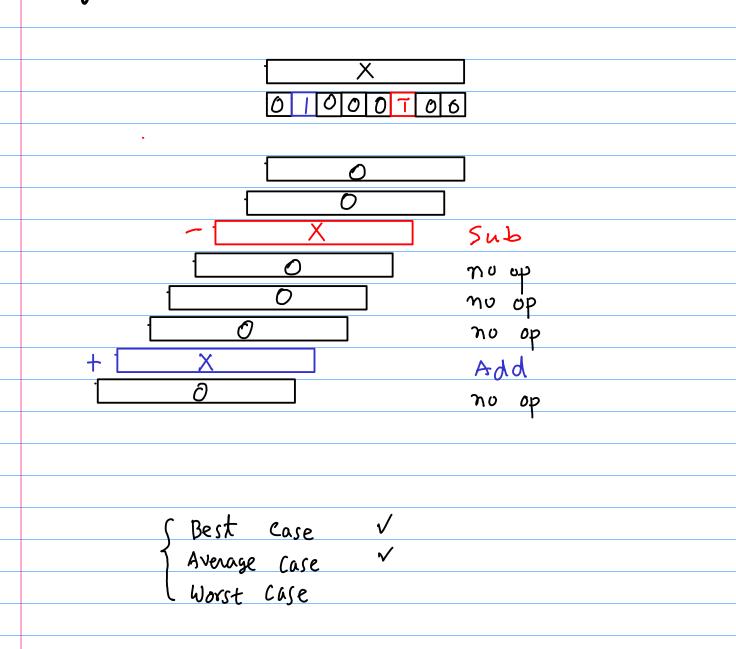
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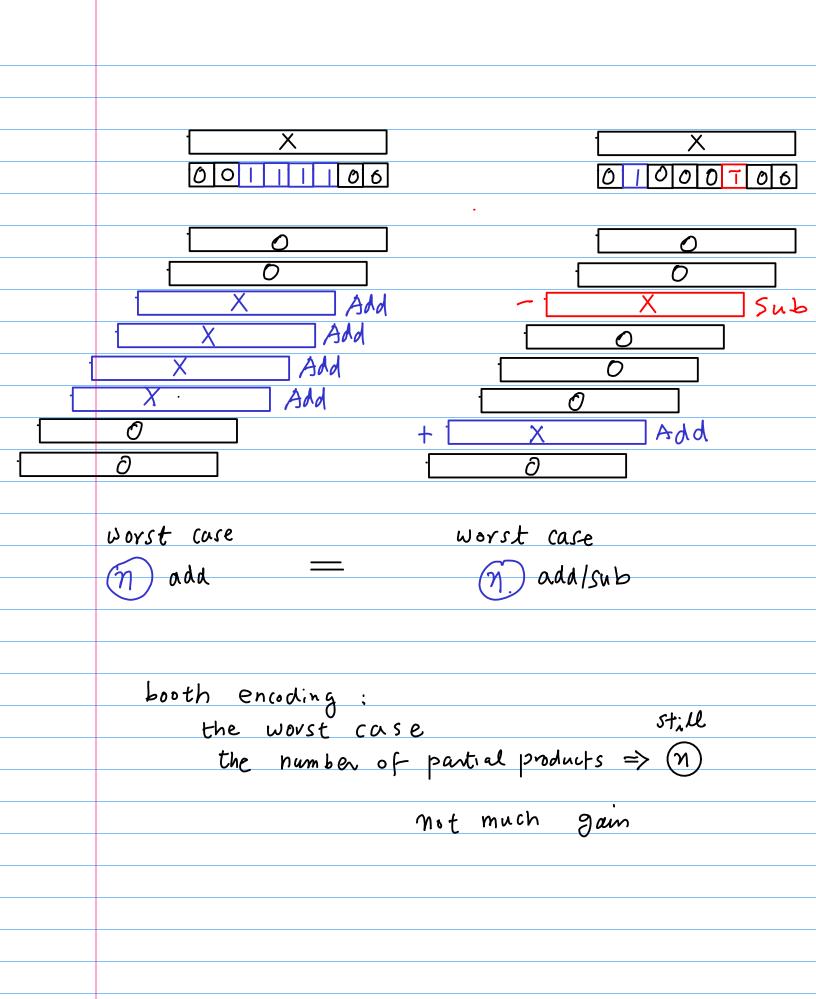
Based on
MJ Flynn's EE 486 Lecture 7 : Integer Multiplication, Stanford University

of Partial Products to be added



of Partial Products to be added Original Booth Encoded





reduces the number of partial products to be adoled delay reduction

works well for Serial multiplication
Variable latency

- Worst case: alternating 1's and T's
 IIIIII
- © Booth encoding does not significantly improve the worst case

> Use modified booth encoding

modified Booth 2

2-bit encoding + 1-bit overlapping

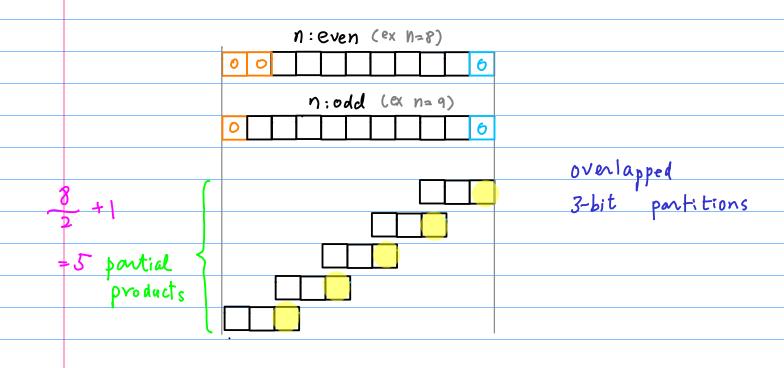
worst case # of p.p.s = 2+1

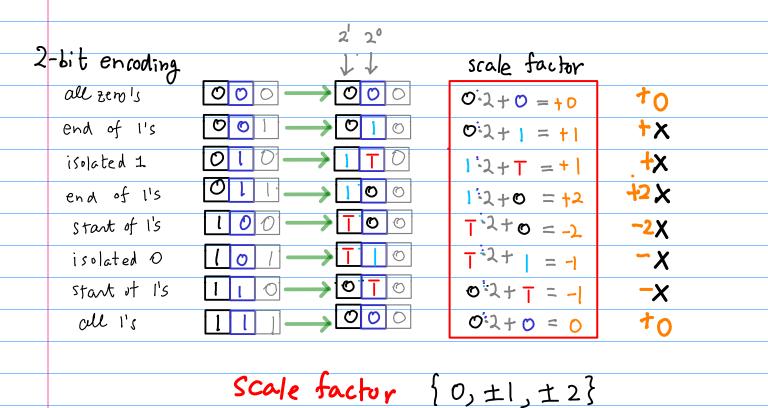
modified Booth 3

3-bit encoding + 1-bit overlapping

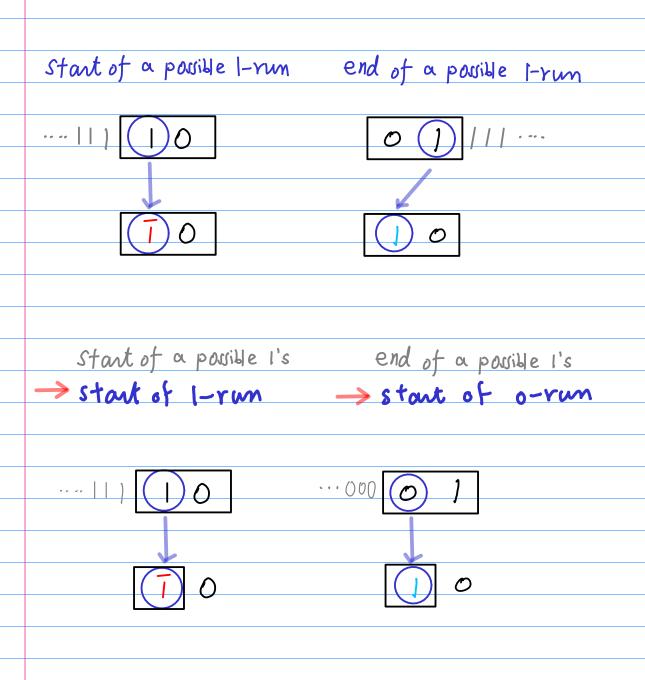
worst case # of p.p.s - 4+1

Modified Booth 2 (unsigned case)



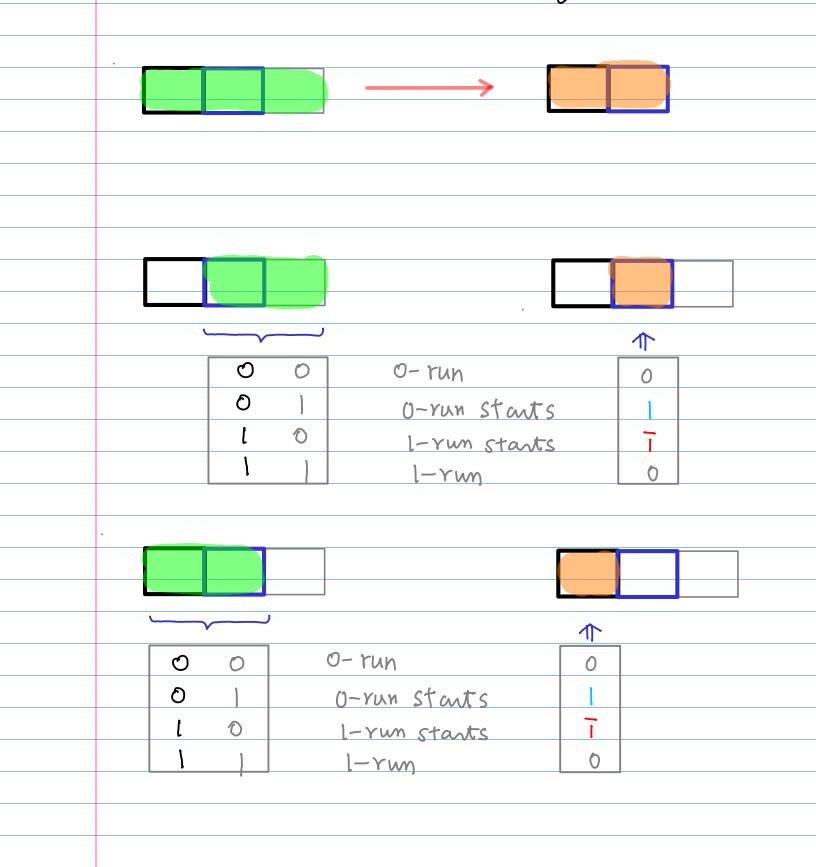


0-run & 1-run

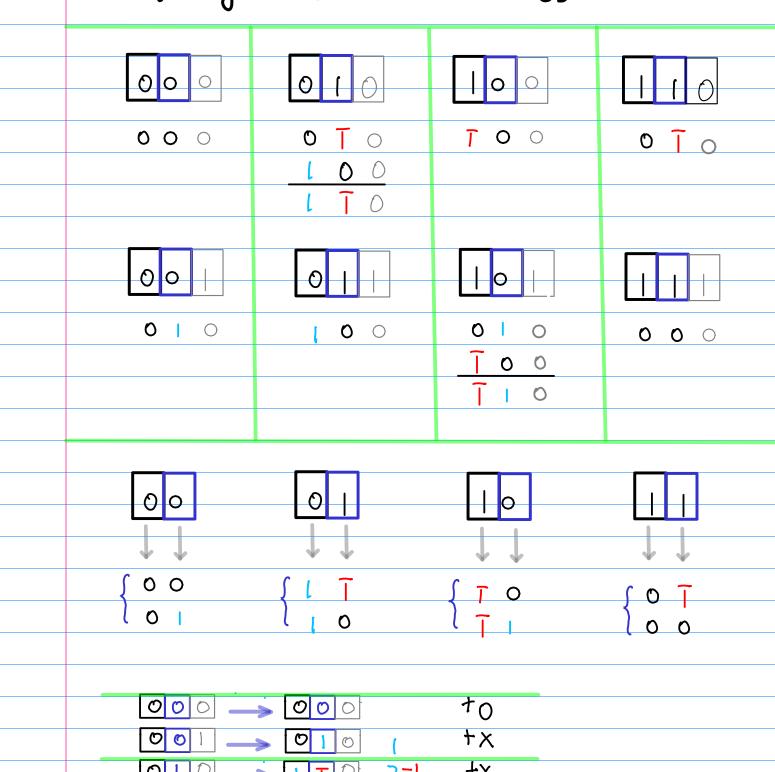


to encode a 1-bit, we need 2-bit info.

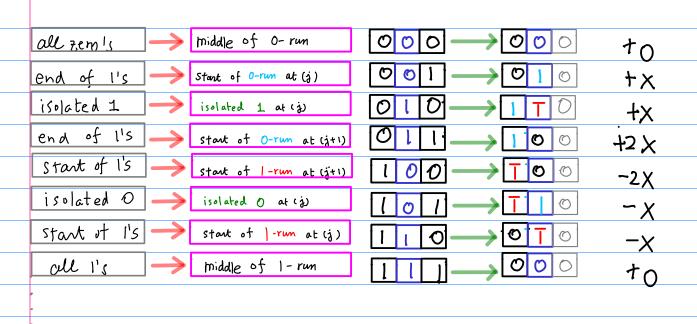
Needs 3-bits for encoding a 2-bit

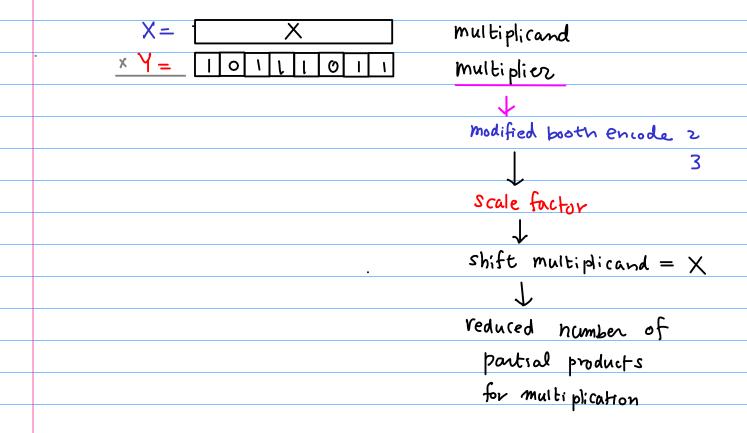


Encoding 2-bit: 2 choices

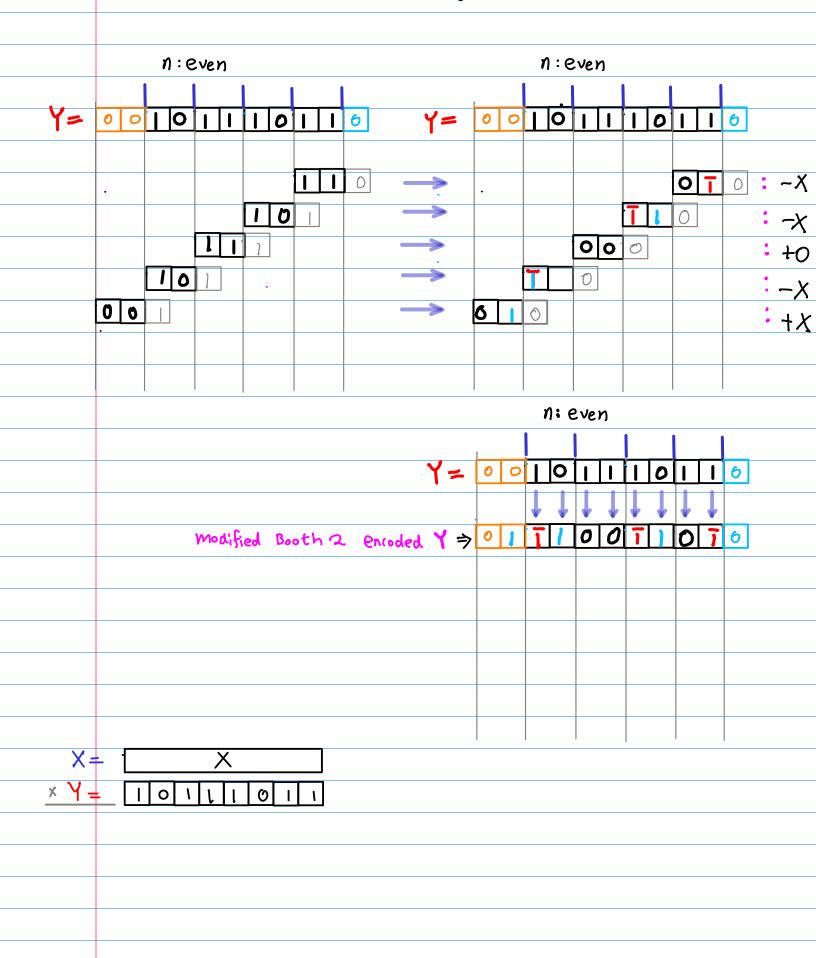


Scale Factor for 2-bit encoding

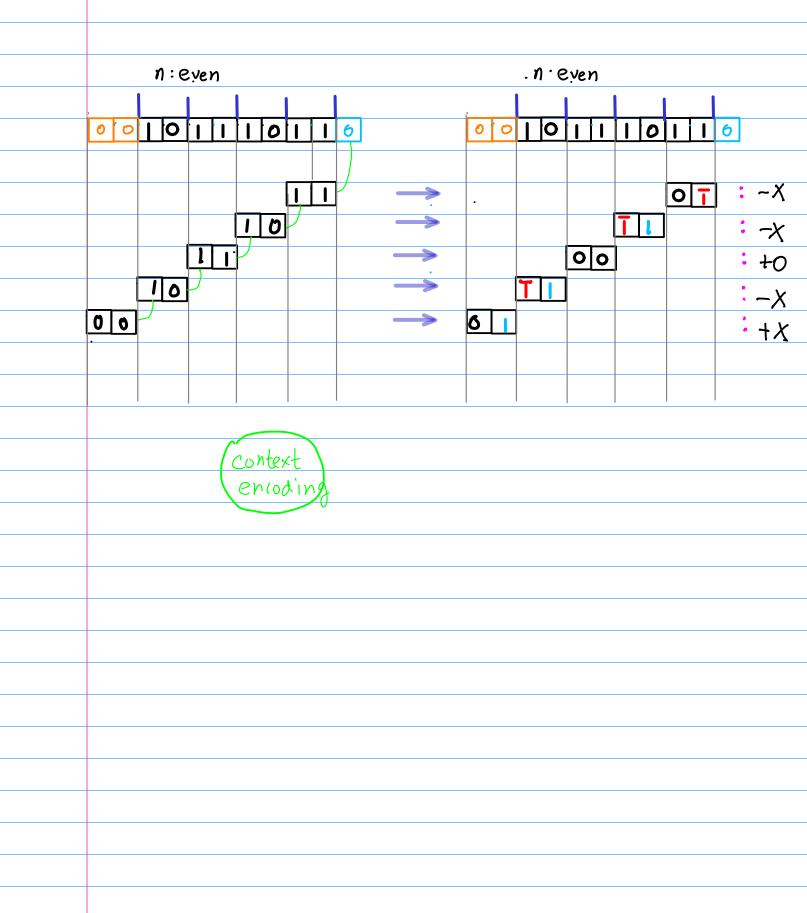




Multiplier Encoding



Basically, 2-bit encoding scheme



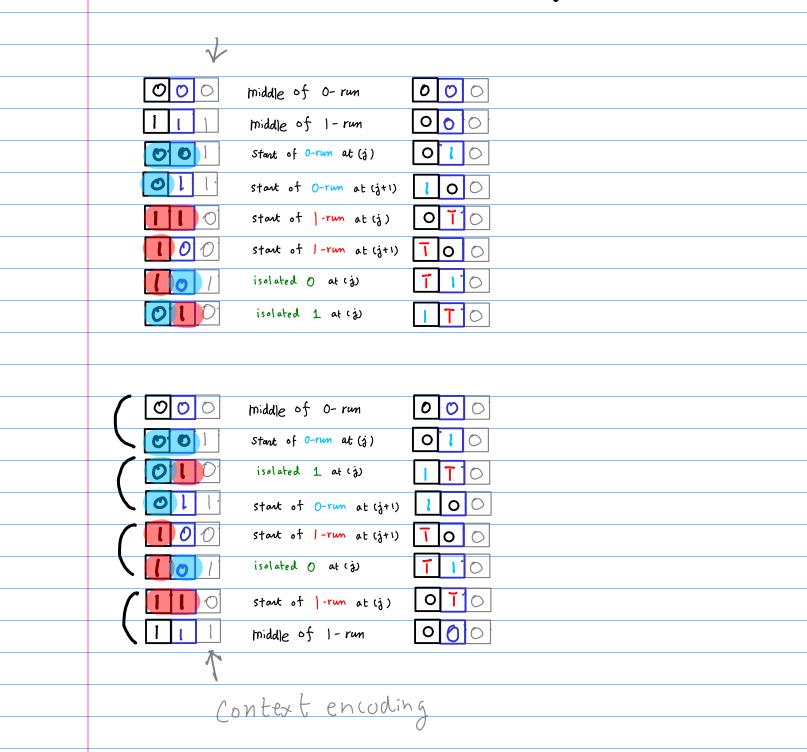
in creasing ovulapping bit -> no. need

0000	middle of 0-run	0000
00001	Middle of 0-run	0000
0010	Stant of 0-run at (j)	0 0 0
0011	Start of 0-rum at (j)	0 1 0 0
0100	isolated 1 at (3)	1 7 0 0
0101	isolated 1 at (3)	1700
0110	start of 0-rum at (j+1)	10000
0111	start of O-run at (j+1)	1000
1000	start of 1-run at (j+1)	T 0 0 0
1001	start of 1-run at (j+1)	T 0 0 0
1010	isolated O at (3)	1 0 0
1011	isolated O at (j)	7 1 0 0
1100	start of -run at (j)	0 7 0 0
1 (0 /	start of [-run at (j)	0 7 0 0
1 1 1 0	middle of 1-run	0 0 0 0
1 1 1 1	middle of 1-run	0 0 0 0

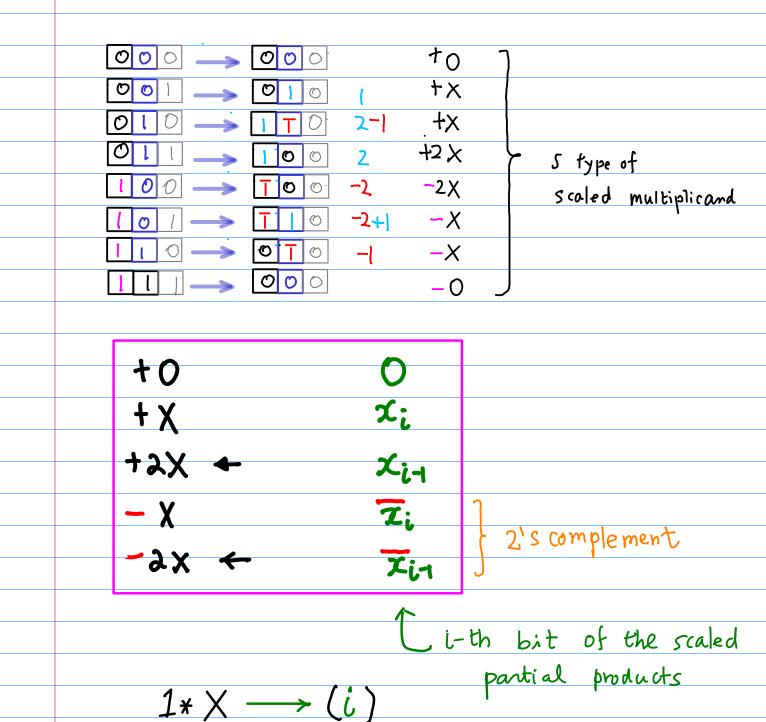
Overlapping bit at both ends -> no need

0000	middle of 0-run	0000
0001	Stant of 0-run at (j)	0010
0010	isolated 1 at (j)	6 I T O
0011	start of O-rum at (j+1)	0100
0 0 0	start of 1-run at (j+1)	0 7 0 0
0101	isolated O at (3)	0 1 1 0
0110	start of 1-run at (j)	6010
0 1 1	middle of 1-run	0000
1000	middle of 0-run	0 0 0 0
1001	Stant of 0-rum at (j)	0 0 1 0
1010	isolated 1 at (3)	6 I T O
	start of O-rum at (j+1)	0 1 0 0
1 1 0 0	start of 1-run at (j+1)	0 7 0 0
1 10 1	isolated O at (3)	0 7 1 0
1110	start of -run at (j)	0 0 1 0
111	middle of 1-rum	0 00 0

2-bit context encoding



5 Types of Partial Products



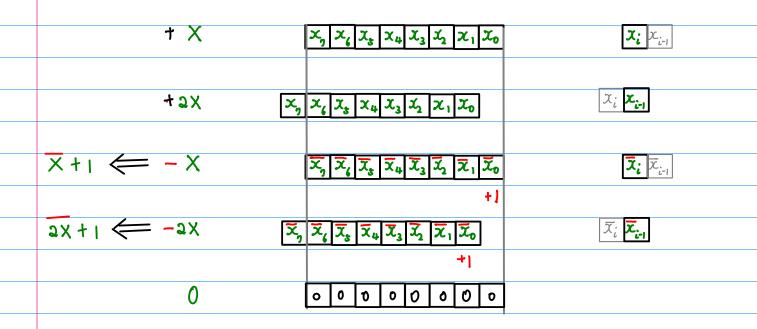
$$2 \times \times \longrightarrow (i-1)$$

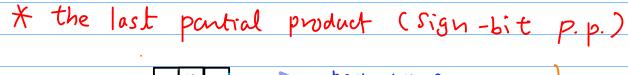
$$(-) \longrightarrow \boxed{1}$$

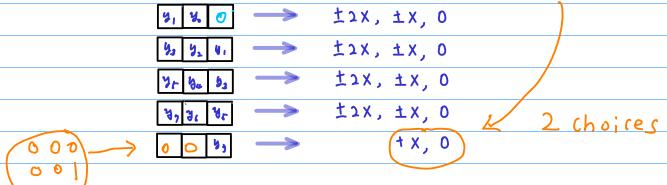
1,5 complement

5 types of Scaling × (= ma(tiplien)

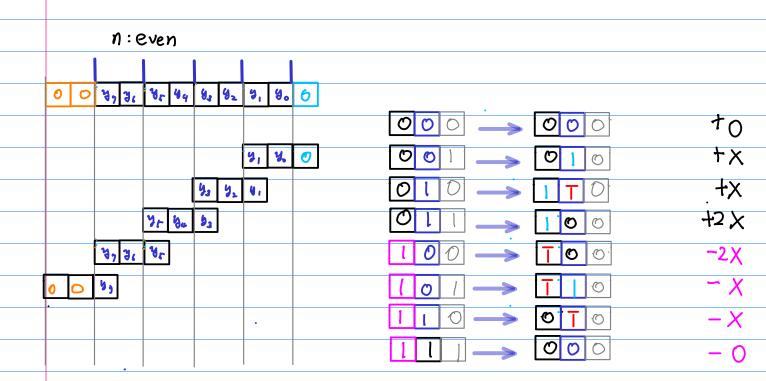
+0	0	
+ X	x_i	i-th bit of the scaled
+2X ←	X _{i-1}	partial products
- X	\overline{z}_{i}	
-ax ←	Fin	





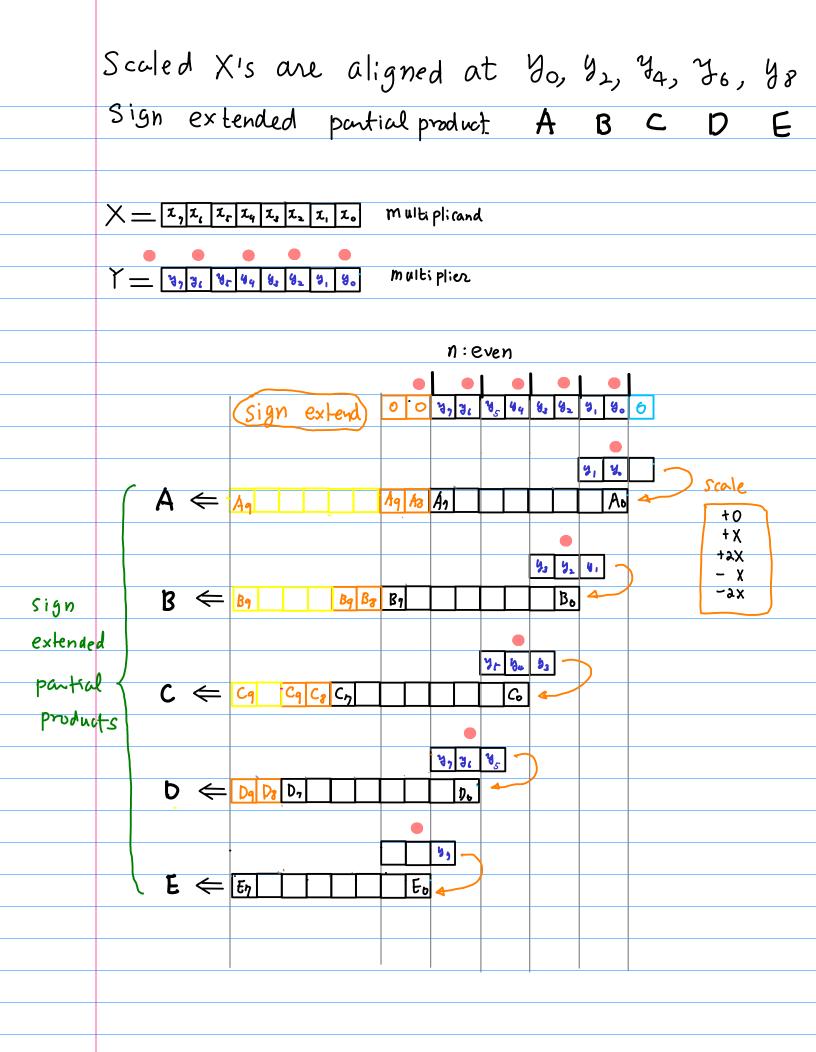


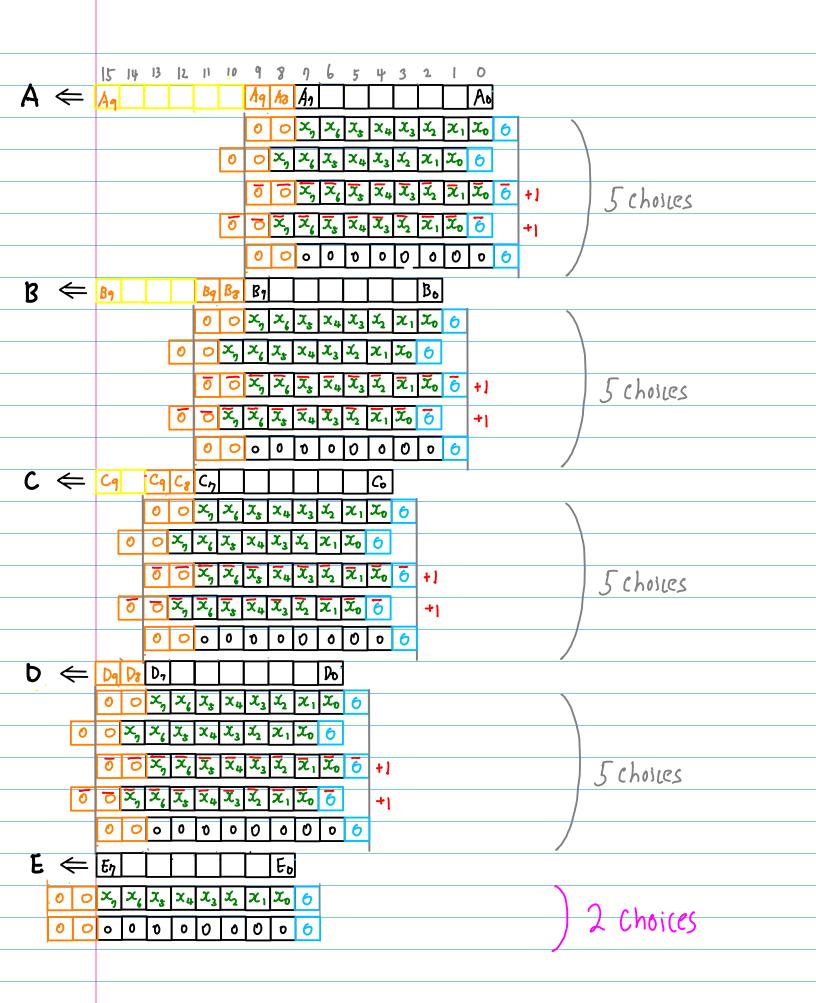
Y1, Y3, Y5, Y1 = 1 -> negative scaling

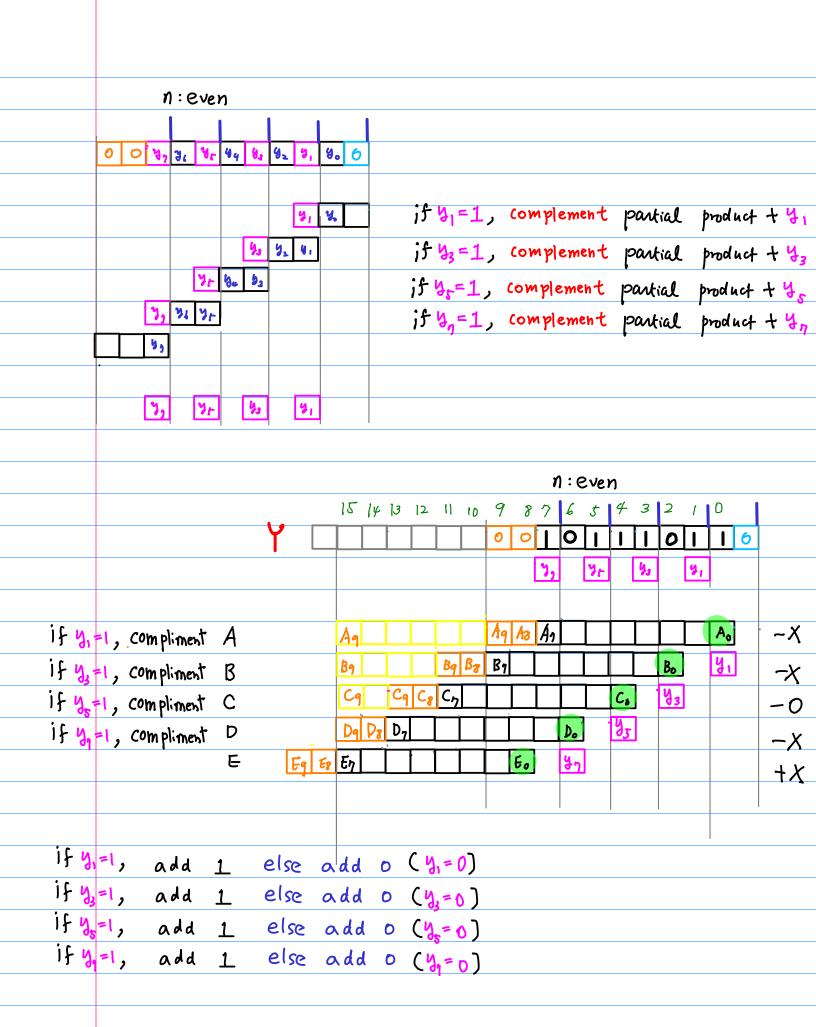


$$\begin{array}{c|cccc}
\hline
 & & & & \\
\hline
 &$$

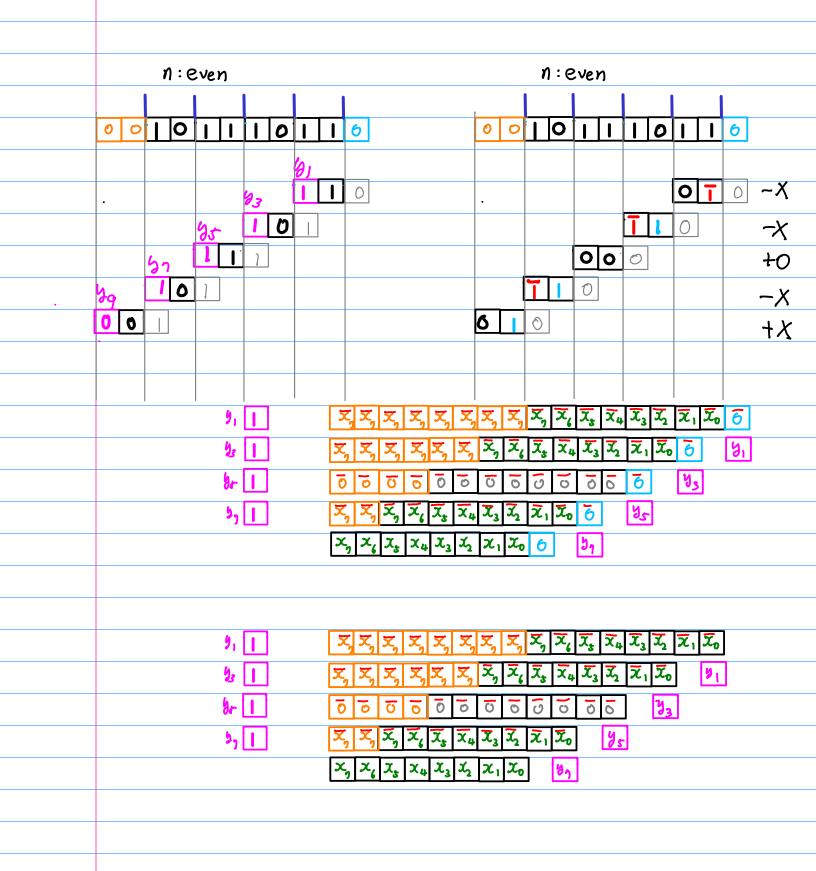
$$\frac{\text{Complement}}{X} \begin{pmatrix} 2X \\ X \end{pmatrix} + \frac{1}{X}$$



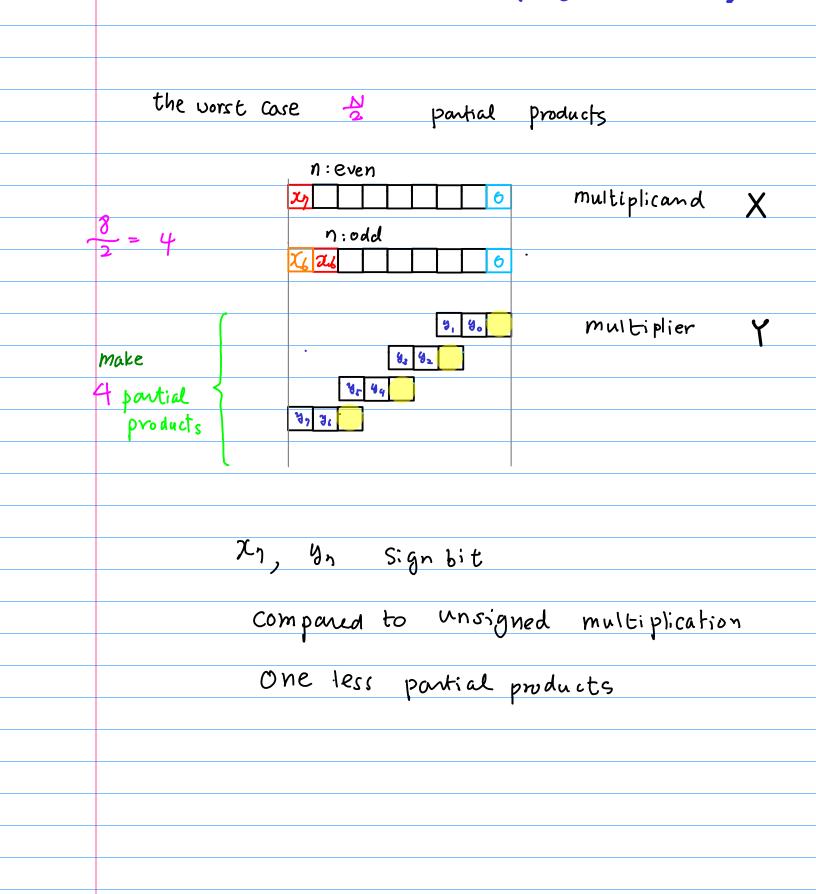




Handling Negative Scales



Modified Booth 2 (signed case)



4-Bit Unsigned number & Booth 2 Code

0	0000	00000
I		00017 21=1
2	00010	00170 4-2=2
3	00011	00107 4-1=3
4	00100	0 1 7 0 0 8-4 =4
5	00101	0 1 1 8-4+2-1 =5
b	00110	01070 8-2=6
ク	00111	0100 7 8-1=7
8	01000	17000 16-8 = 8
9	01001	11 0 1 7 16-8+2-1 = 9
10	01010	1 T 1 T 0 16-8+4-2 = 10
1)	01011	17107 16-8+4-1=11
12	01100	1070016-4=12
13	01101	16717 16-4+2-1=13
14	01110	100 10 16 -2 = 14
15	0 1 1 1 1	10007 167 = 15
		Booth 2 code:
		Signed Digit Number
	-	> need special treatment
		to make all positive number
		•

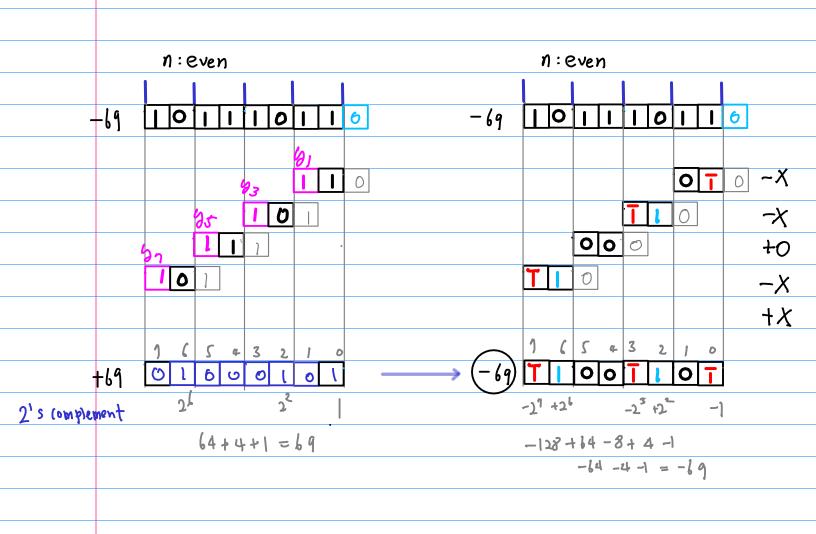
4-Bit 2's complement number & Booth 2 Code (Signed)

0	0000	0000	O
1	0001	0017	2 -1 = 1
2	0010	0 1 7 0	4-2=2
3	0011	0 1 0 7	4-1 = 3
4	0100	1 7 0 0	8-4 =4
5	0101		8-4+2-1 =5
b	0110	1 0 T 0	8-2=6
7	0111	1001	8-1=7
-8	1000	7000	-8
- γ	1001	7017	-8+2-1 = -7
-1	1010	TITO	8+4-2=-6
-5		TIOT	-8+4-1 = -5
~¥.	1100	0 1 0 0	-4
-3	1 1 0 1	6717	-4+2-1 =-3
-2	1110	0 0 T O	-2
-1		0007	-1

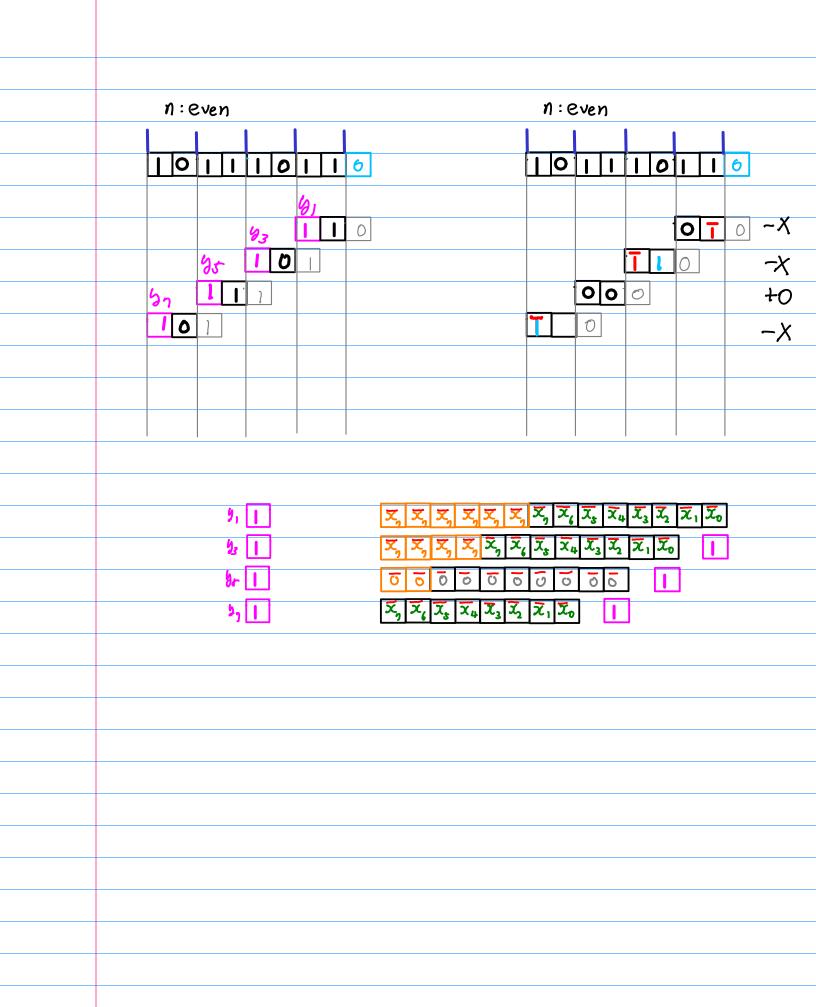
Booth 2 code: Signed Digit Number

To special treatment

For the Sign bit (9,7)



no problem in Booth encoding Signed numbers!



Modified Booth 3 (unsigned)

