

# Binary Angle Measurement (5A)

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- MSE Variance CORDIC
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# BAM Background

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S. Y. Park, "Fixed-Point Error Analysis of CORDIC Processor Based on the Variance Propagation Formula", IEEE Trans on Circuits & Systems, 2004

# Error Analysis

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Fixed-point Mean Square Error (MSE) analysis

Error source and models

Output error

Modified CORDIC algorithms

FFT processor – output error variance w.r.t. Wordlength

Error Bound

# Comparison

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## References

- [1] <http://en.wikipedia.org/>
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- [3] R. Andraka, A survey of CORDIC algorithms for FPGA based computers
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- [6] T.K. Rodrigues, "Adaptive CORDIC: Using Parallel Angle Recording to Accelerate Rotations", IEEE Trans on Computers, 2010