

Background - Accessing Memory (1A)

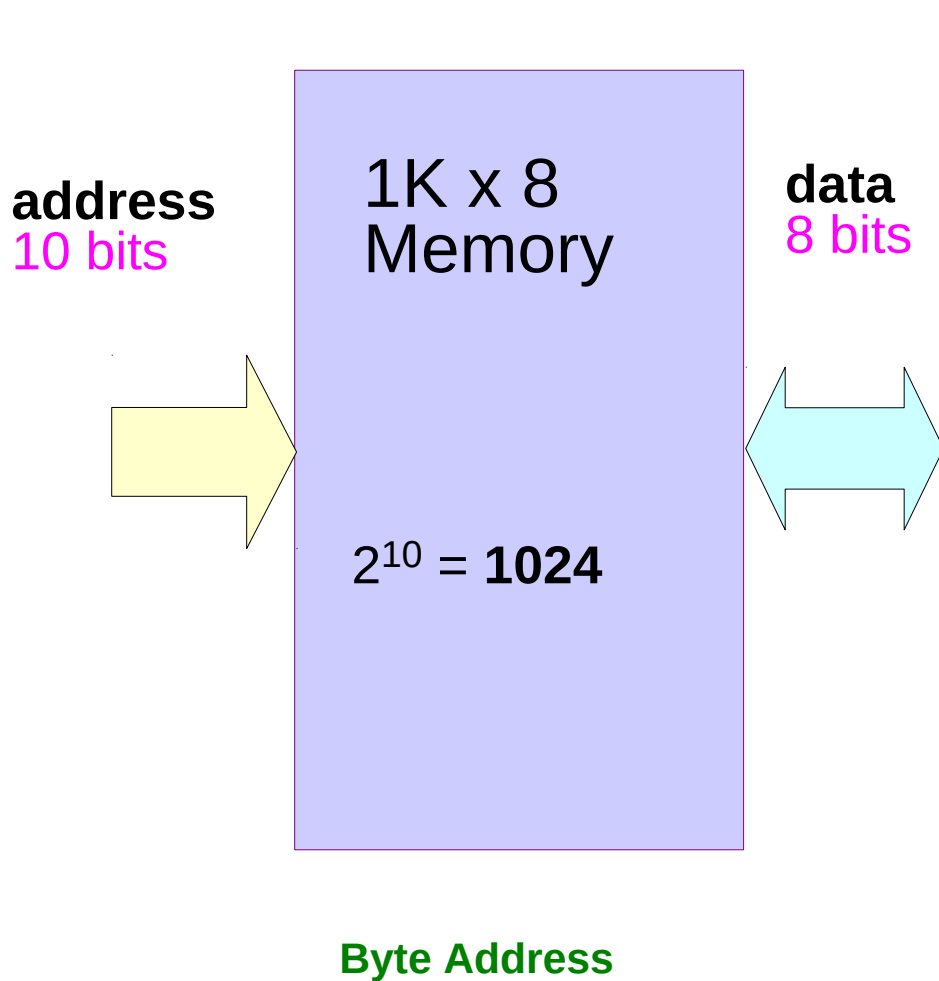
Copyright (c) 2010 - 2018 Young W. Lim.

Permission is granted to copy, distribute and/or modify this document under the terms of the GNU Free Documentation License, Version 1.2 or any later version published by the Free Software Foundation; with no Invariant Sections, no Front-Cover Texts, and no Back-Cover Texts. A copy of the license is included in the section entitled "GNU Free Documentation License".

Please send corrections (or suggestions) to youngwlim@hotmail.com.

This document was produced by using LibreOffice.

Byte Address and Data in a Memory



Variables and their addresses

	address	data
<code>int a;</code>	<code>&a</code>	<code>a</code>
<code>int * p;</code>	<code>&p</code>	<code>p</code>

Assignment of a value

int a;

int b;

address

data

&a

a = 111



&b

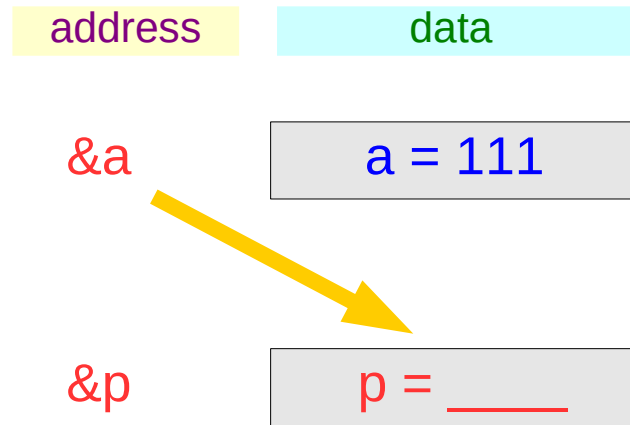
b = _____

b = a;

Assignment of an address

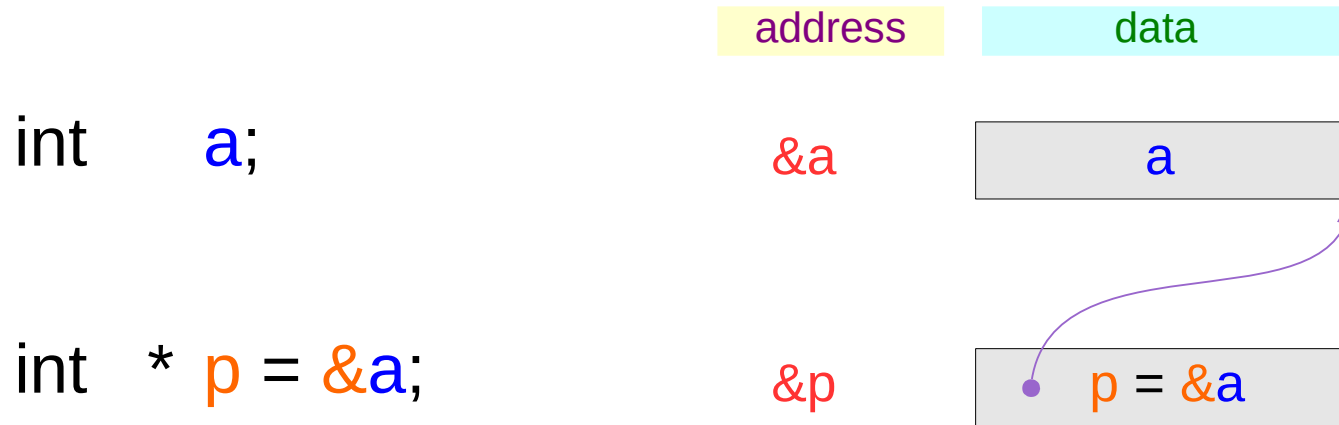
```
int a;
```

```
int * p;
```



```
p = &a;
```

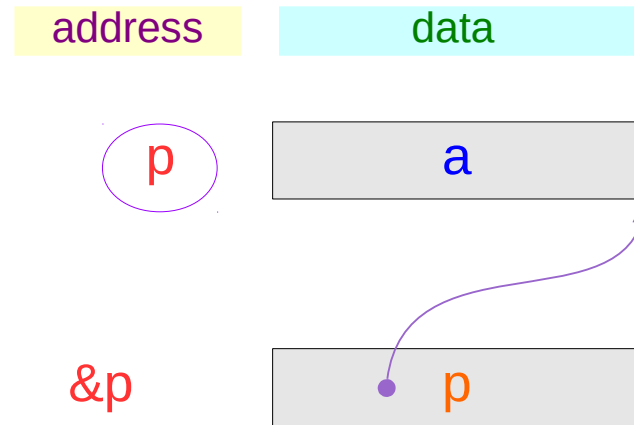
Variables with initializations



Pointed addresses : p

```
int a;
```

```
int * p = &a;
```



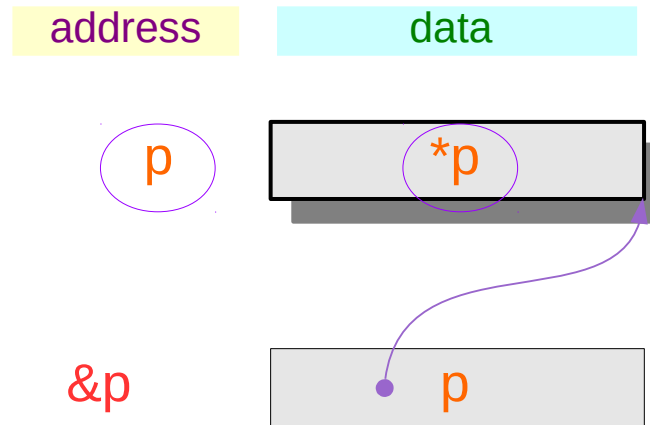
$p \equiv \&a$

Dereferenced Variable : *p

```
int a;
```

```
int *p = &a;
```

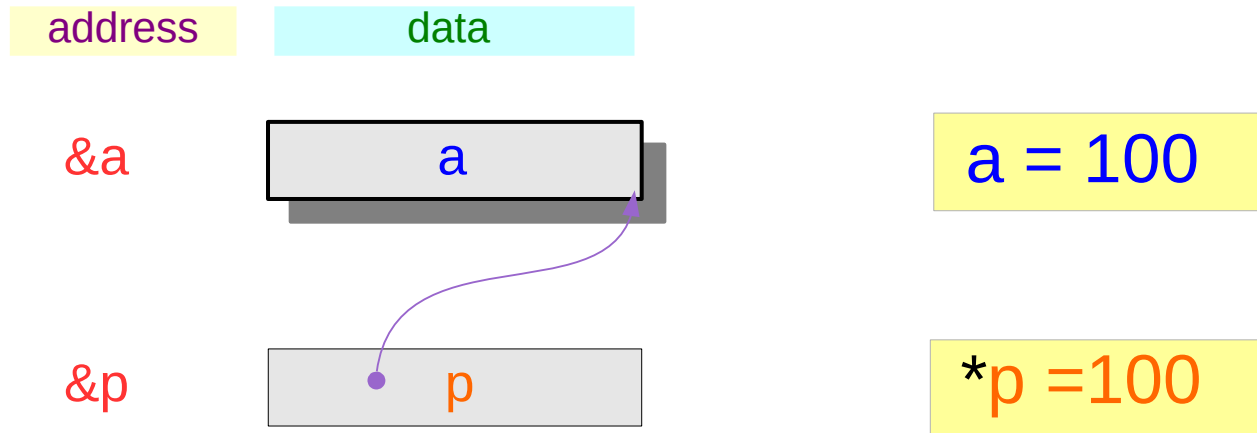
assignment



equivalence

$p \equiv \&a$
 $*p \equiv *\&a$
 $*p \equiv a$

Two way to access: `a` and `*p`



- 1) Read/Write `a`
- 2) Read/Write `*p`

Applications of pointers

1. Pass by Reference
2. Arrays

Pass by Reference

Variable Scopes

```
int func1 (int a, int b)
{
  int i, int j;
  ...
  ...
}
```

i and **j**'s
variable scope



cannot access
each other

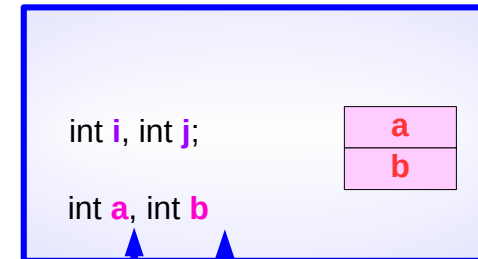
x and **y**'s
variable scope

```
int main ()
{
  int x, int y;
  ...
  ...
  func1 ( 10, 20 );
  ...
  ...
}
```

Only **top** stack frame is active
and its variable can be accessed

Communications are performed
only through the **parameter** variables

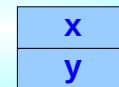
func1's
Stack
Frame



(10, 20)

main's
Stack
Frame

int x, int y;



Pass by Reference

```
int func1 (int* a, int* b)
{
  int i, int j;
  ...
  ...
}
```

x and **y** are made known to **func1**
func1 can read / write **x** and **y**
through their addresses

a=&x
b=&y

x and **y**'s
variable scope

```
int main ()
{
  int x, int y;
  ...
  ...
  func1 ( &x, &y );
  ...
  ...
}
```

***a**
***b**

func1's
Stack
Frame

int i, int j;

int* a, int* b

(&x, &y)

main's
Stack
Frame

int x, int y;

***a**
***b**

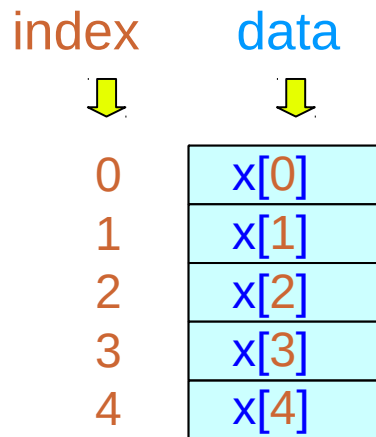
Arrays

Accessing array elements – using an address

```
int x[5];
```

x holds the *starting address* of 5 consecutive *int* variables

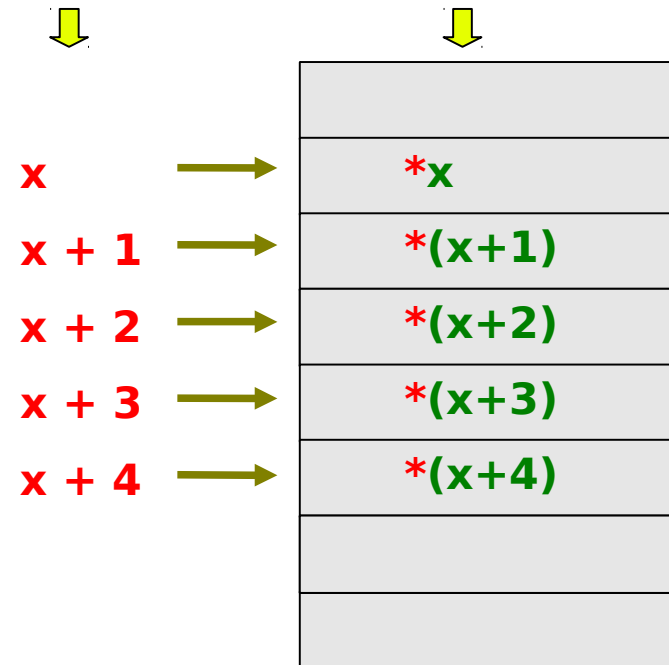
5 int variables



cannot change
address x
(constant)

address

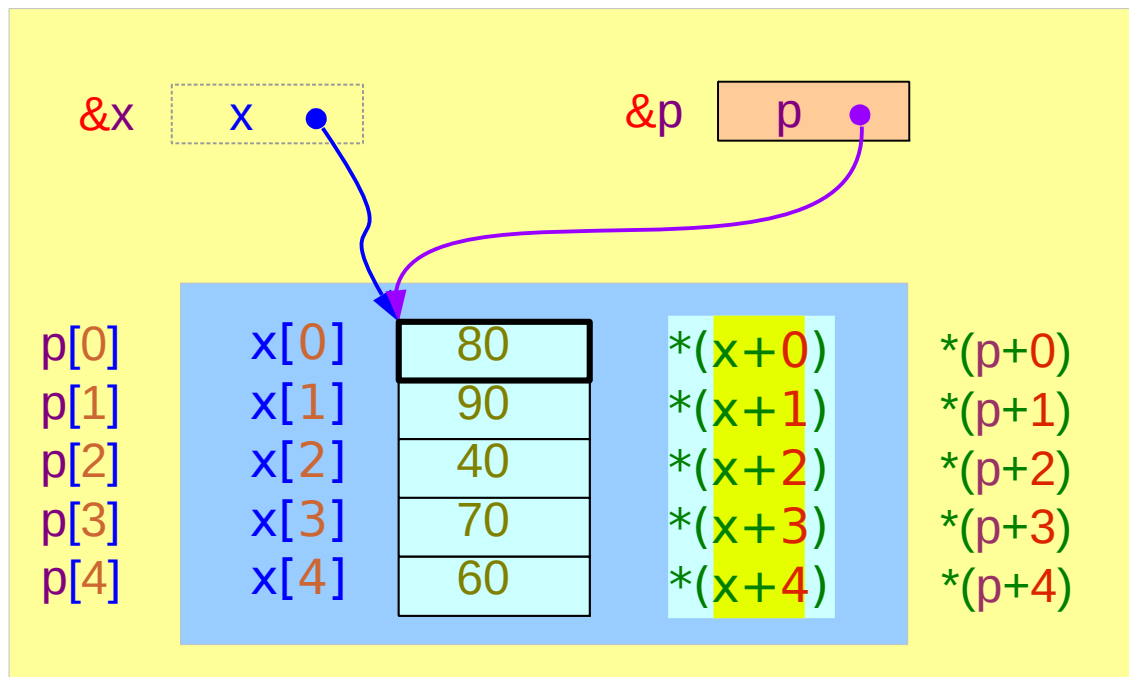
data



Accessing an Array with a Pointer Variable

```
int x [5] = { 1, 2, 3, 4, 5 };
```

```
int *p = x;
```



`x` is a constant symbol
cannot be changed

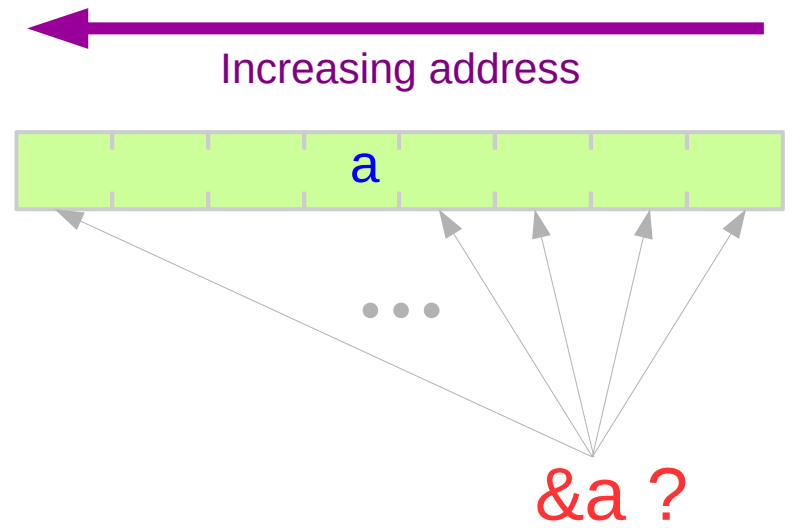
`p` is a variable
can point to other addresses

Byte Address
Little Endian
Big Endian

Byte Addresses

long a;

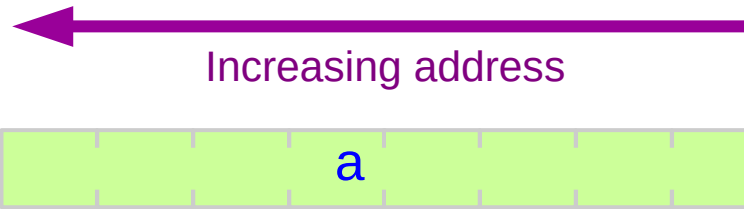
8-byte size data type



which byte?

Little / Big Endian Ordering of Bytes

long a;



a_7 a_6 a_5 a_4 a_3 a_2 a_1 a_0

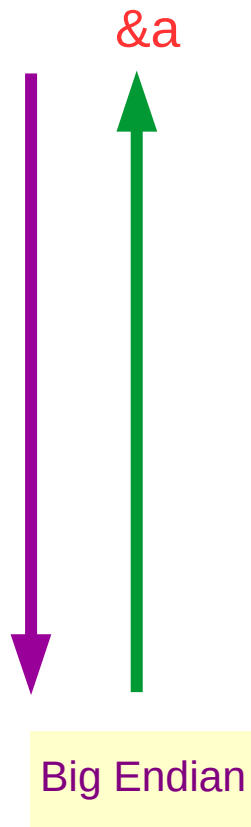


a_0 a_1 a_2 a_3 a_4 a_5 a_6 a_7

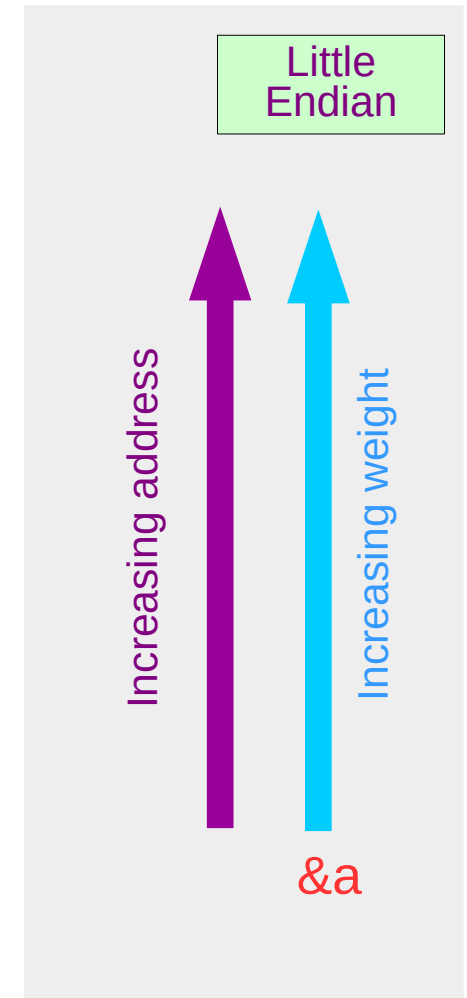
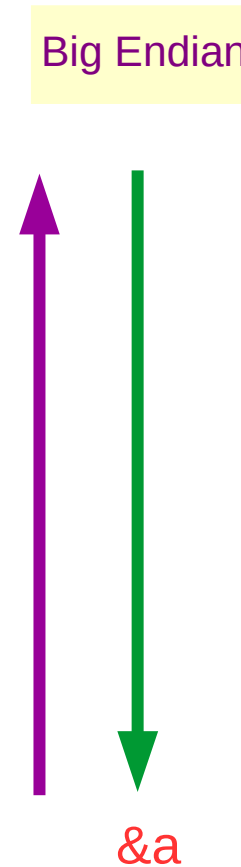


Increasing address, Increasing weight

downward, increasing address



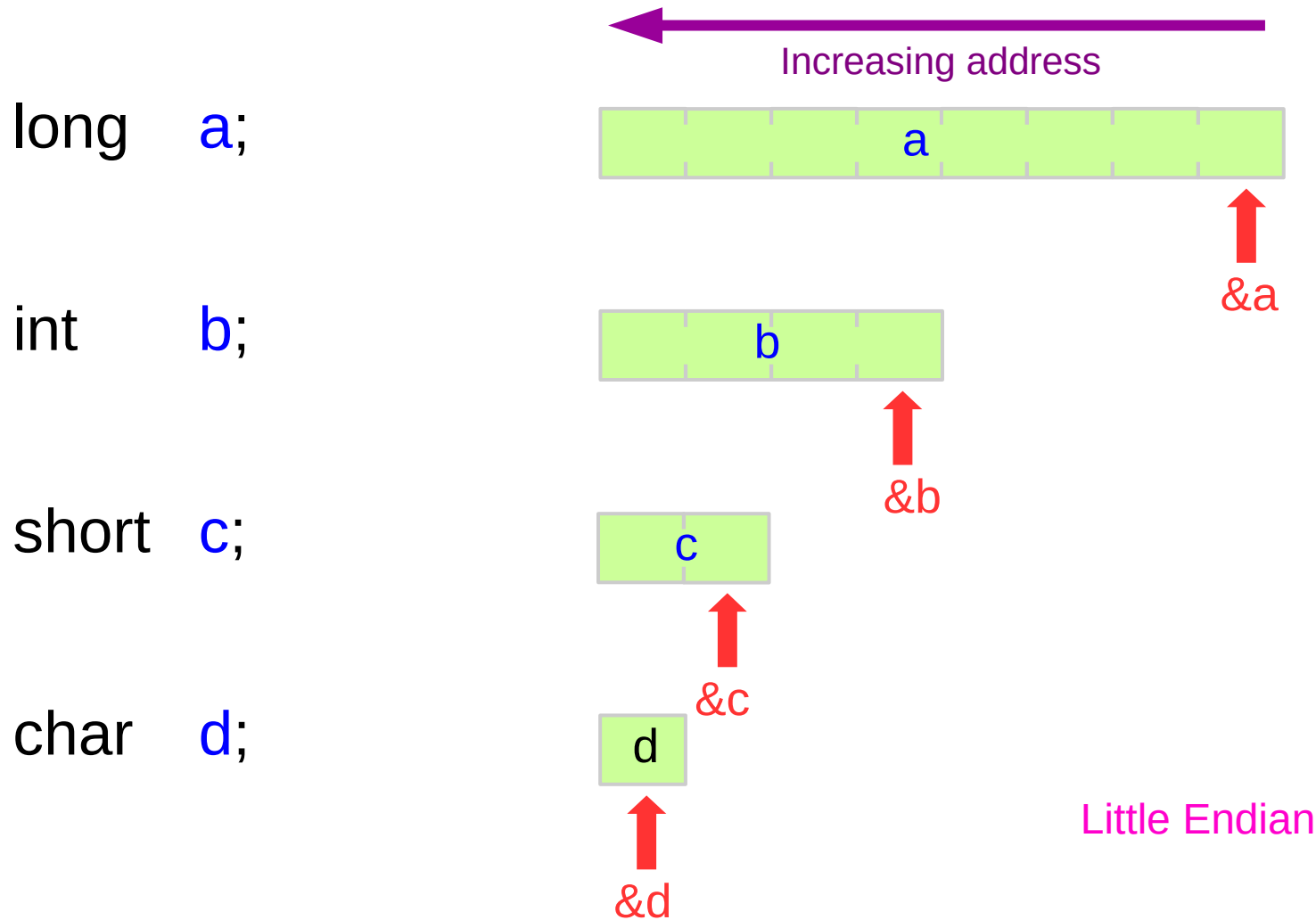
upward, increasing address



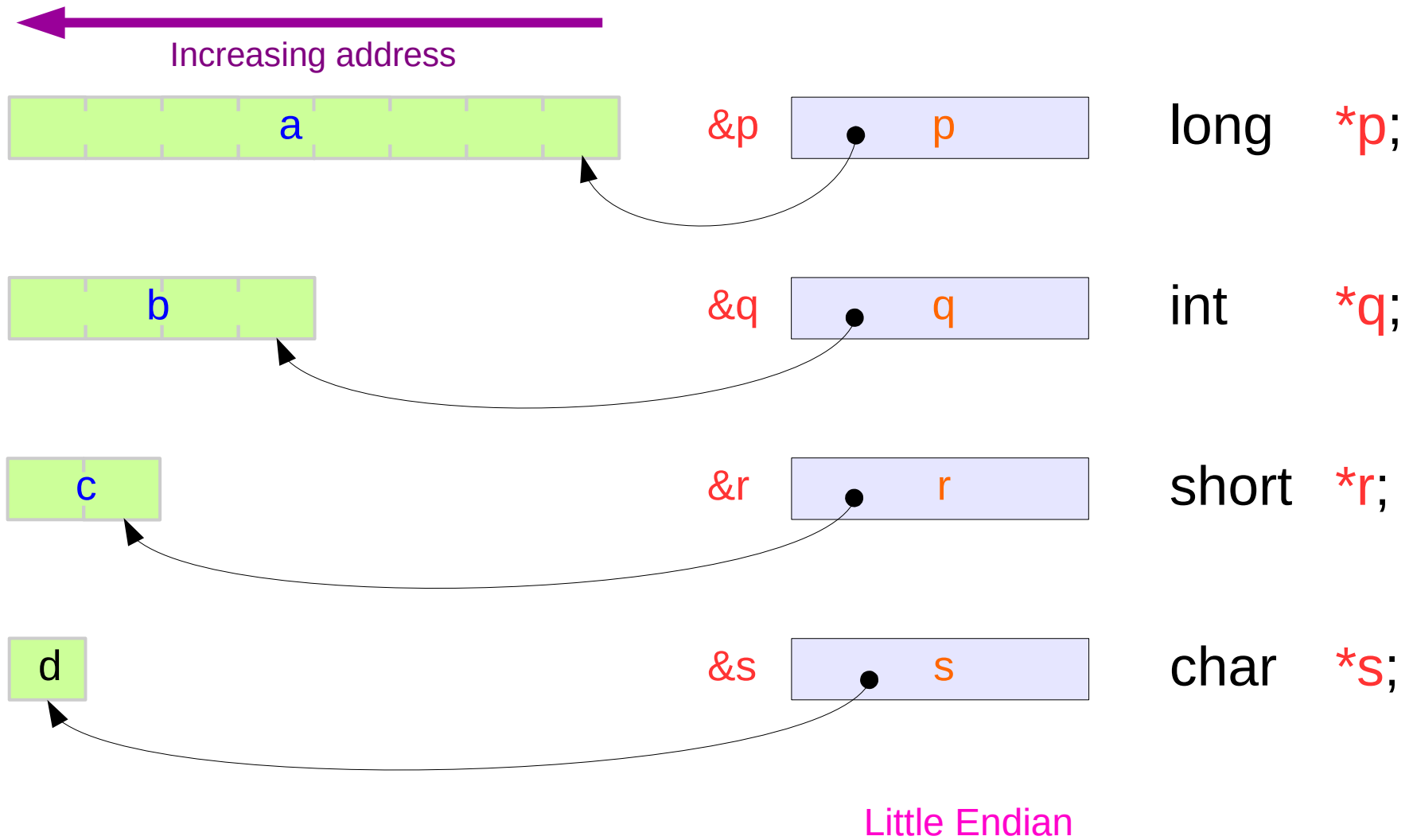
<https://stackoverflow.com/questions/15620673/which-bit-is-the-address-of-an-integer>

Pointer Types

Integer Type Variables and Their Addresses



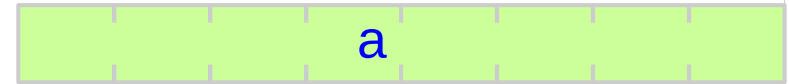
Points to the LSByte



Aligning variables of different sizes

long a;

8-byte slots



&a ↑

int b;

4-byte slots



&b ↑

short c;

2-byte slots



&c ↑

char d;

1-bytes slots



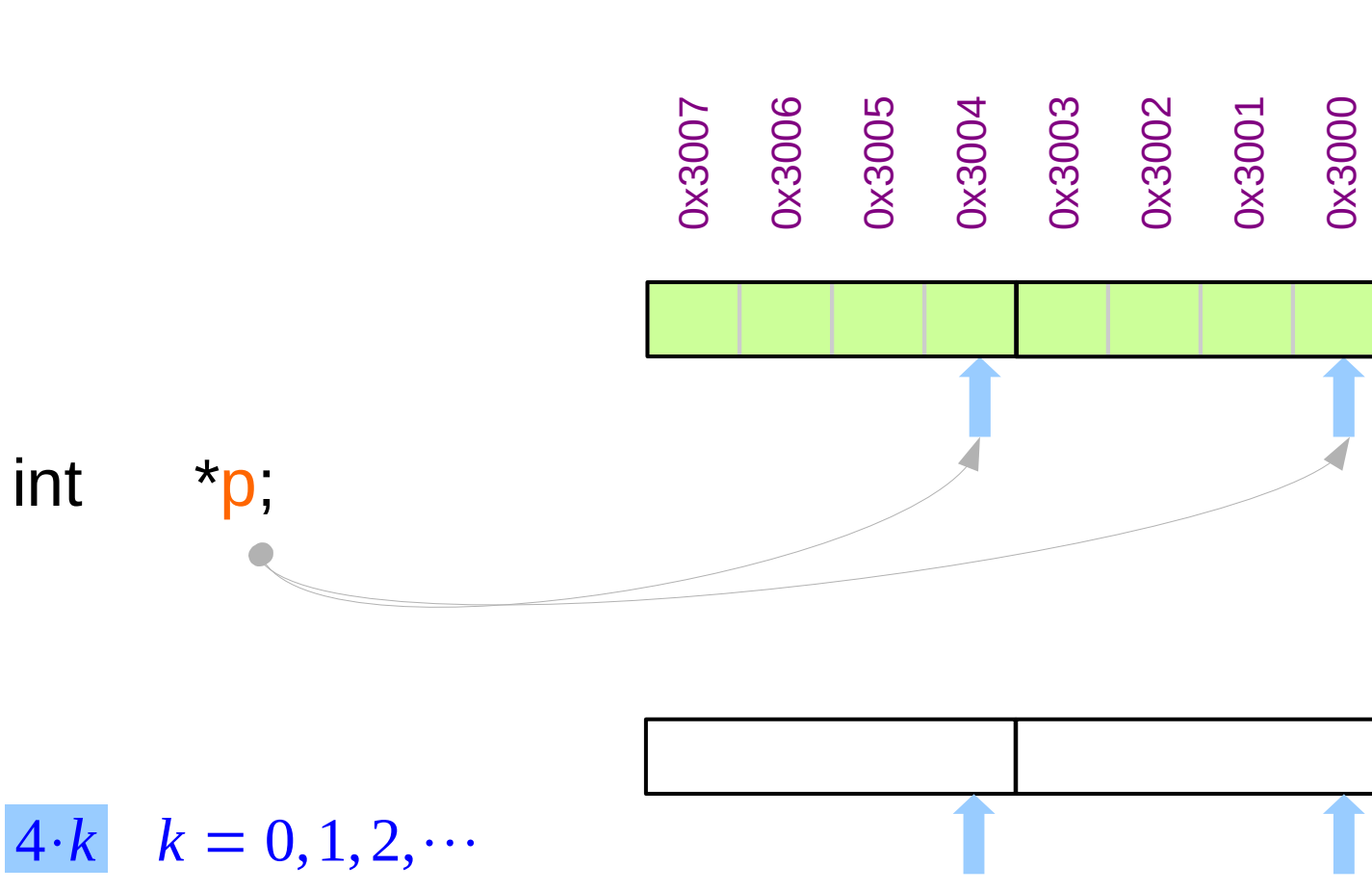
&d ↑

all these slots
are aligned

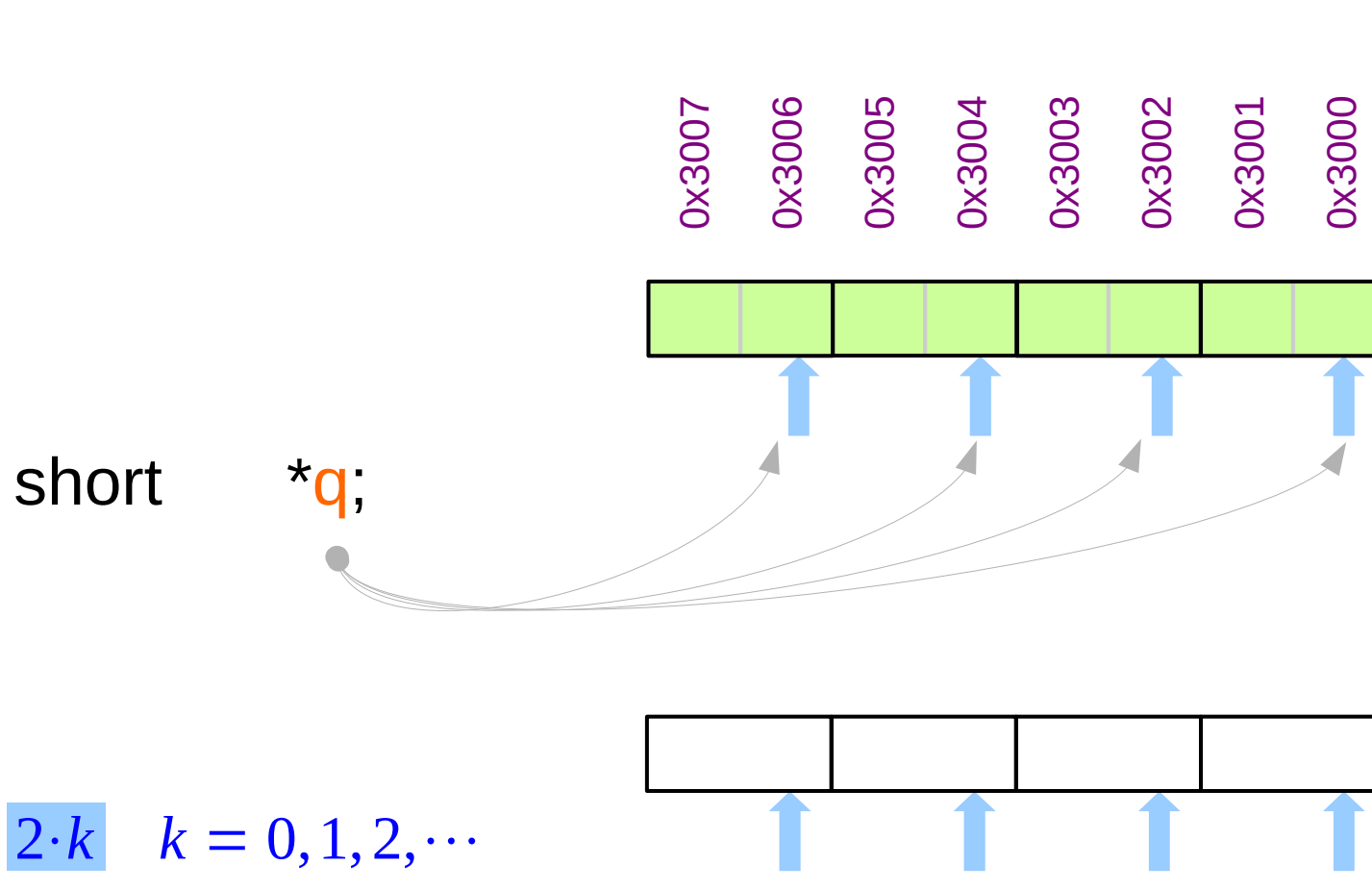
Memory Alignment
in the Little Endian



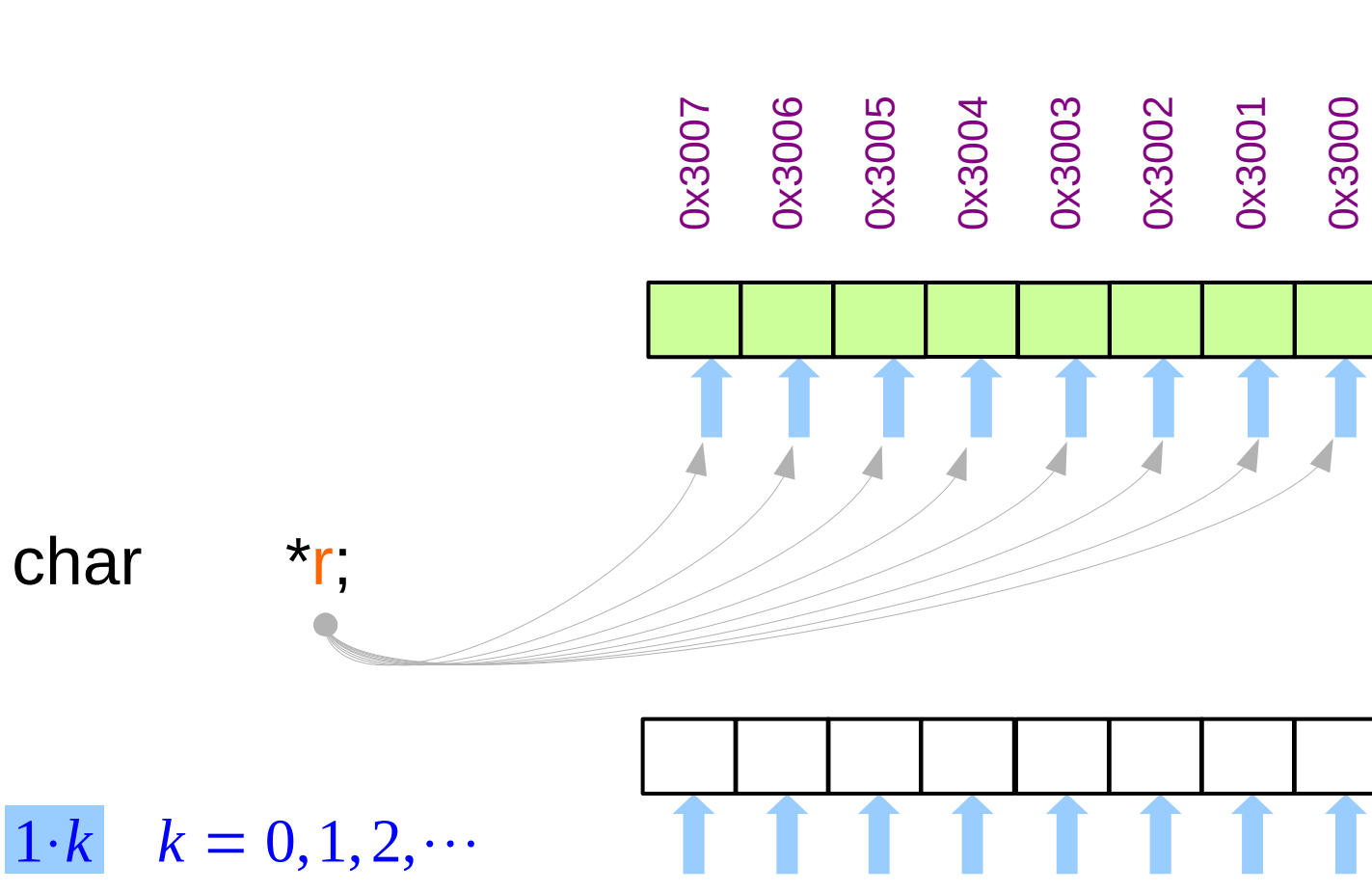
Possible addresses for `int` values



Possible addresses for **short** values

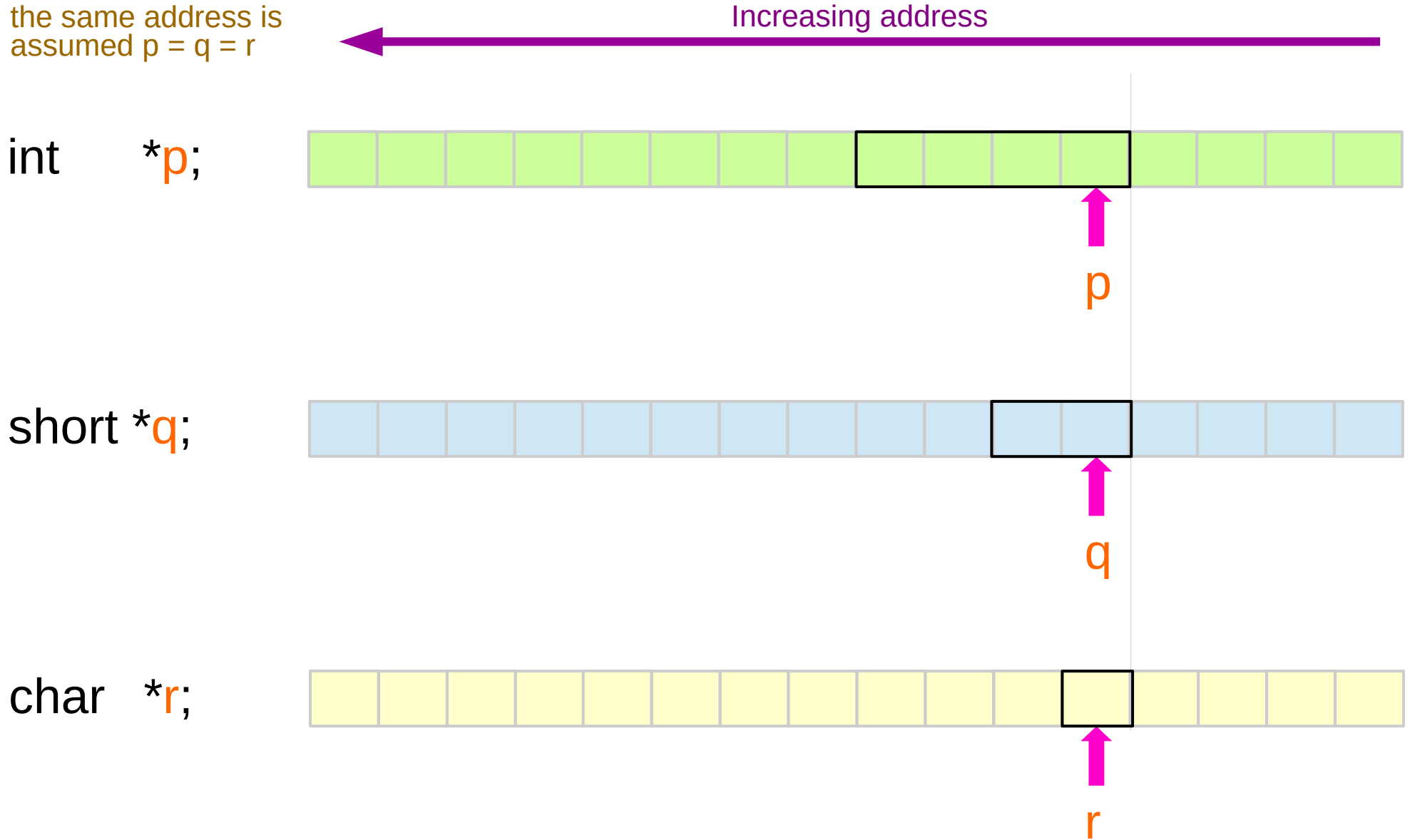


Possible addresses for **char** values



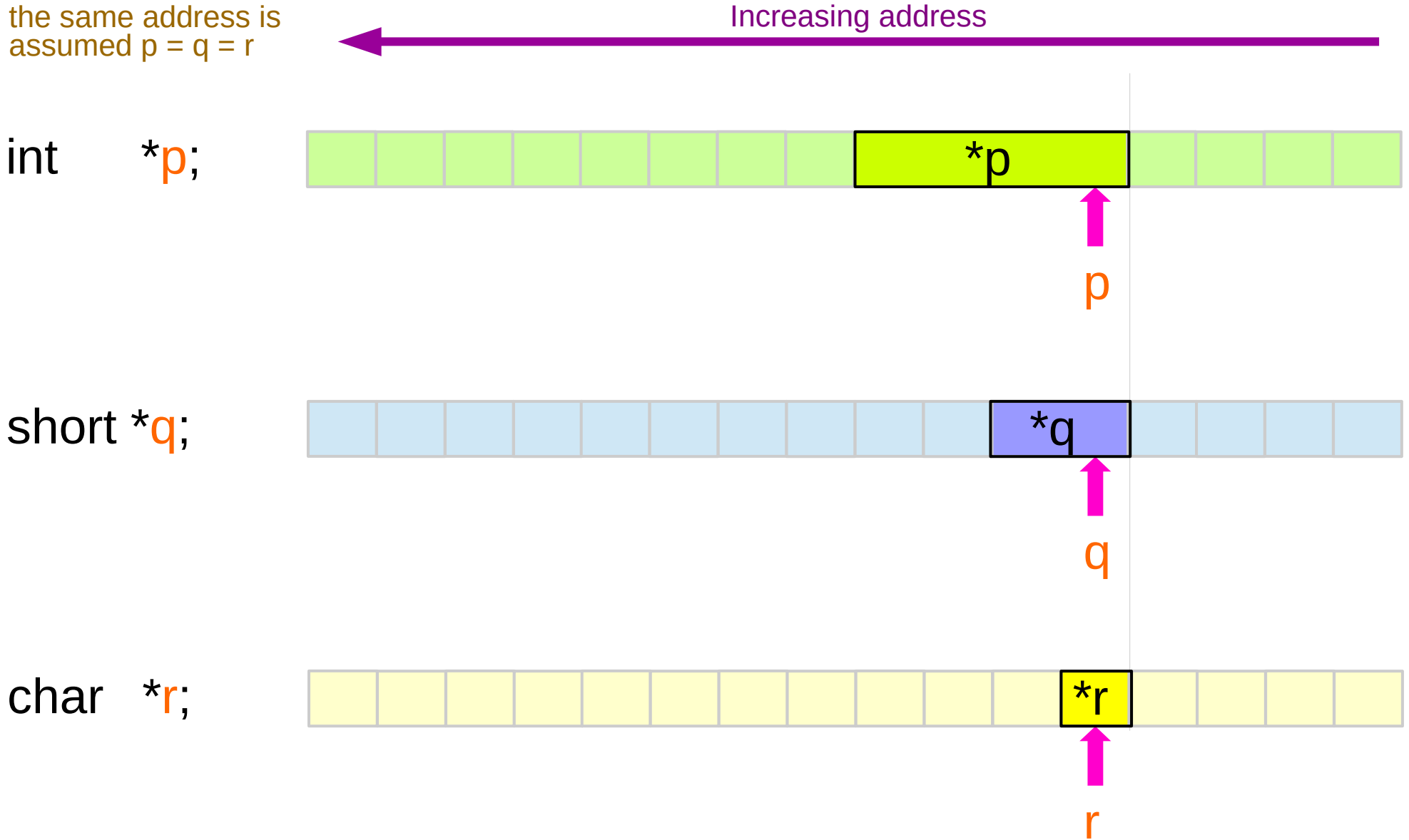
Data size at an address

the same address is assumed $p = q = r$



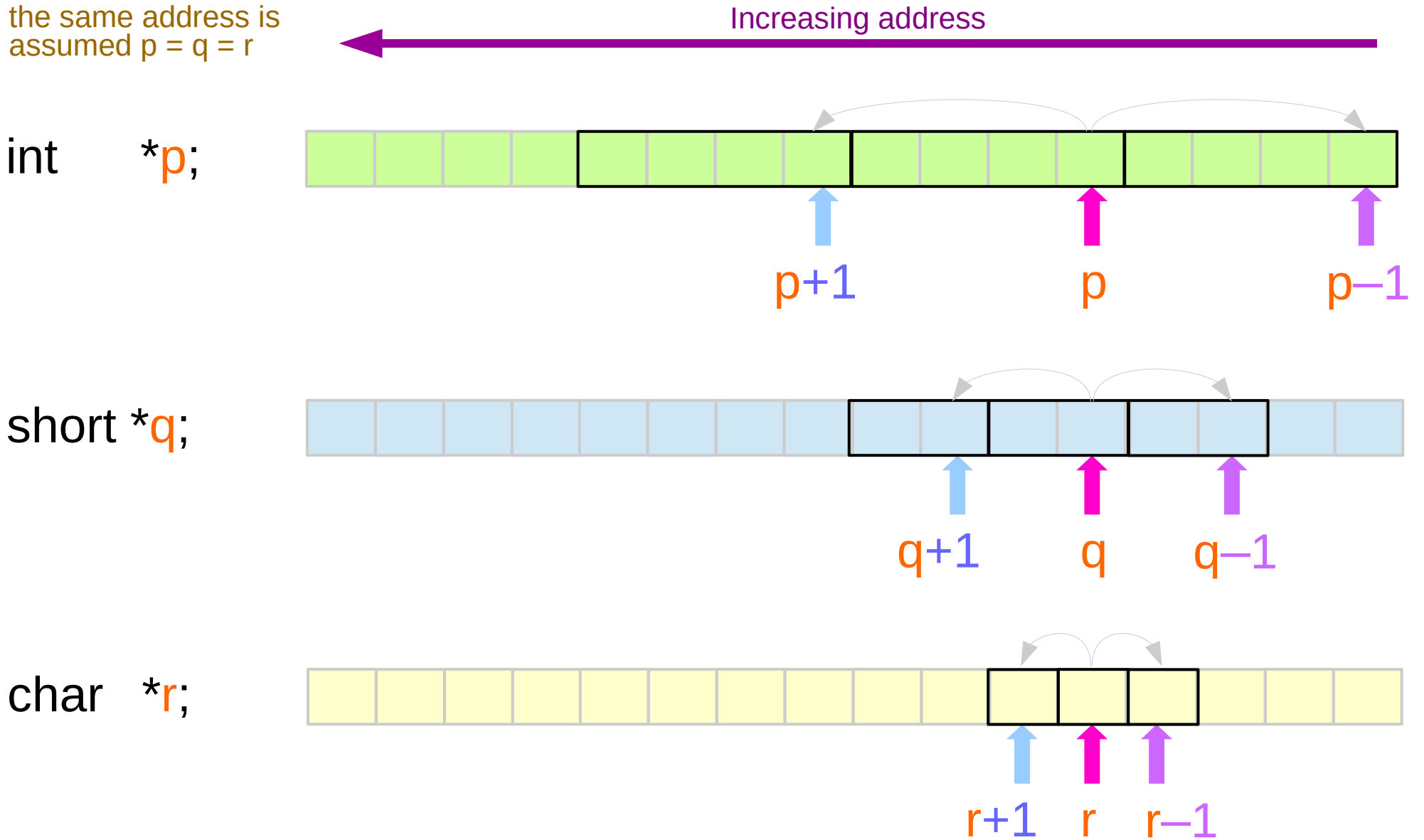
Associated data at the pointed addresses

the same address is assumed $p = q = r$



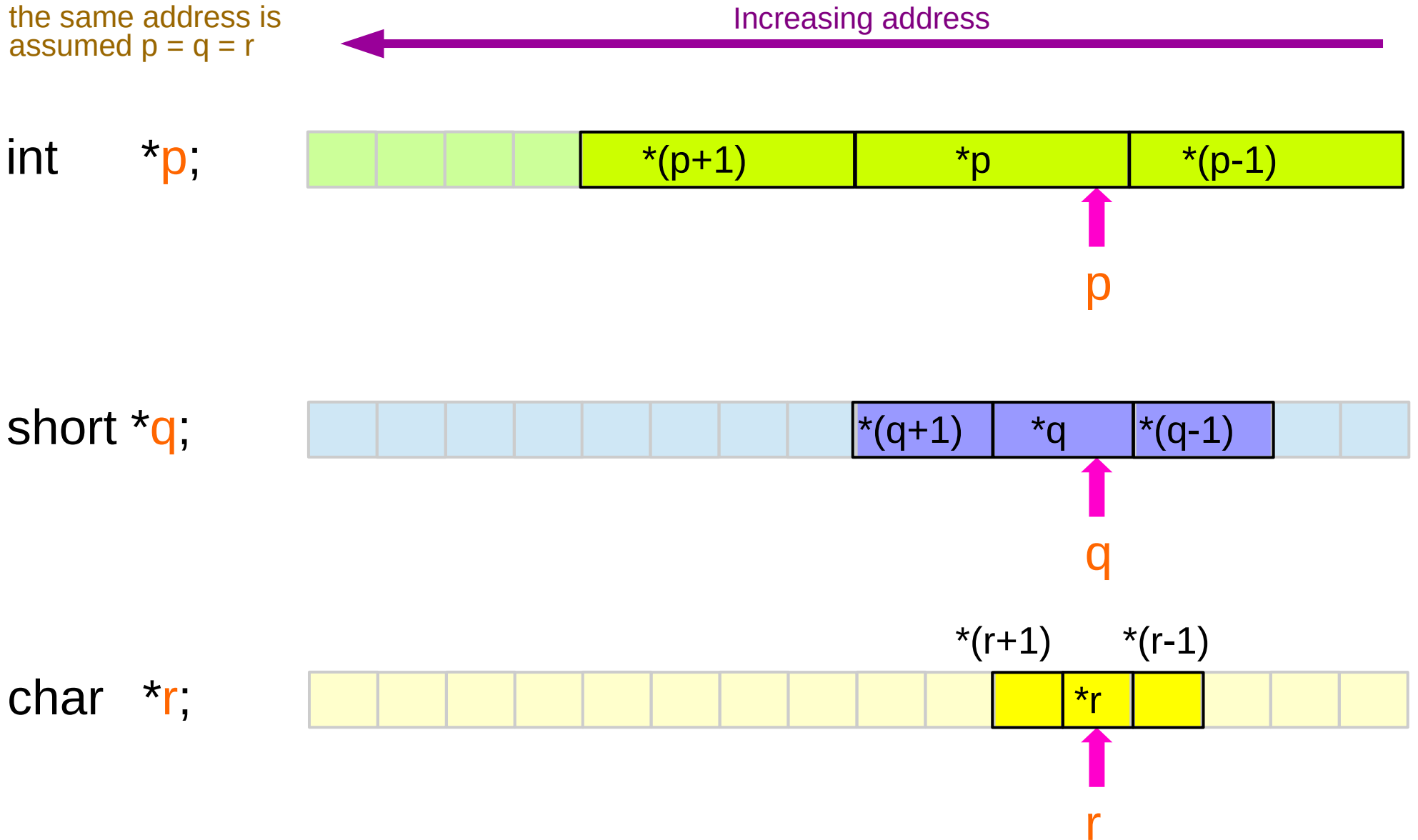
Incrementing / decrementing pointers

the same address is assumed $p = q = r$



Dereferencing the pointers

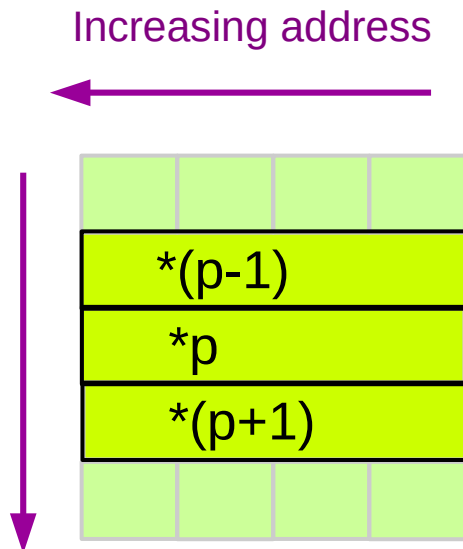
the same address is assumed $p = q = r$



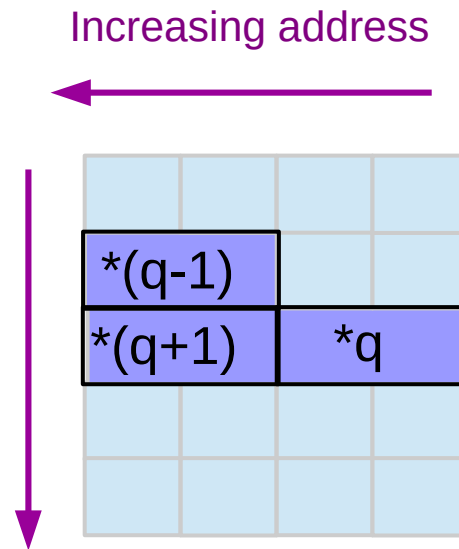
32-bit word view

the same address is assumed $p = q = r$

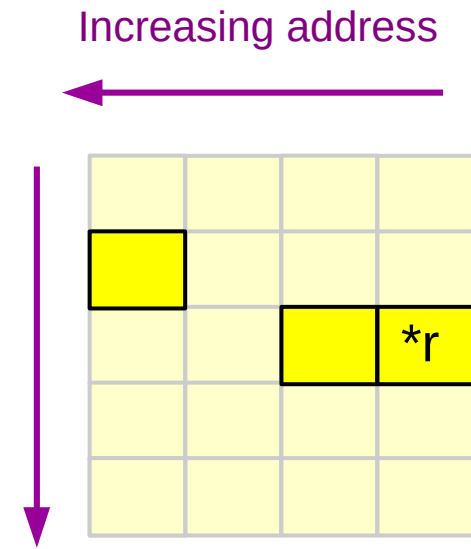
1 word = 4 bytes = 4 (8 bits) = 32 bits



int $*p$;



short $*q$;

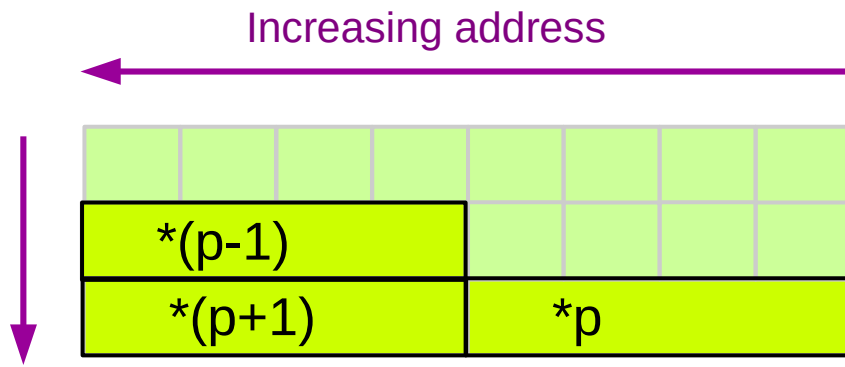


char $*r$;

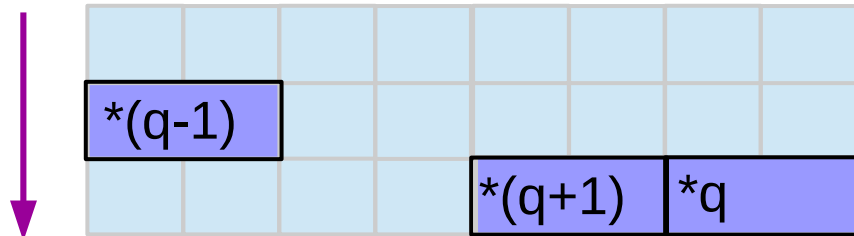
64-bit word view

1 word = 8 bytes = 8 (8 bits) = 64 bits

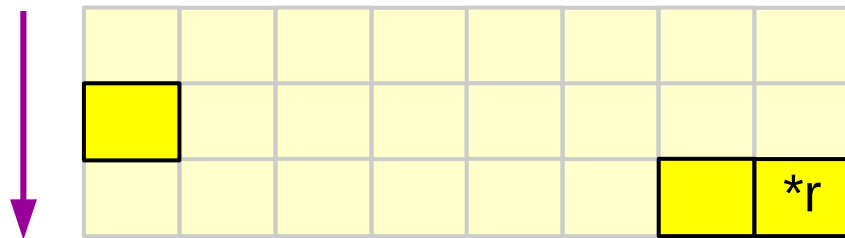
the same address is assumed $p = q = r$



```
int *p;
```

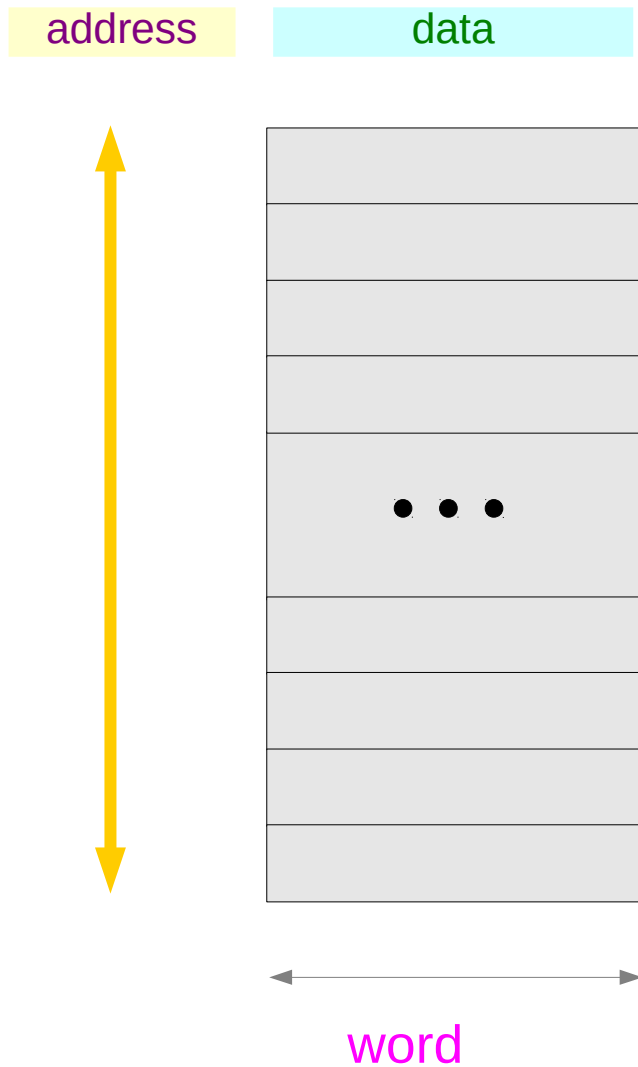


```
short *q;
```



```
char *r;
```

32-bit vs. 64-bit machines



data unit per memory access

32-bit

64-bit

word size

32-bit

64-bit

max address space

2^{32}

2^{64}

References

- [1] Essential C, Nick Parlante
- [2] Efficient C Programming, Mark A. Weiss
- [3] C A Reference Manual, Samuel P. Harbison & Guy L. Steele Jr.
- [4] C Language Express, I. K. Chun