

# Hybrid CORDIC 2.A Sine/Cosine Generator

20170705

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# Wilson ROM based Sine/Cosine Generation

[24] Fu & Willson Sine / Cosine Generation

ROM-based

for high resolution, ROM size grows exponentially

quarter-wave symmetry

$$\sin \theta = \cos\left(\frac{\pi}{2} - \theta\right)$$

$$\phi [0, 2\pi] \rightarrow [0, \frac{\pi}{4}]$$

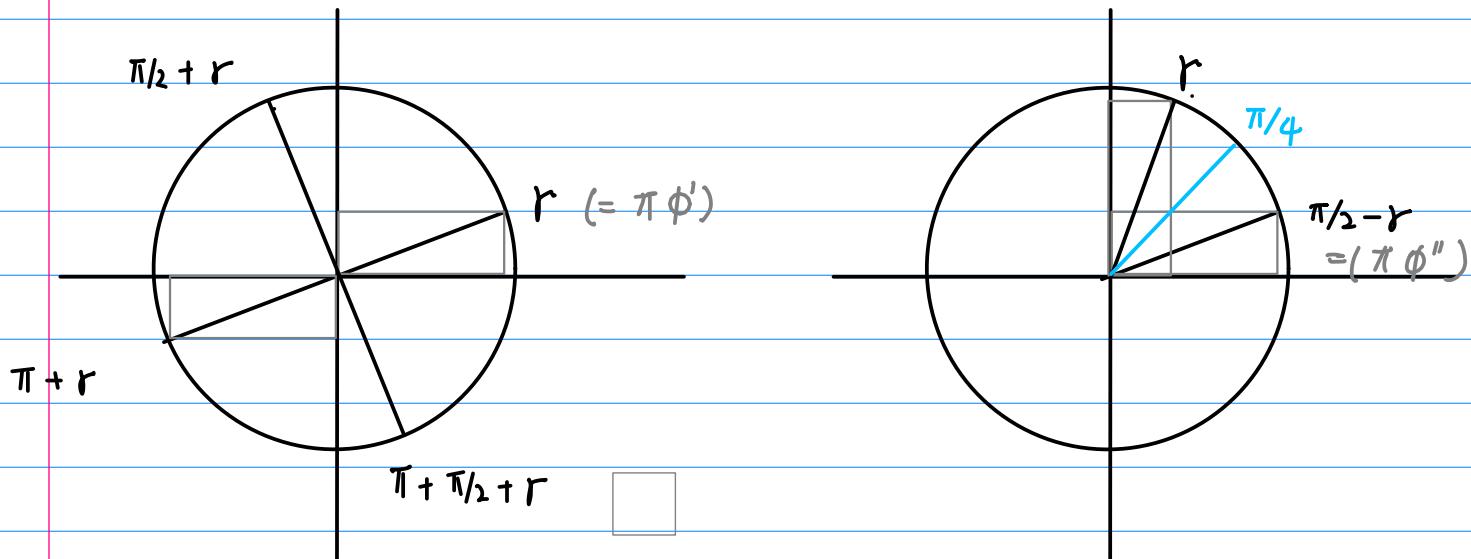
conditionally interchanging inputs  $x_0$  &  $y_0$

conditionally interchanging and negating outputs  $X$  &  $Y$

$$X = x_0 \cos \phi - y_0 \sin \phi$$

$$Y = y_0 \cos \phi + x_0 \sin \phi$$

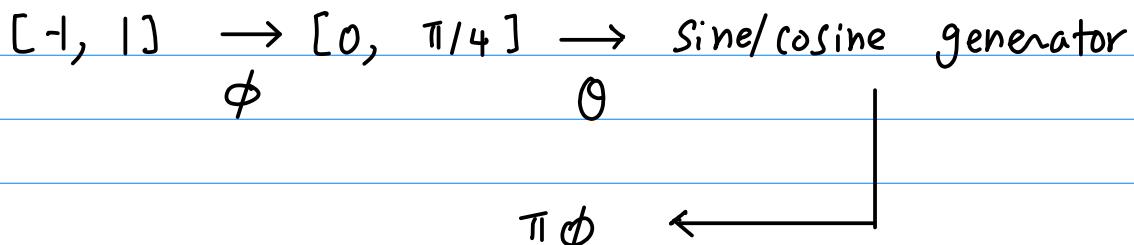
Madisetti VLSI arch



for frequency synthesis

Argument: Signed normalized by  $\pi$  angle [-1, 1]

binary representation of a radian angle required



① a phase accumulator  $\phi$  [-1, 1]

② a radian converter  $\phi \rightarrow \theta$

③ a sine/cosine generator

④ an output stage

$\sin \theta, \cos \theta$

$\downarrow$   
 $\sin \theta, \cos \theta$

$\sin \pi\phi \quad \cos \pi\phi$

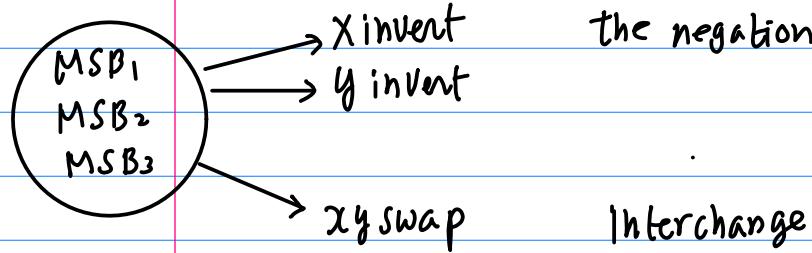


Output stage

$$\begin{aligned}\sin \theta &\rightarrow \sin \pi \phi \\ \cos \theta &\rightarrow \cos \pi \phi\end{aligned}$$

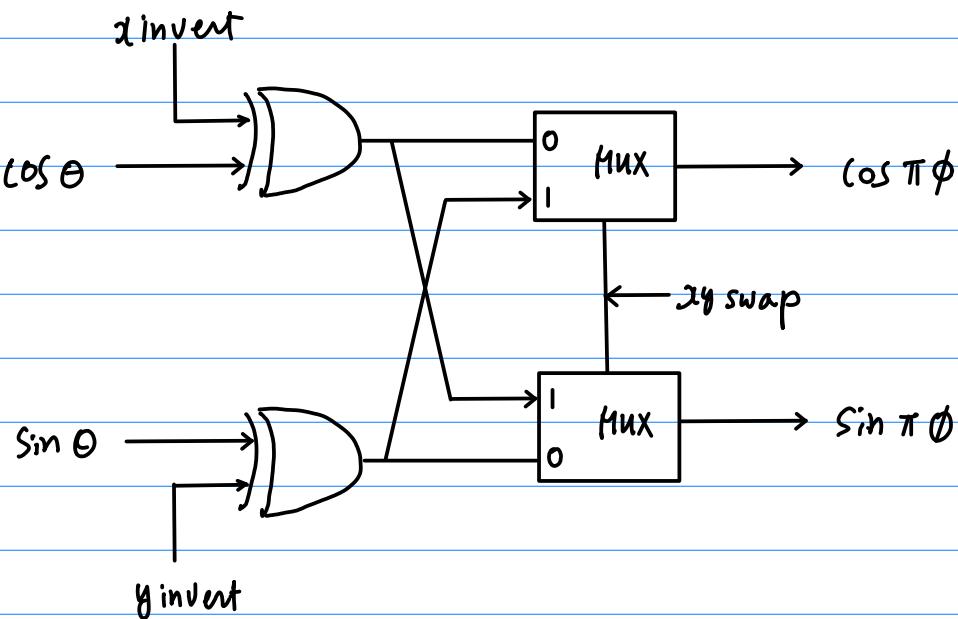
$$[-\pi, +\pi]$$

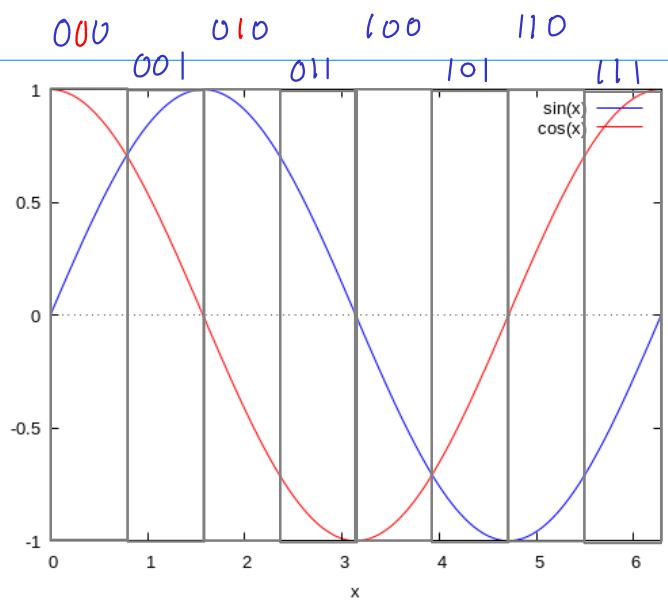
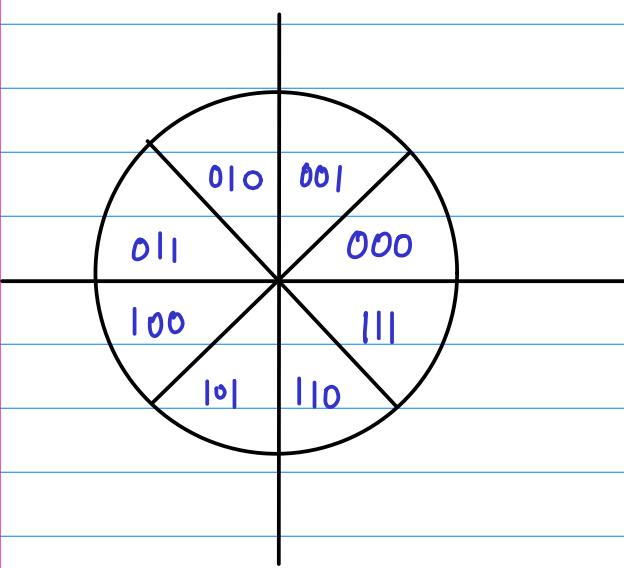
Negation / interchange



the negation of  $\cos \theta = X_{N+1}$   
 $\sin \theta = Y_{N+1}$

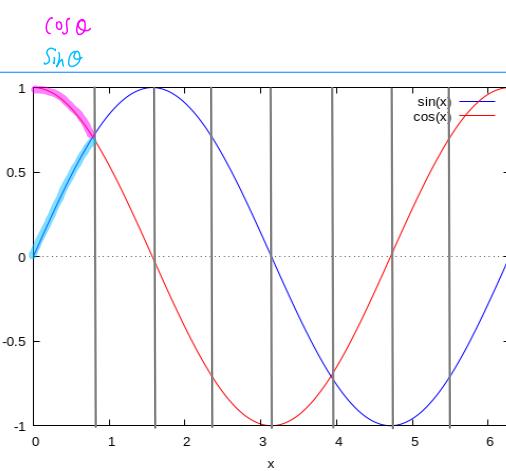
Negate before swap



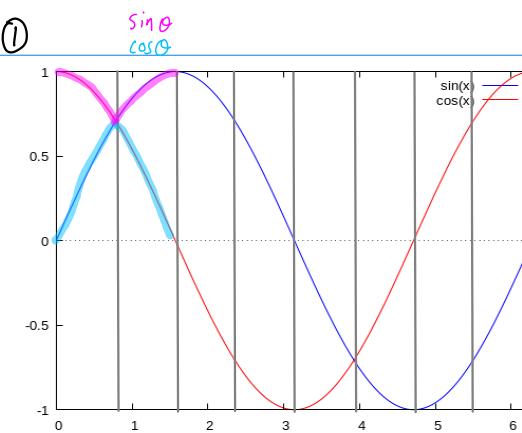


|       | $\cos$    | $\sin$ .  |      |                |                 |
|-------|-----------|-----------|------|----------------|-----------------|
|       | $X_{inv}$ | $Y_{inv}$ | swap | $\cos \theta$  | $\sin \pi \phi$ |
| 0 0 0 | 0         | 0         | 0    | $\cos \theta$  | $\sin \theta$   |
| 0 0 1 | 0         | 0         | 1    | $\sin \theta$  | $\cos \theta$   |
| 0 1 0 | 0         | 1         | 1    | $-\sin \theta$ | $\cos \theta$   |
| 0 1 1 | 1         | 0         | 0    | $-\cos \theta$ | $\sin \theta$   |
| 1 0 0 | 1         | 1         | 0    | $-\cos \theta$ | $-\sin \theta$  |
| 1 0 1 | (         | 1         | 1    | $-\sin \theta$ | $-\cos \theta$  |
| 1 1 0 | 1         | 0         | 1    | $\sin \theta$  | $-\cos \theta$  |
| 1 1 1 | 0         | 1         | 0    | $\cos \theta$  | $-\sin \theta$  |

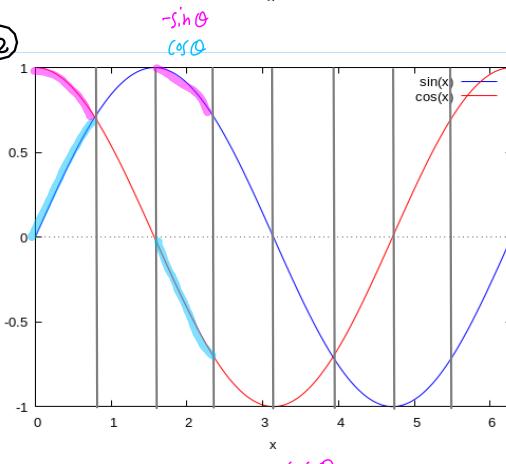
(6)



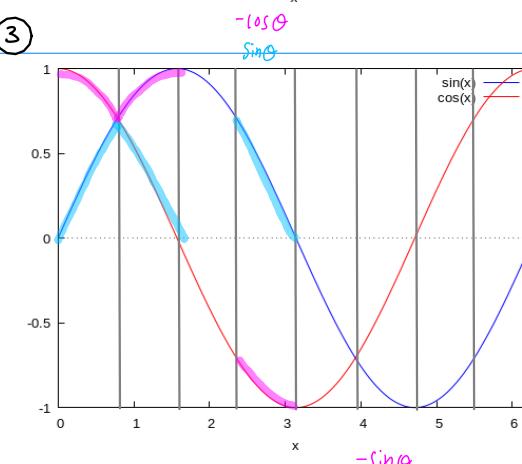
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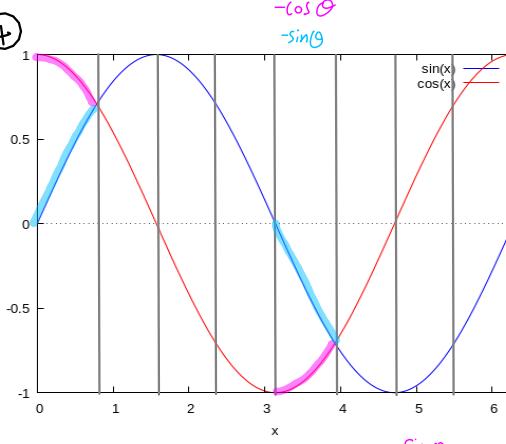
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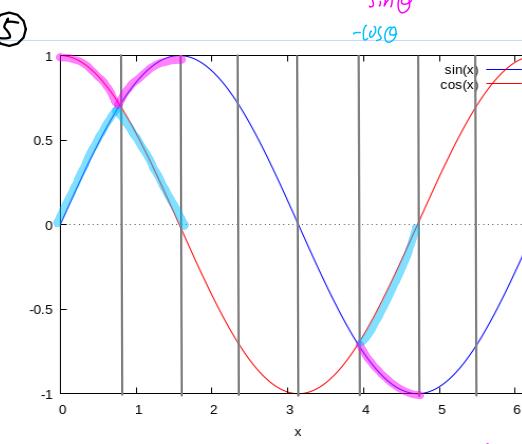
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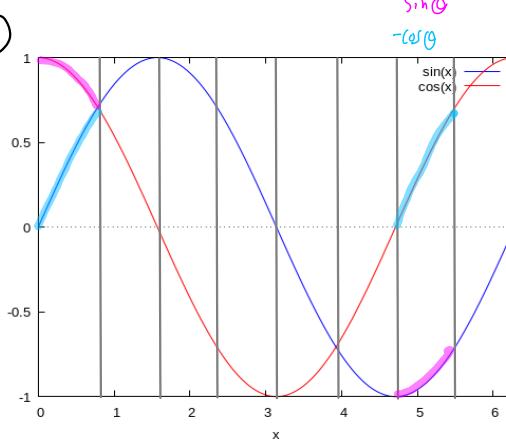
(4)



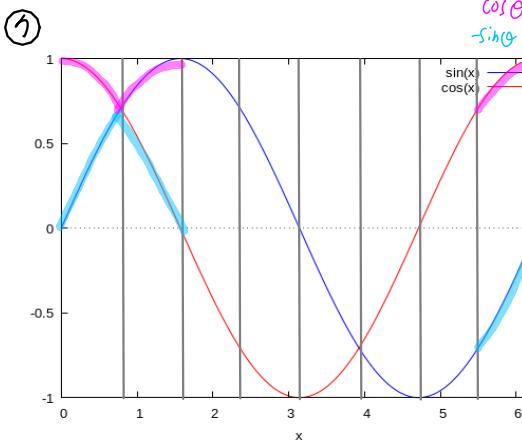
(5)



(6)

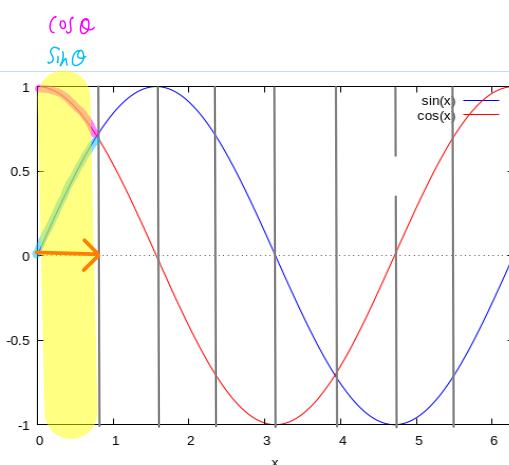


(7)

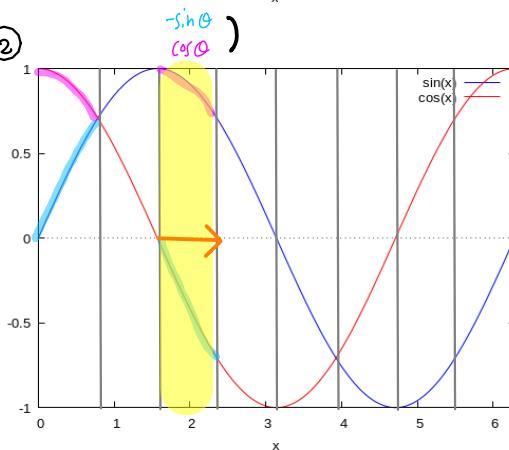


$\left\{ \begin{array}{l} \cos \phi \\ \sin \phi \end{array} \right.$ 

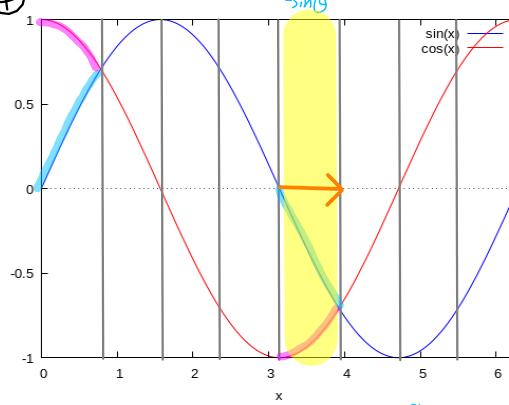
⑥



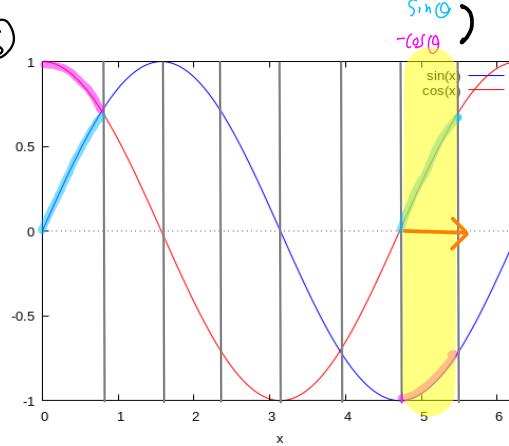
②



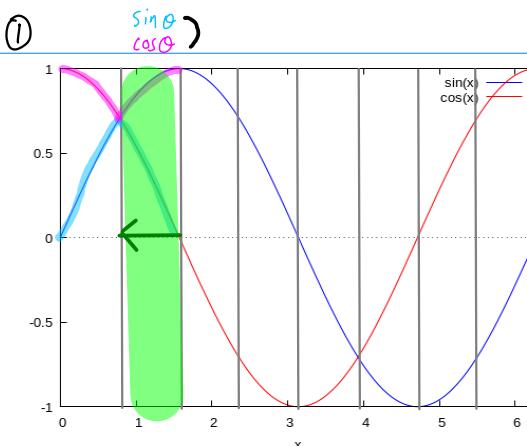
④



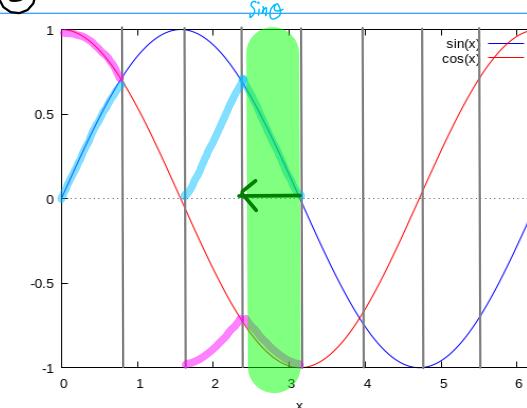
⑥



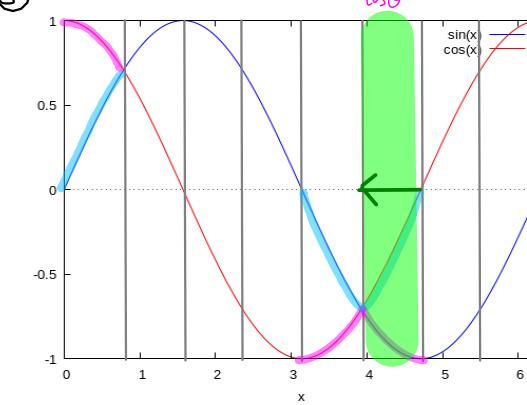
①



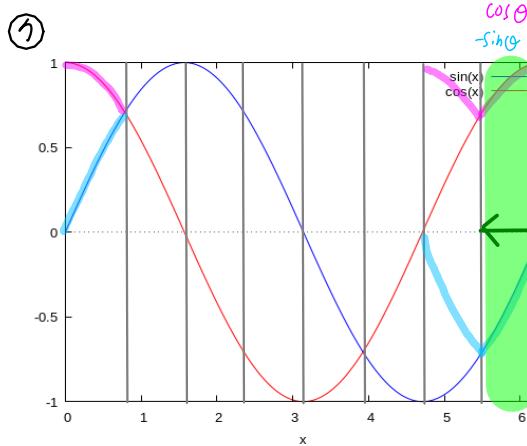
③

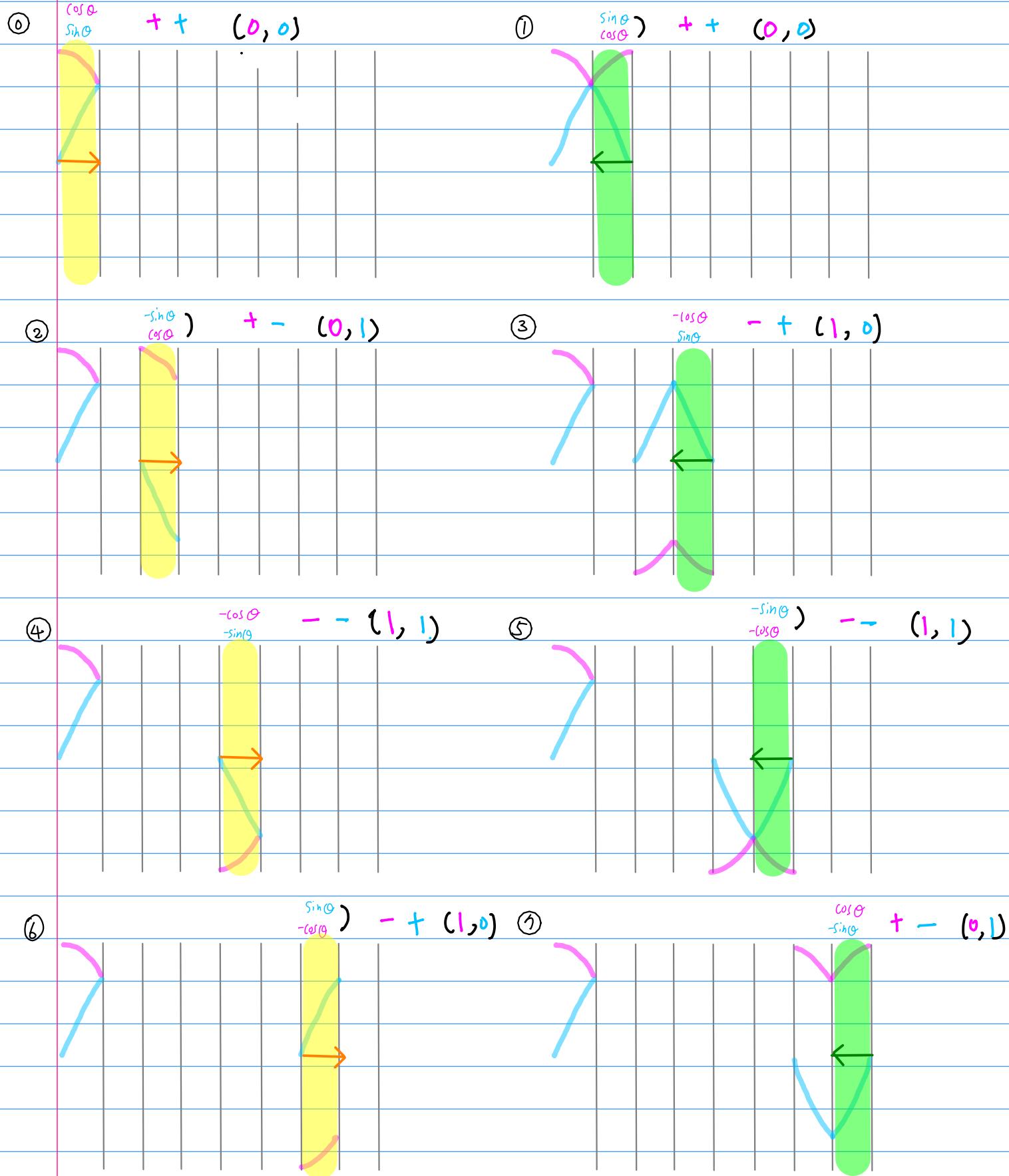


⑤



⑦



$\sin \phi$ 

| $X_{in}$ | $Y_{in}$ | swap | $\cos \pi \phi$ | $\sin \pi \phi$ |
|----------|----------|------|-----------------|-----------------|
| 0 0 0    | 0 0 0    | 0    | $\cos \theta$   | $\sin \theta$   |
| 0 0 1    | 0 0 0    | 1    | $\sin \theta$   | $\cos \theta$   |
| 0 1 0    | 0 1 1    | 1    | $-\sin \theta$  | $\cos \theta$   |
| 0 1 1    | 1 1 0    | 0    | $-\cos \theta$  | $\sin \theta$   |
| 1 0 0    | 1 1 1    | 0    | $-\cos \theta$  | $-\sin \theta$  |
| 1 0 1    | 1 1 1    | 1    | $-\sin \theta$  | $-\cos \theta$  |
| 1 1 0    | 1 0 0    | 1    | $\sin \theta$   | $-\cos \theta$  |
| 1 1 1    | 0 1 1    | 0    | $\cos \theta$   | $-\sin \theta$  |

|   |   |
|---|---|
| 0 | 0 |
| 0 | 0 |
| 0 | 1 |
| 1 | 0 |
| 1 | 1 |
| 1 | 0 |
| 0 | 1 |

0 0 0 0  
 0 1 1 0  
 1 1 1 1  
 1 0 0 1

$$\theta = \sum_{k=1}^N b_k \theta_k$$

$b_k$  sign + N bit — (N+1) bit fractional b

$$b_k \in \{0, 1\}$$

$$\theta_k = 2^{-k}$$

$\theta$  is constrained to be positive  $b_0 = 0$

$$\theta = \sum_{k=1}^N b_k 2^{-k} = \phi_0 + \sum_{k=2}^{N+1} r_k 2^{-k}$$

$$r_k \in \{-1, +1\} \quad \text{Signed digits}$$

$\phi_0$  constant

$\oplus$  Subrotation by  $2^{-k}$

2 equal  $\oplus$  half rotations by  $2^{-k-1}$

$\odot$  Subrotation

2 equal opposite half rotations by  $\pm 2^{-k-1}$

## Binary Representation

$b_k = 1$  : rotation by  $2^{-k}$

$b_k = 0$  : zero rotation

$b$ -th rotation

fixed rotation by  $2^{-k-1}$

$\begin{cases} \text{pos rotation} \leftarrow b_k = 1 \\ \text{neg rotation} \leftarrow b_k = 0 \end{cases}$

Combining all the fixed rotations

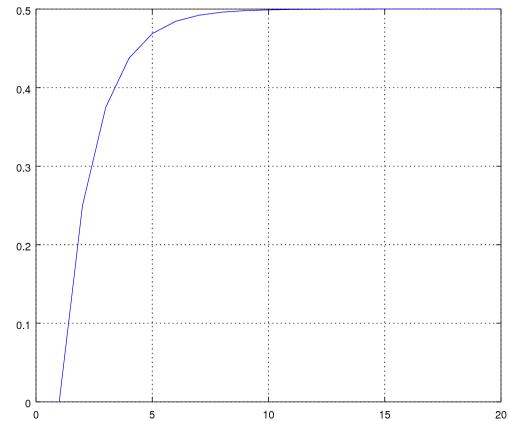
$\rightarrow$  initial fixed rotation

| $b_1$<br>$2^{-1}$      | $b_2$<br>$2^{-2}$      | $b_3$<br>$2^{-3}$      | $\dots$ | $b_N$<br>$2^{-N}$        |
|------------------------|------------------------|------------------------|---------|--------------------------|
| $+2^2$                 | $+2^{-3}$              | $+2^{-4}$              |         | $+2^{-N-1}$              |
| $(b_1=1)$<br>$+2^{-2}$ | $(b_2=1)$<br>$+2^{-3}$ | $(b_3=1)$<br>$+2^{-4}$ |         | $(b_N=1)$<br>$+2^{-N-1}$ |
| $(b_1=0)$<br>$-2^{-2}$ | $(b_2=0)$<br>$-2^{-3}$ | $(b_3=0)$<br>$-2^{-4}$ |         | $(b_N=0)$<br>$-2^{-N-1}$ |

initial fixed rotation

$$\phi_0 = \frac{1}{2^1} + \frac{1}{2^3} + \dots + \frac{1}{2^{N+1}}$$

$$= \frac{\frac{1}{2^2} (1 - \frac{1}{2^N})}{(1 - \frac{1}{2})} = \frac{1}{2} \left(1 - \frac{1}{2^N}\right) = \frac{1}{2} - \frac{1}{2^{N+1}}$$



the rotation after recoding

— a fixed initial rotation  $\phi_0$

a sequence of  $\oplus/\ominus$  rotations

$$b_k = 1 \quad + 2^{-k-1} \text{ rotation}$$
$$b_k = 0 \quad - 2^{-k-1} \text{ rotation}$$

$$r_k = (2b_{k-1} - 1)$$

$$2 \cdot 1 - 1 = +1$$

$$b_{k-1} = 1 \rightarrow r_k = +1$$

$$2 \cdot 0 - 1 = -1$$

$$b_{k-1} = 0 \rightarrow r_k = -1$$

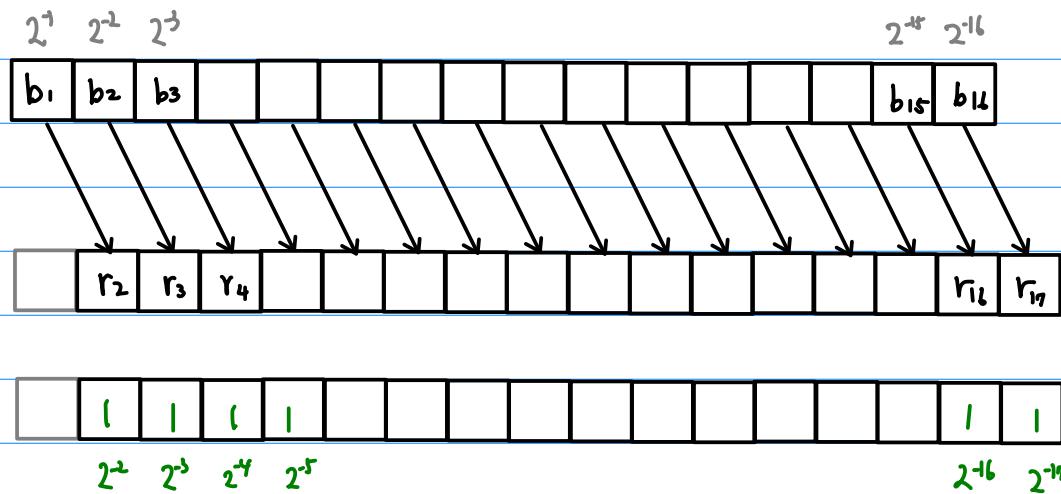
The recoding need not be explicitly performed

Simply replacing  $b_k = 0$  with  $\ominus 1$

This recoding maintains  
a constant scaling factor  $k$

$$\Theta = \sum_{k=1}^N b_k 2^{-k} = \phi_0 + \sum_{k=2}^{N+1} r_k 2^{-k}$$

Binary Representation  $\{b_k\}$



Signed Digit Recoding  $\{r_k\}$

The scaling  $K$ .

The initial rotation  $\phi$ .

rotation starting point

$$(X_0, Y_0) = (K \cos \phi_0, K \sin \phi)$$

— fixed

— no error buildup

— rotation direction

immediately obtained from the binary representation

→ no need for comparison

the subangles  $\theta_k = 2^{-k}$  used in recoding

the subangles  $\theta_k = \tan^{-1}(2^{-k})$  used in CORDIC

$\tan \theta_k$  multipliers used

in the first few subrotation stages

cannot be implemented

as a simple shift-and-add operations

→ ROM implementation

reduced chip area

higher operating speed.

# Architecture

- ① phase accumulator  $\phi \in [-\pi, +\pi]$
- ② radian converter  $\phi \rightarrow \theta \in [0, \frac{\pi}{4}]$
- ③ sine/cosine generator  $\sin(\theta)$   $\cos(\theta)$
- ④ output stage  $\sin(\pi\phi)$   $\cos(\pi\phi)$

Overflowing 2's complement accumulator

Normalized by  $\pi$  angle  $\phi$

Need radian angle  $\theta \in [0, \frac{\pi}{4}]$

$0 < \theta < 1$  rad

N-bit binary representation of  $\theta$

Controls the direction of subrotation

N-bit precision of  $\cos \theta$  &  $\sin \theta$

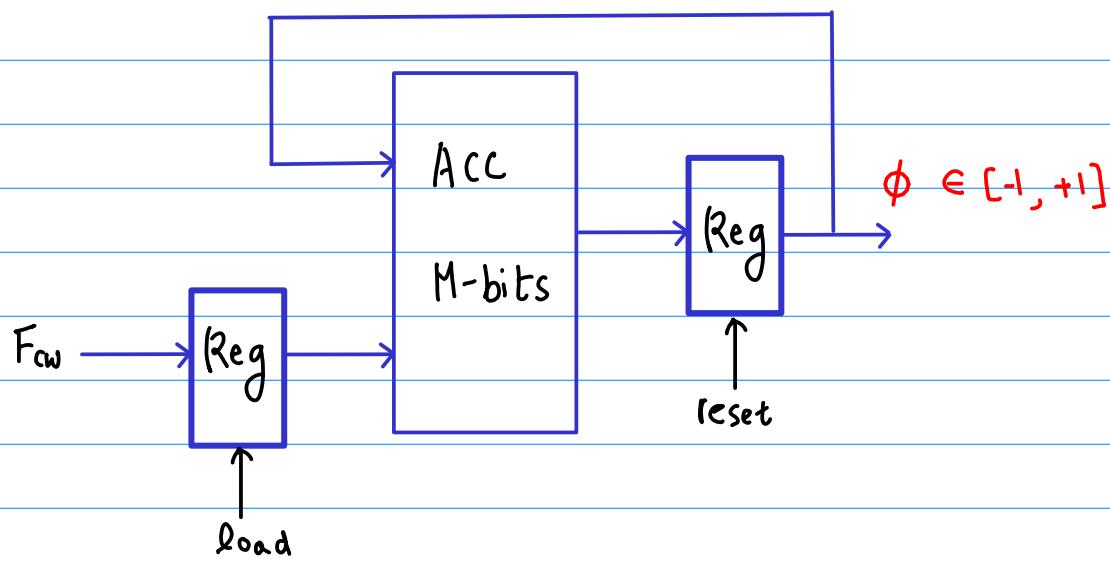
Output stage

$$\theta \rightarrow \pi\phi$$

$$\sin \theta \rightarrow \sin \pi\phi$$

$$\cos \theta \rightarrow \cos \pi\phi$$

# phase accumulator



M-bit adder

repeatedly increments the phase angle

by Fcw at each clock cycle

frequency control word

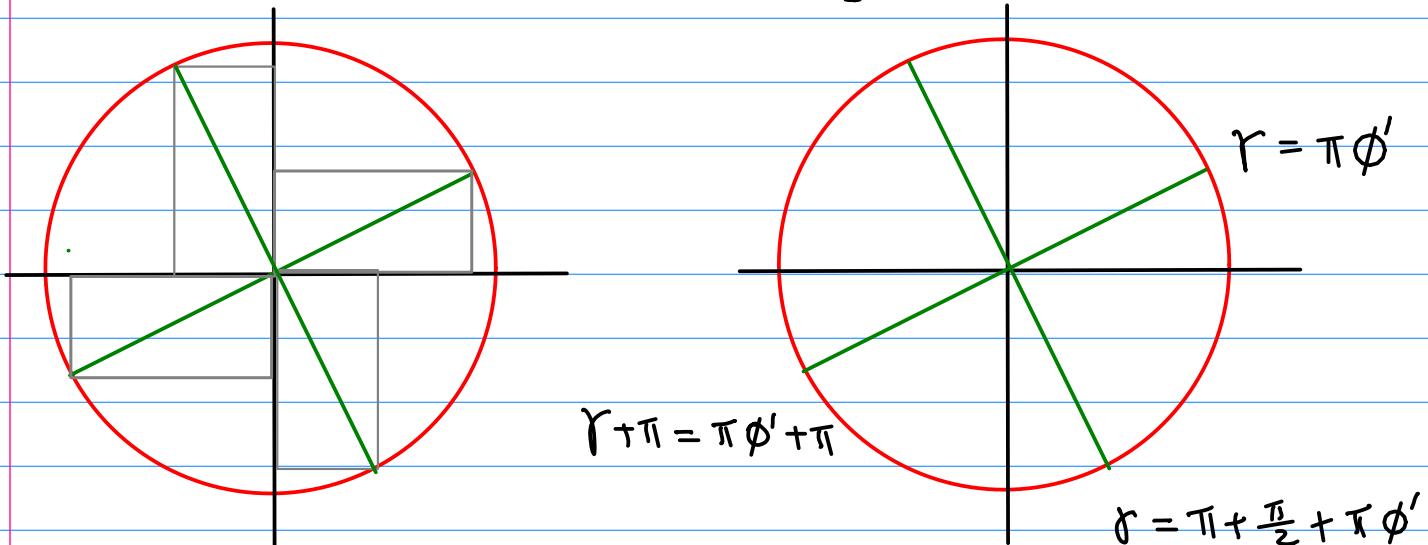
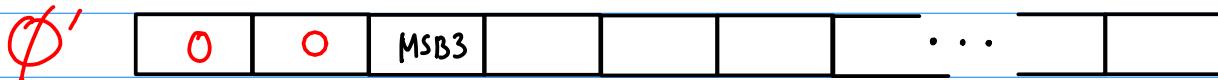
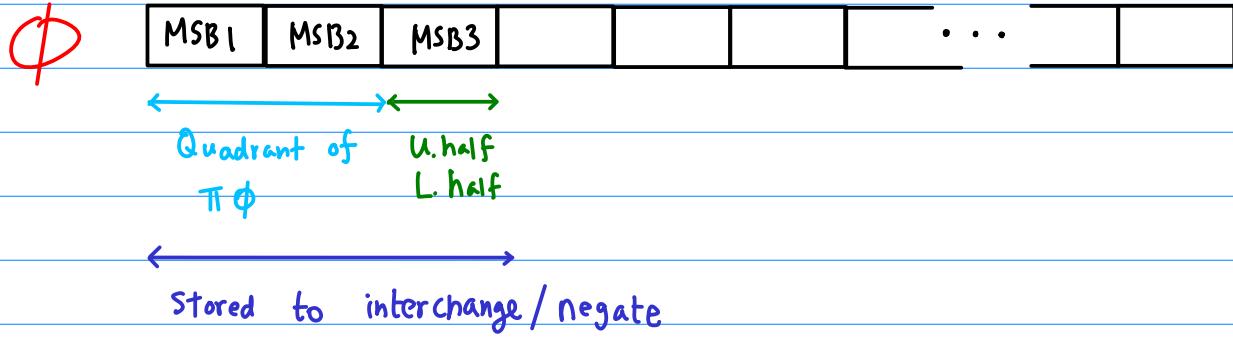
at time  $n$ ,  $\phi = n Fcw / 2^M$

$$\cos \phi = \cos(n Fcw / 2^M)$$

$$\sin \phi = \sin(n Fcw / 2^M)$$

# Radian Converter

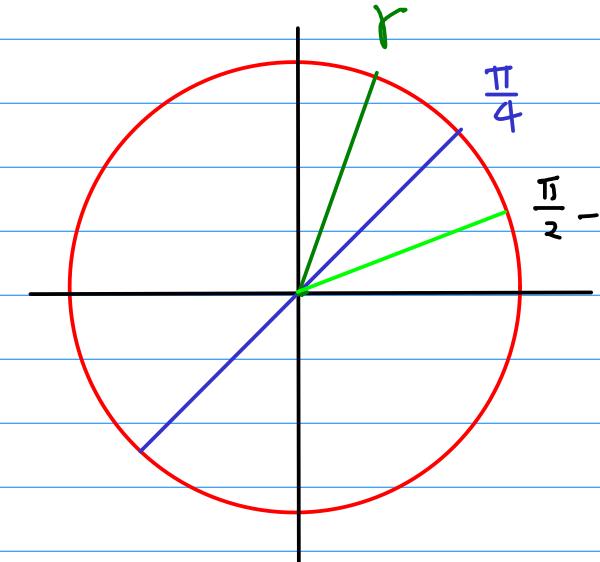
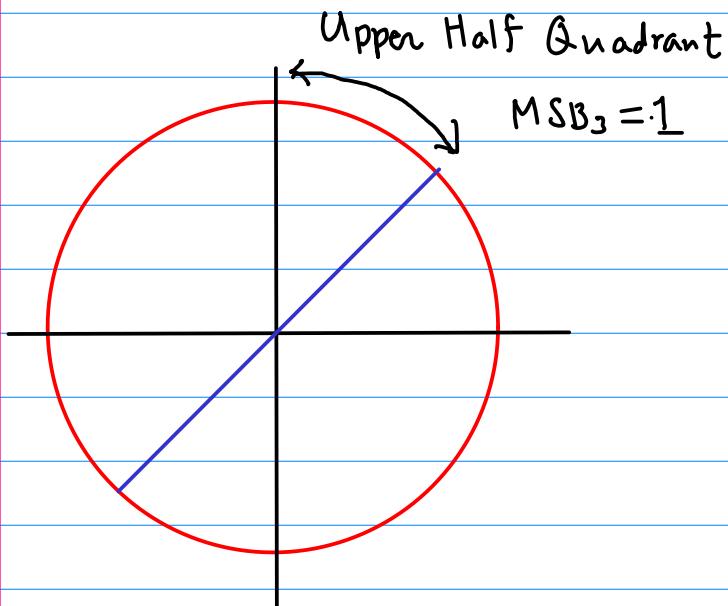
Normalized angle  $\phi$



$$\begin{array}{ccccccc}
 \phi & \rightarrow & \phi' & \rightarrow & \pi \phi' & + & 0 \cdot \frac{\pi}{2} & 00 \\
 & & & & \pi \phi' & + & 1 \cdot \frac{\pi}{2} & 01 \\
 & & & & \pi \phi' & + & 2 \cdot \frac{\pi}{2} & 10 \\
 & & & & \pi \phi' & + & 3 \cdot \frac{\pi}{2} & 11
 \end{array}$$

$\phi'$

|   |   |      |  |  |  |  |     |  |
|---|---|------|--|--|--|--|-----|--|
| 0 | 0 | MSB3 |  |  |  |  | ... |  |
|---|---|------|--|--|--|--|-----|--|



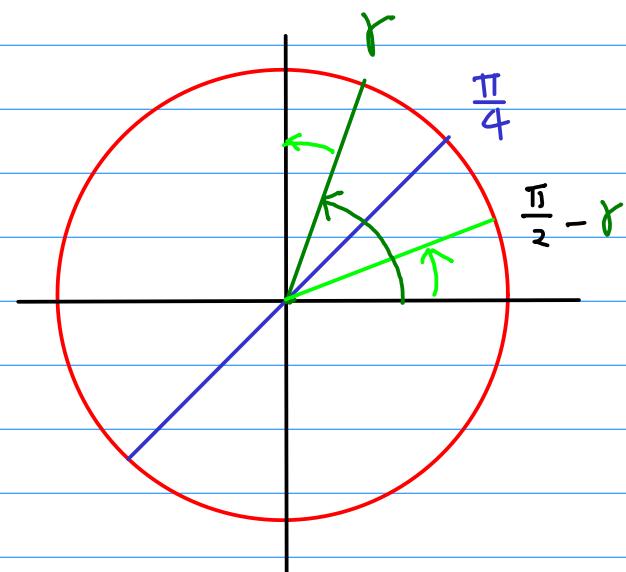
$r > \frac{\pi}{4}$  : Upper Half (MSB<sub>3</sub> = 1)

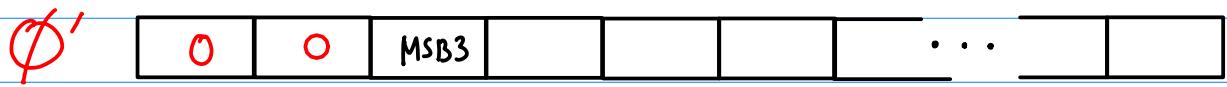
$r < \frac{\pi}{4}$  : Lower Half (MSB<sub>3</sub> = 0)

$$\cos \phi = \sin\left(\frac{\pi}{2} - \phi\right)$$

$$\sin \phi = \cos\left(\frac{\pi}{2} - \phi\right)$$

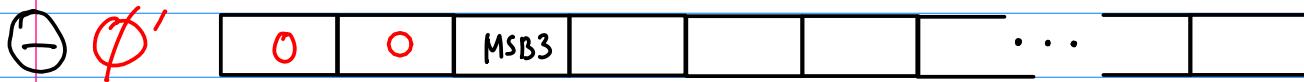
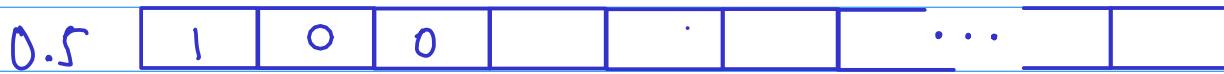
$$r > \frac{\pi}{4} \quad \frac{\pi}{2} - \phi < \frac{\pi}{4}$$





$$MSB3 = 1 \quad \phi' > \frac{\pi}{4}$$

$$\phi'' = \frac{\pi}{2} - \phi'$$



$$\begin{cases} MSB3 = 0 & \phi'' = \phi' \\ MSB3 = 1 & \phi'' = 0.5 - \phi' \end{cases}$$

$$\theta = \pi \phi'' \quad (\text{Handwired Multiplier})$$

$$0 < \theta < \frac{\pi}{4}$$

# Sine / Cosine Generator

Substitution

$$X_{k+1} = X_k - (r_k \tan \theta_k) Y_k$$

$$Y_{k+1} = Y_k + (r_k \tan \theta_k) X_k$$

$$\begin{bmatrix} X_0 \\ Y_0 \end{bmatrix} = \begin{bmatrix} \cos \theta & -\sin \theta \\ \sin \theta & \cos \theta \end{bmatrix} \begin{bmatrix} X_0 \\ Y_0 \end{bmatrix}$$

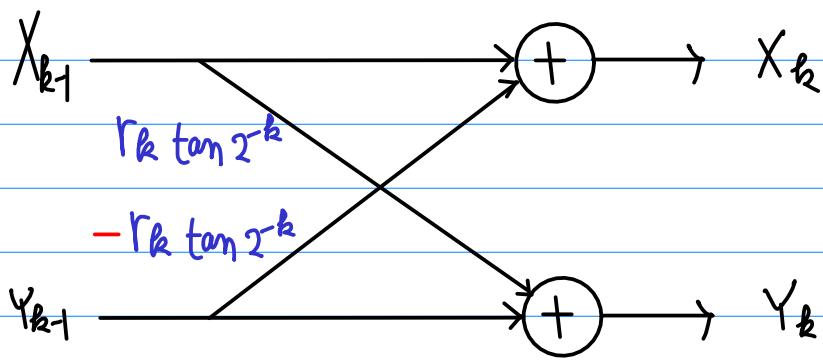
$$= \cos \theta \begin{bmatrix} 1 & -\tan \theta \\ \tan \theta & 1 \end{bmatrix} \begin{bmatrix} X_0 \\ Y_0 \end{bmatrix}$$

$$\theta = \sigma_0 \theta_0 + \sigma_1 \theta_1 + \cdots + \sigma_N \theta_N$$

$$\sigma_k = \{-1, 0, +1\}$$

$$\begin{bmatrix} X_0 \\ Y_0 \end{bmatrix} = K \begin{bmatrix} 1 & -\tan \sigma_N \theta_N \\ \tan \sigma_N \theta_N & 1 \end{bmatrix} \cdots \begin{bmatrix} 1 & -\tan \sigma_0 \theta_0 \\ \tan \sigma_0 \theta_0 & 1 \end{bmatrix} \begin{bmatrix} X_0 \\ Y_0 \end{bmatrix}$$

$$K = \cos \sigma_0 \theta_0 \cdot \cos \sigma_1 \theta_1 \cdots \cos \sigma_N \theta_N$$



$r_k$  or  $b_{k-1}$

