NAND Design (H1)

20160706

NAND Layout

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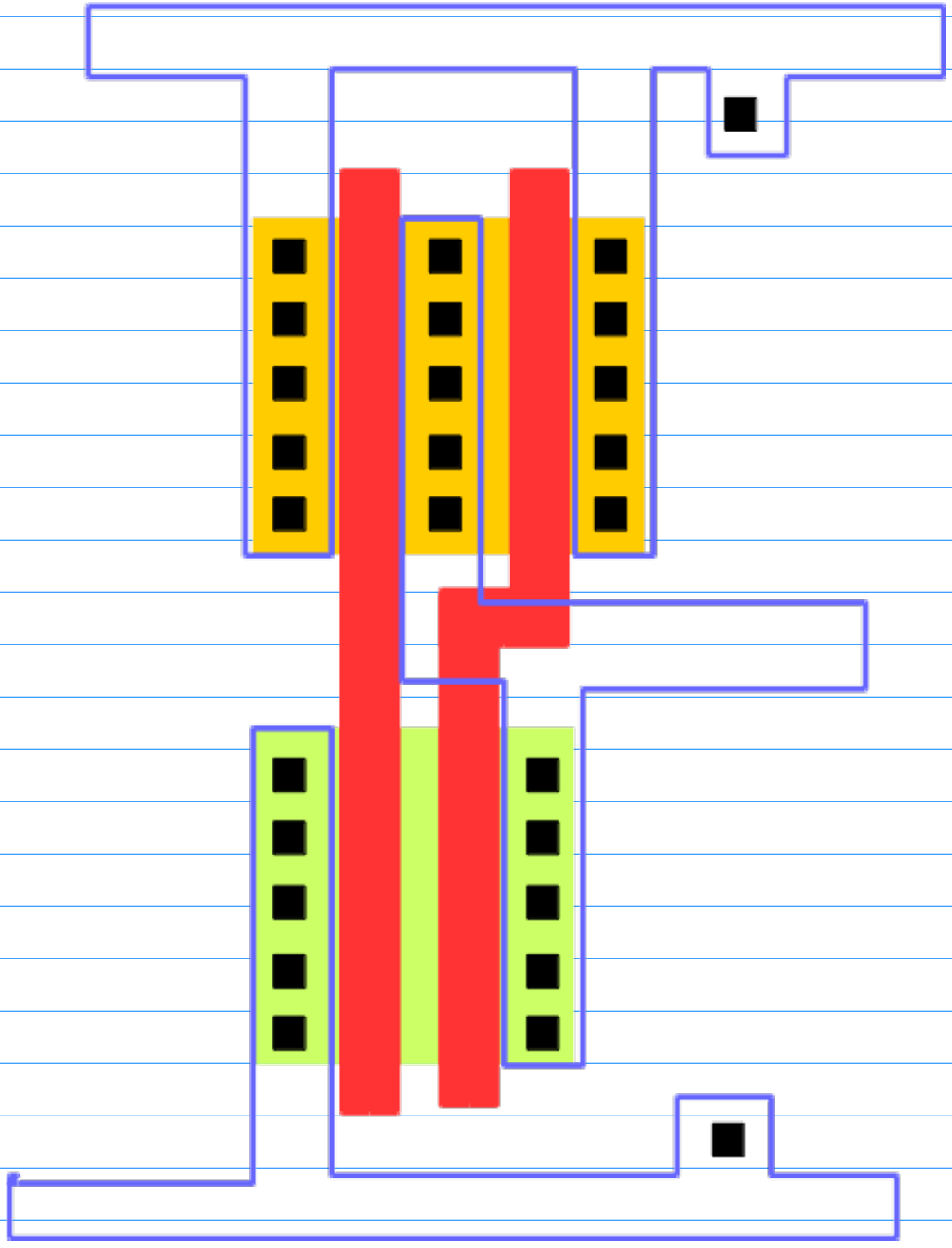
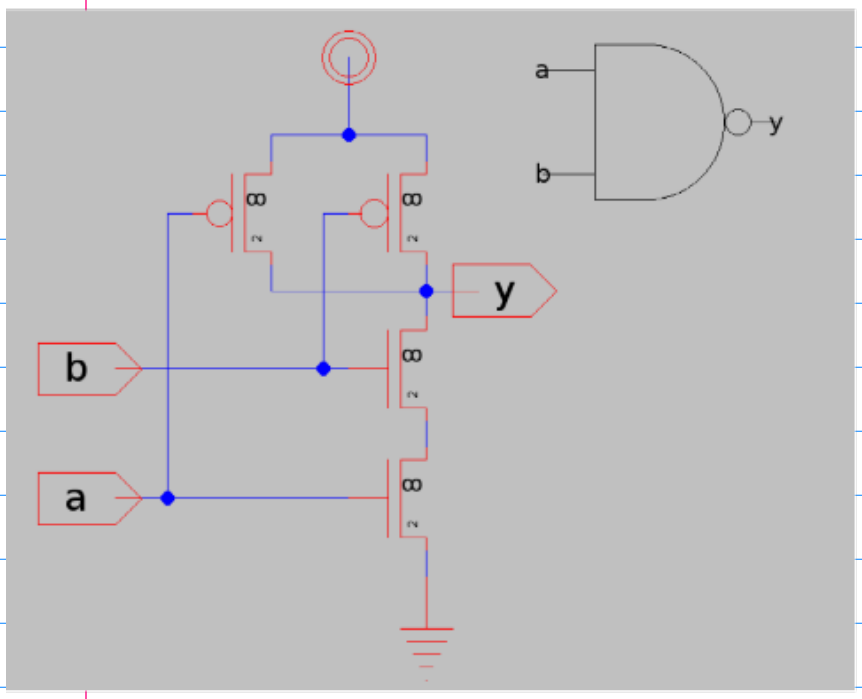
Based on

Uyemura
Introduction to VLSI Circuits and Systems

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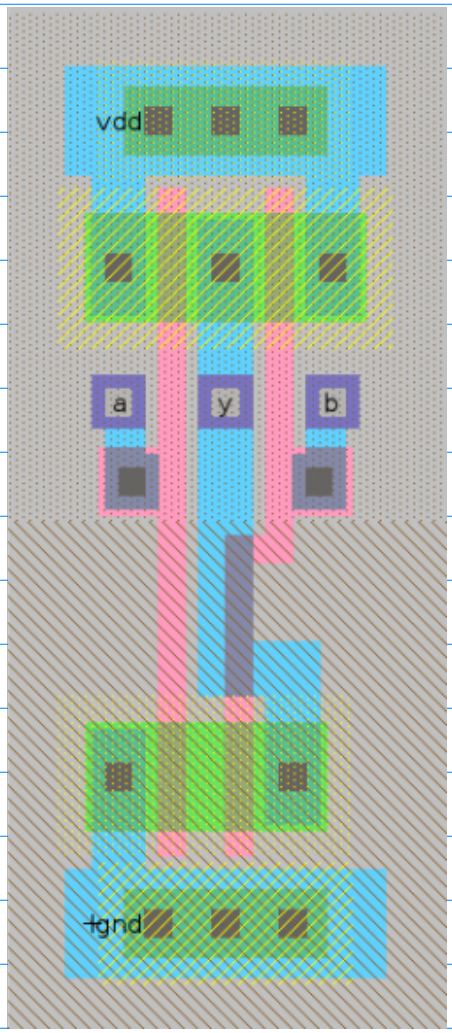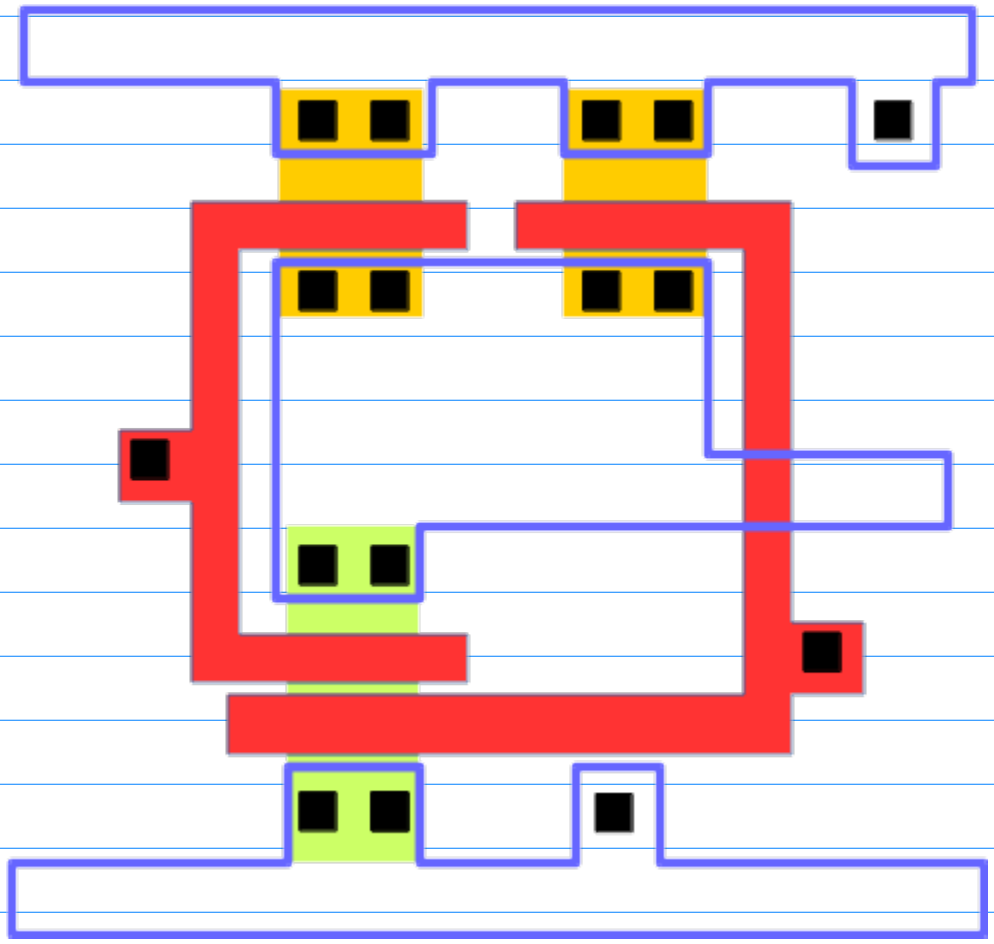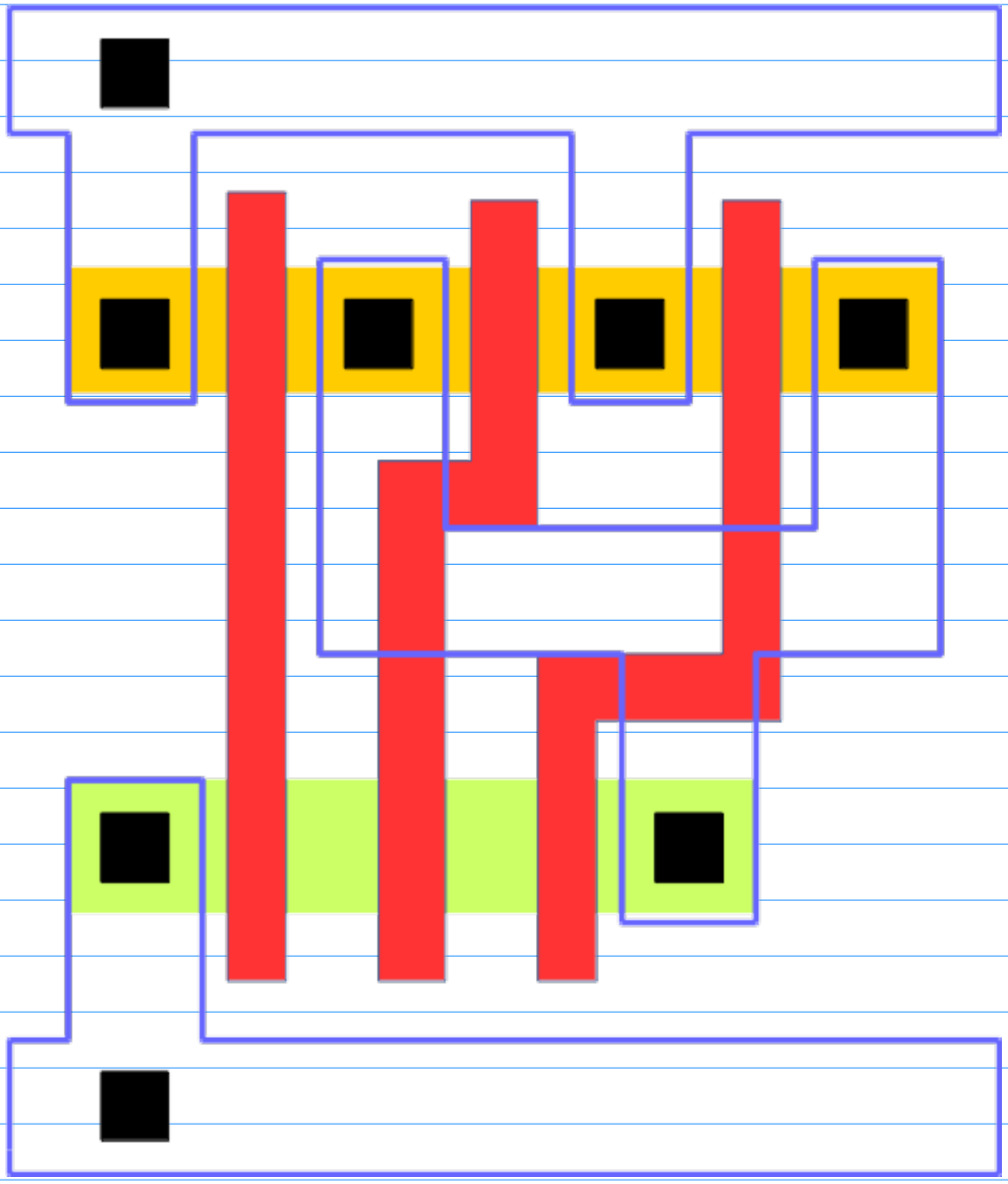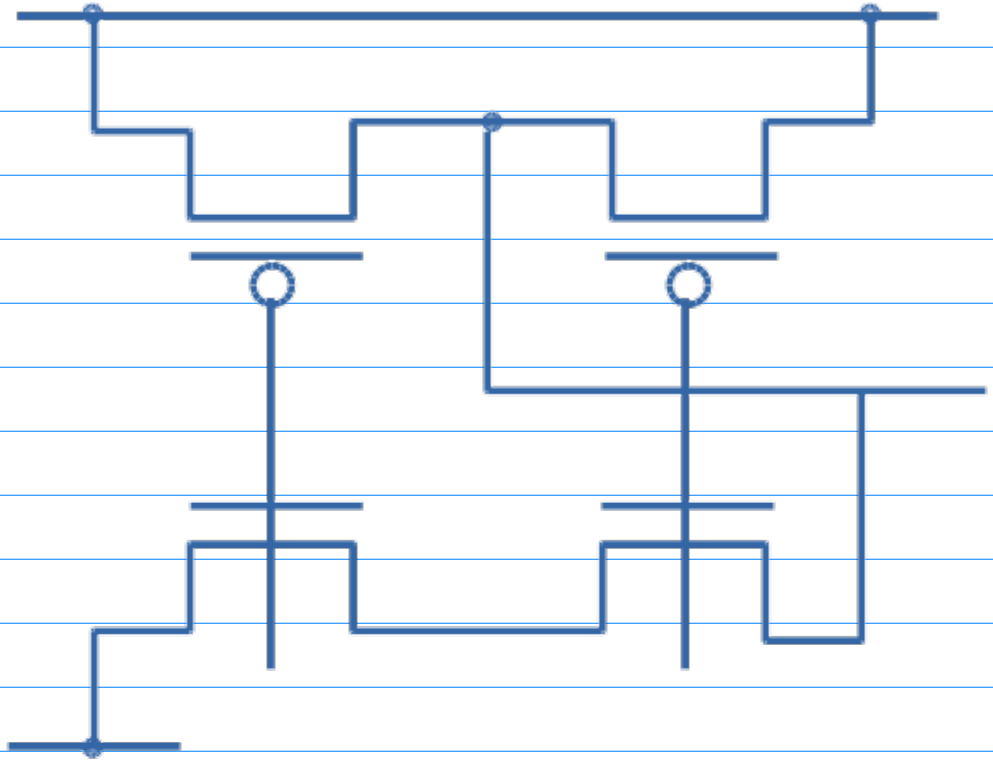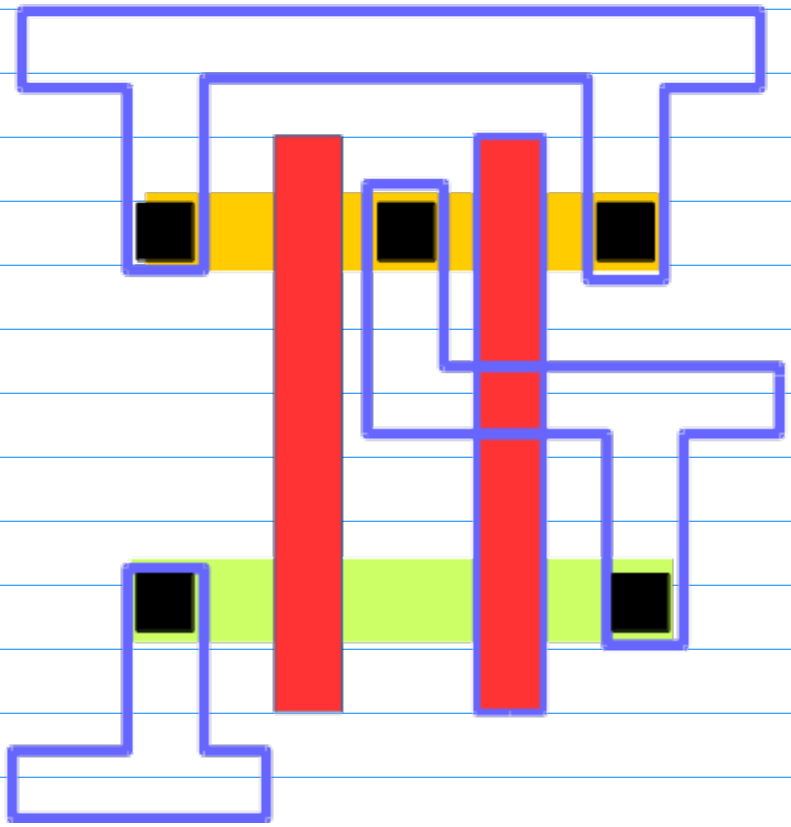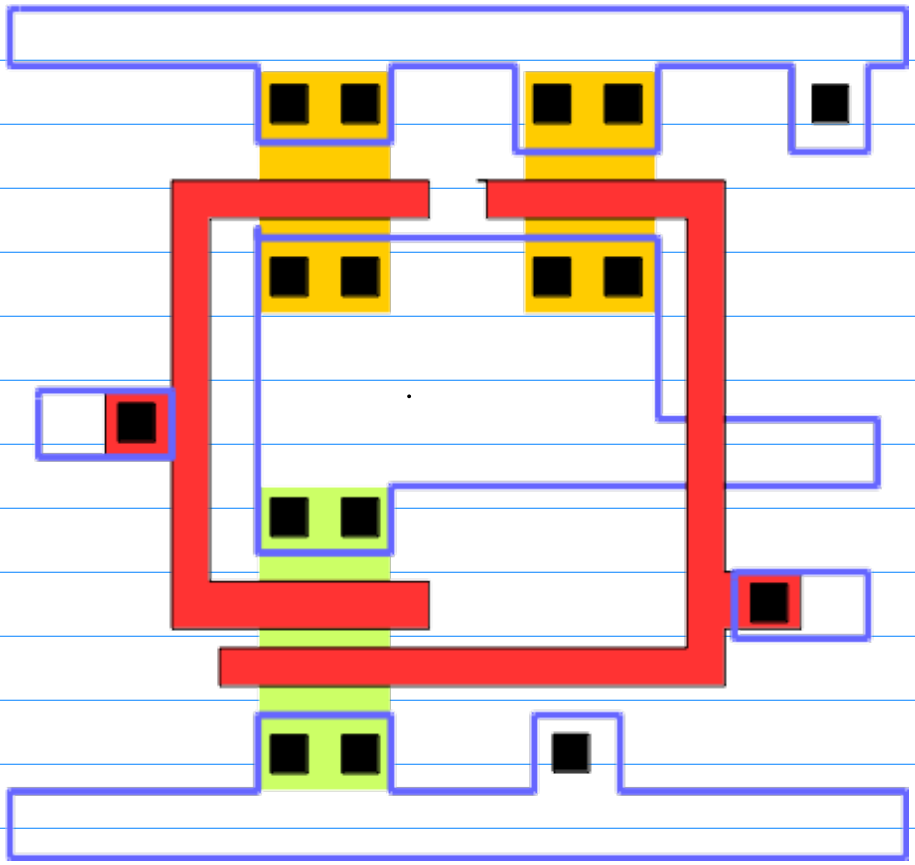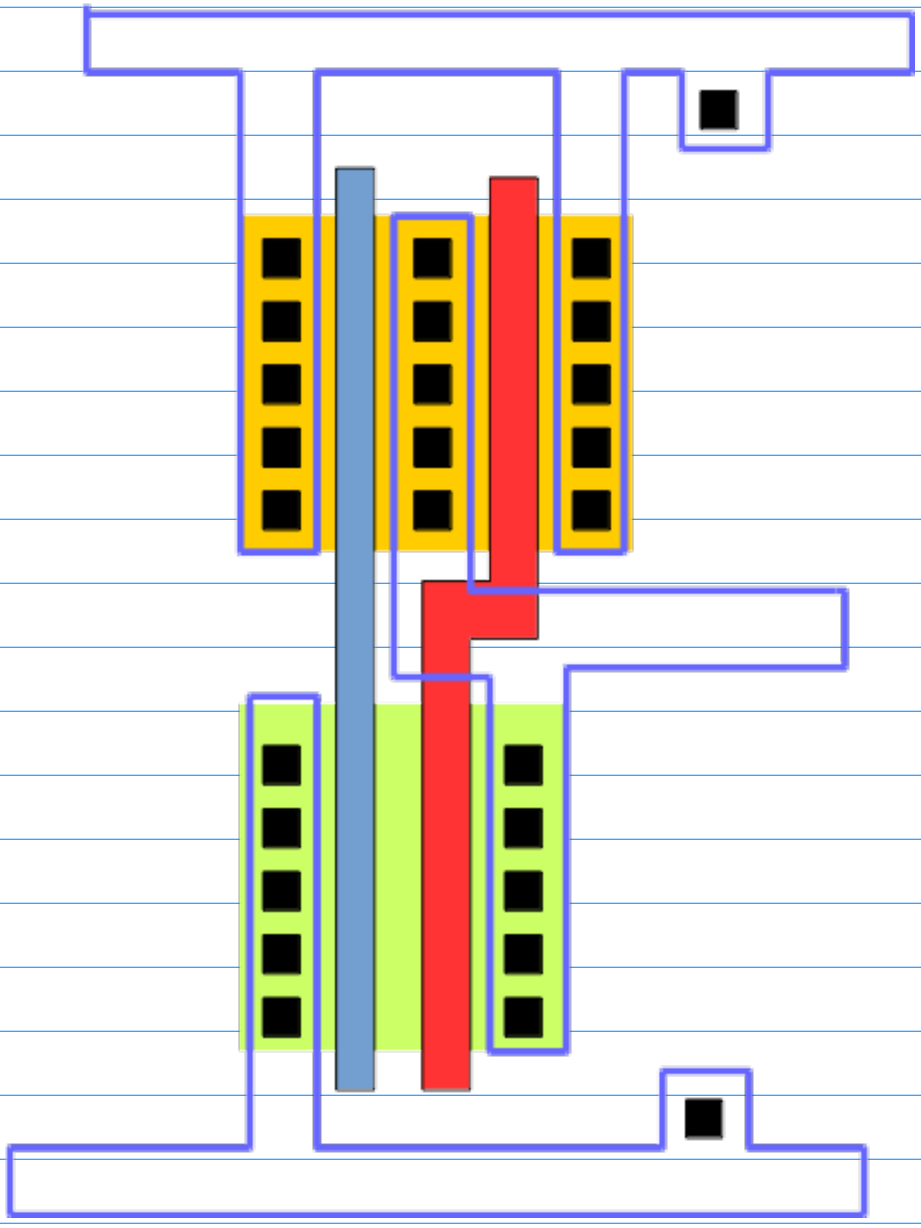
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