

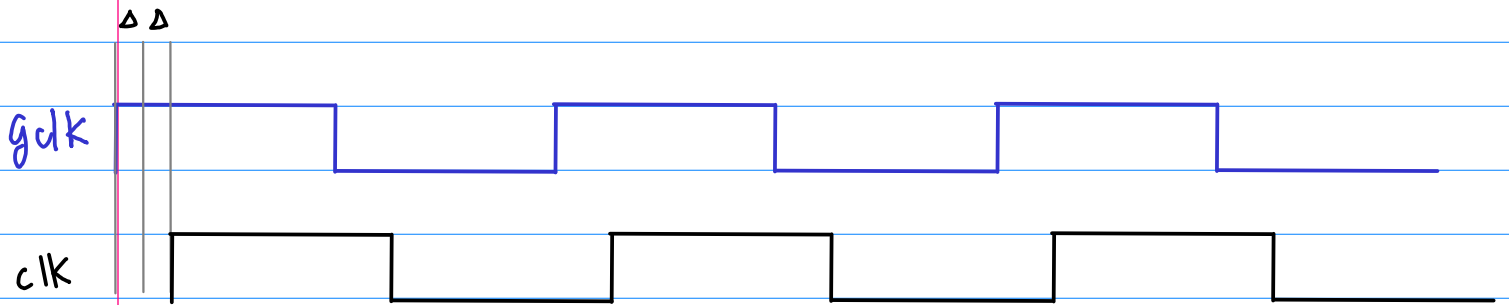
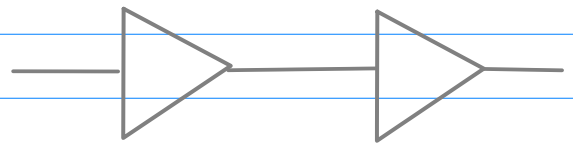
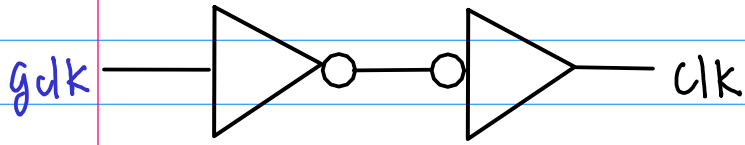
SOC HW #3

20160416

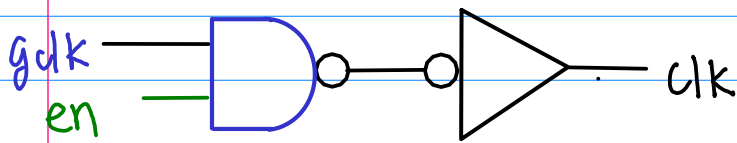
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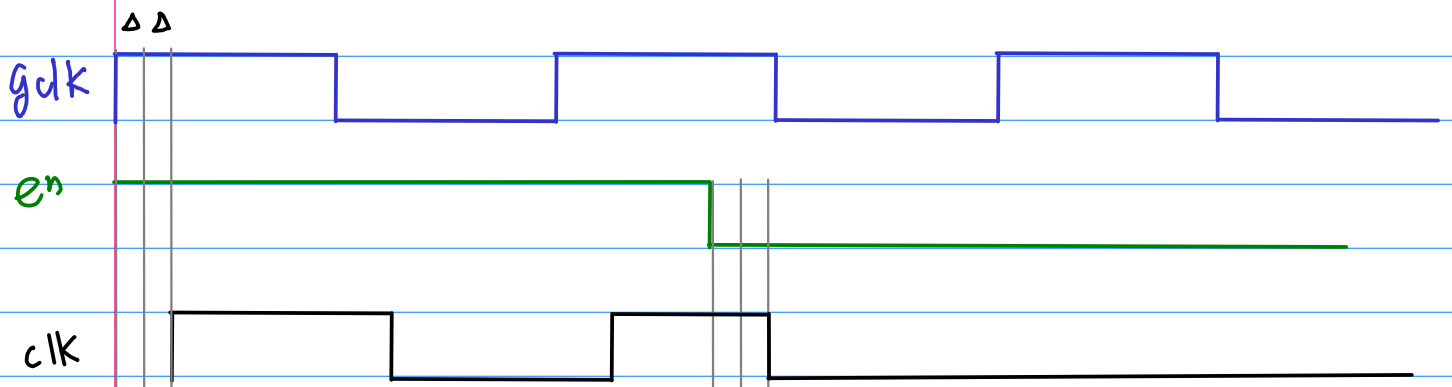
* clock buffer



* Enabled / Qualified / Gated clock

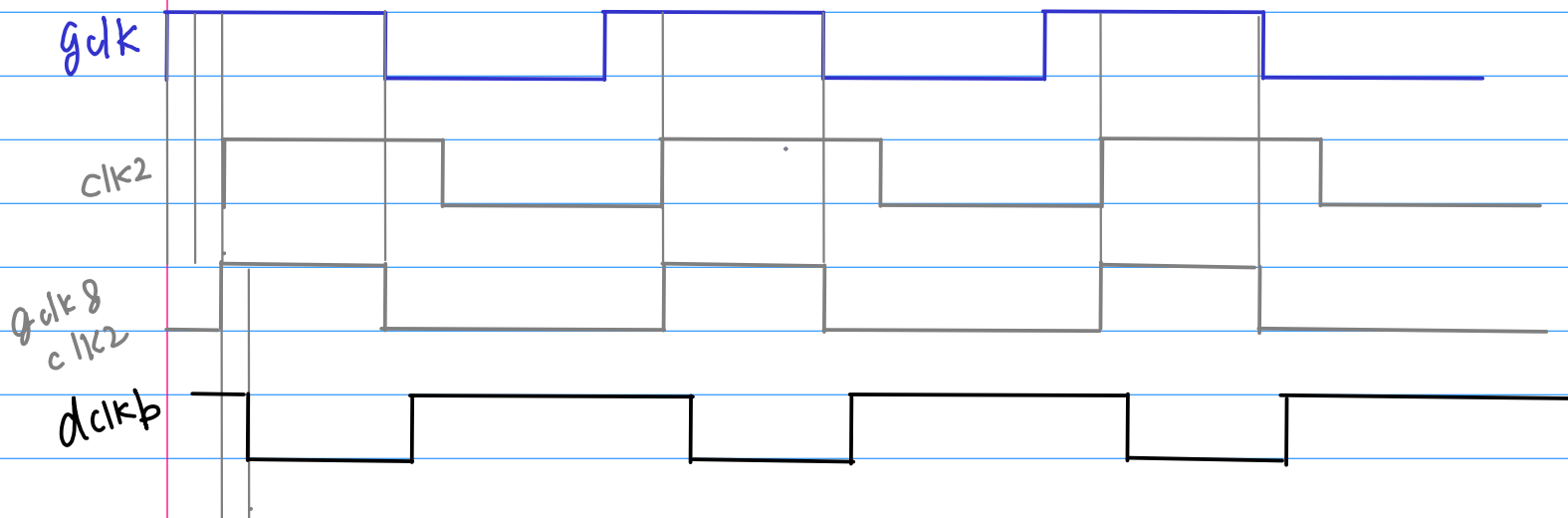
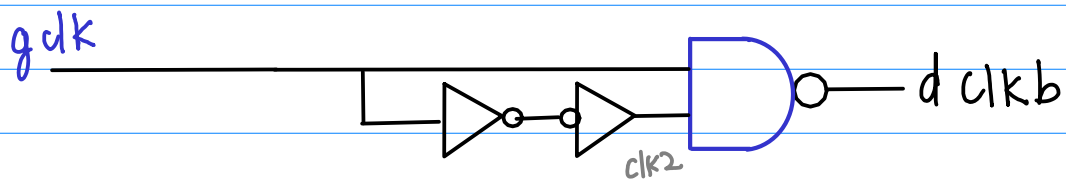
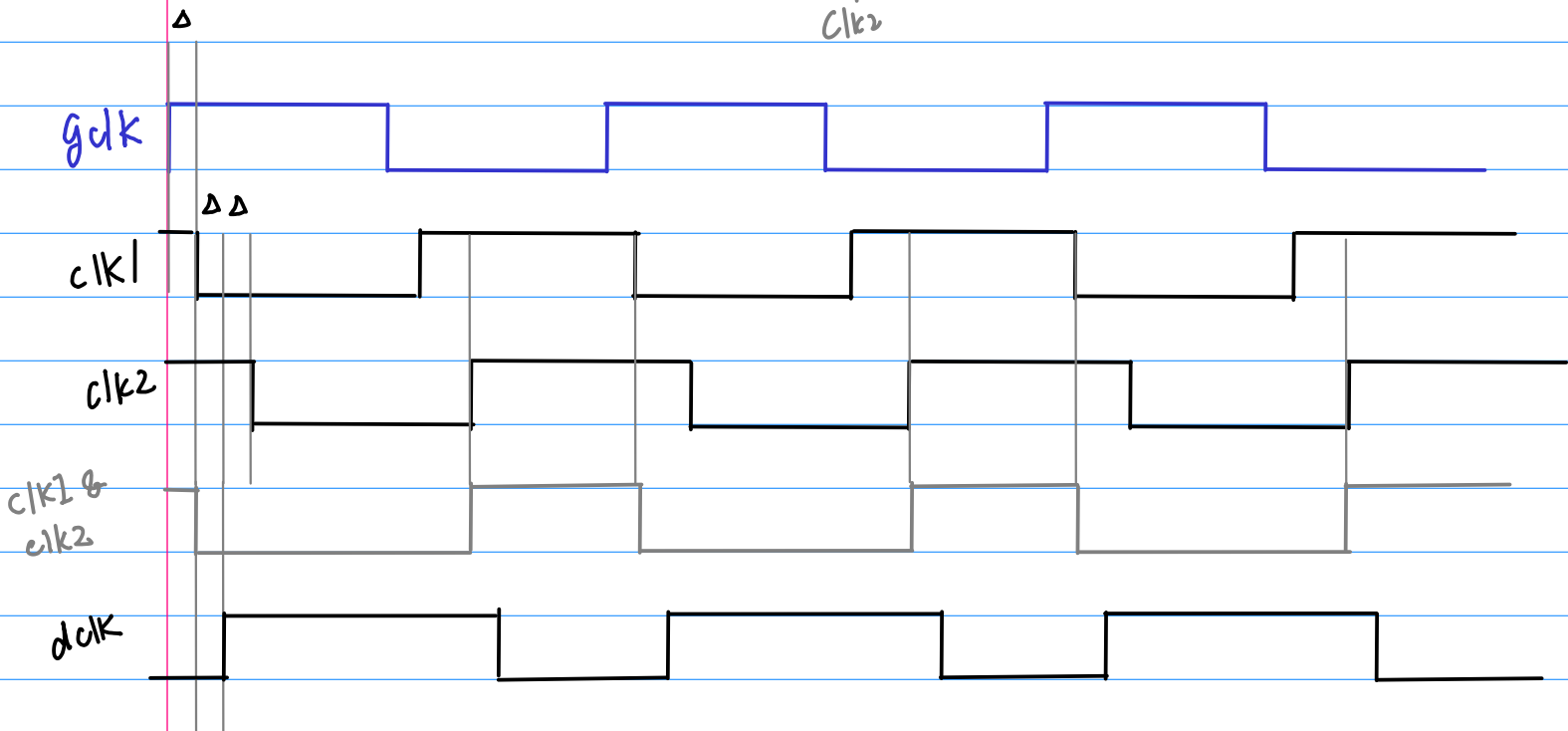
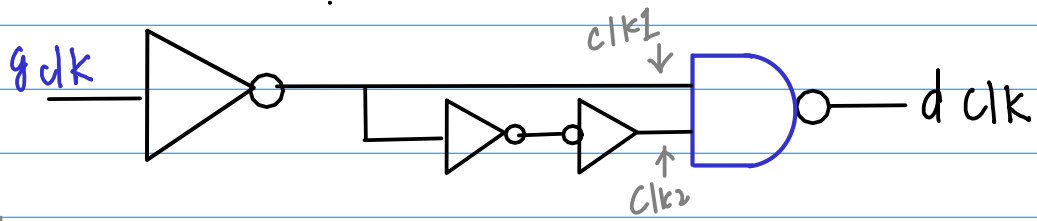


assume en=1

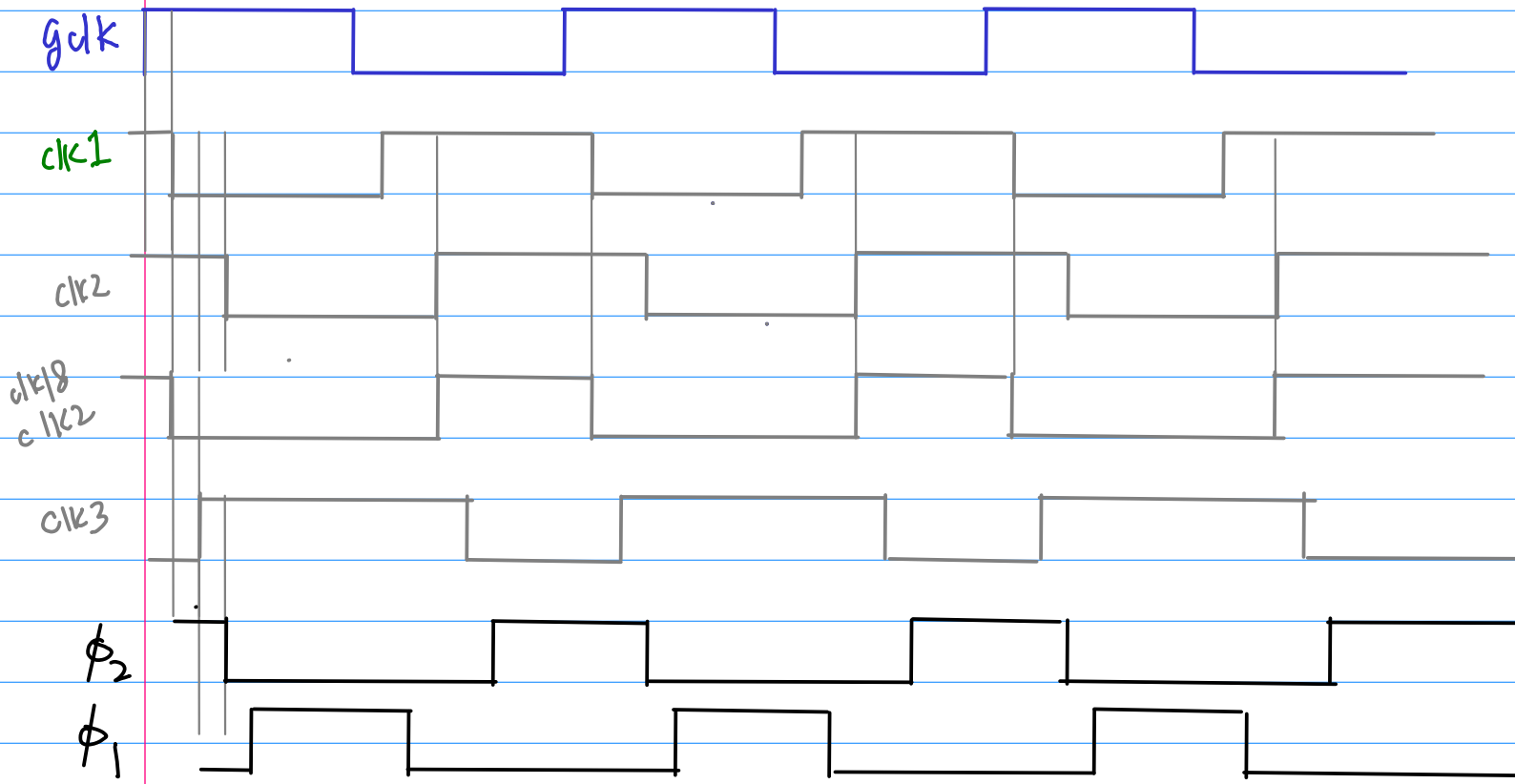
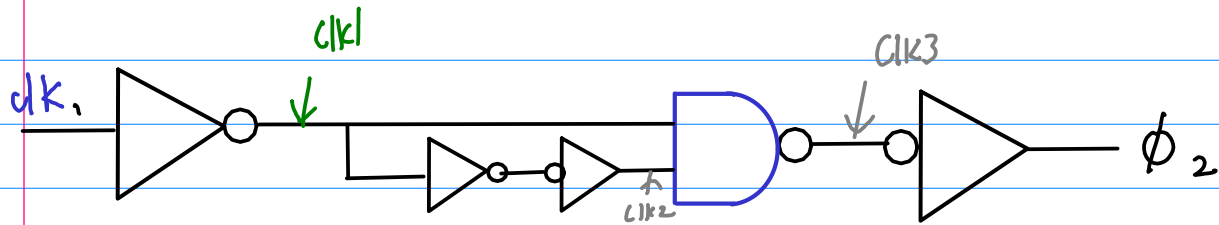
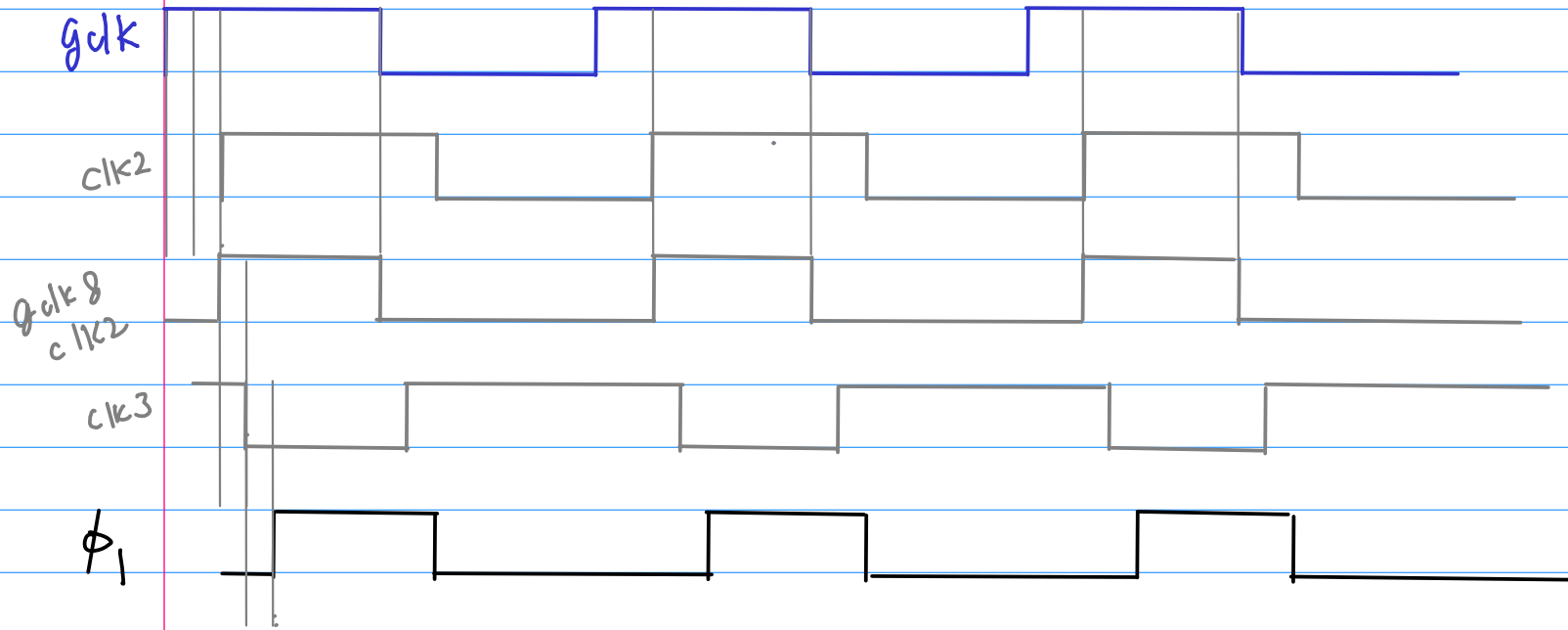
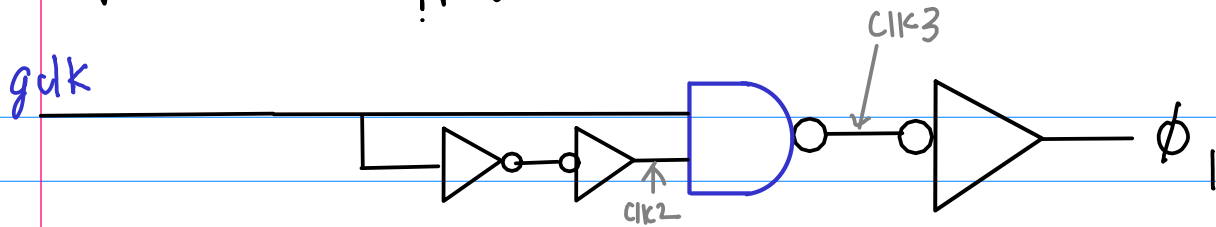




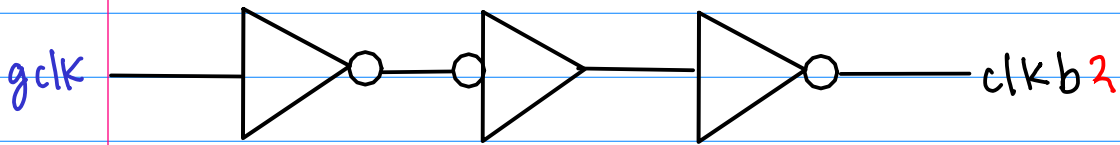
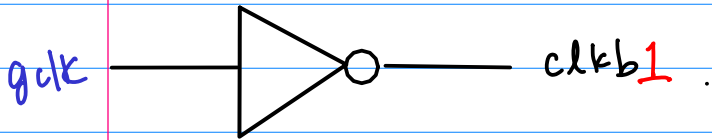
* Stretched Clocks



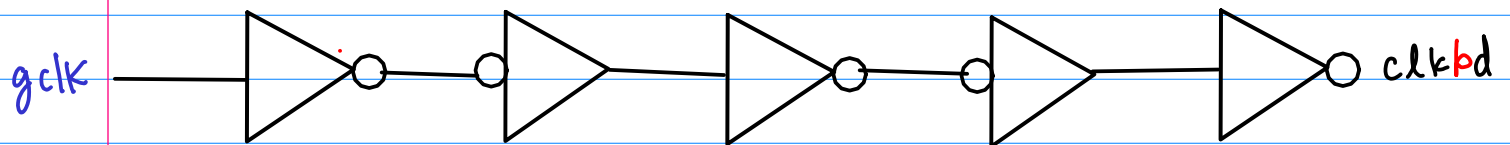
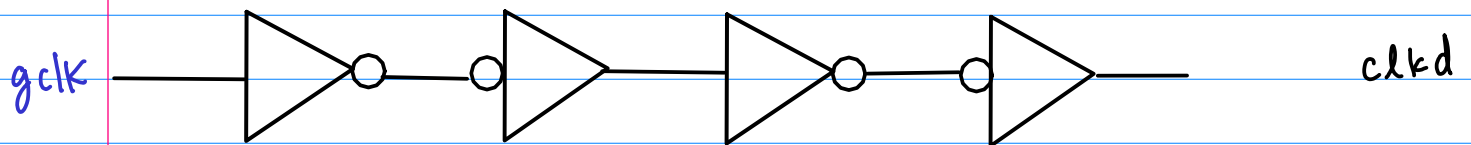
★ Non-overlapping clocks



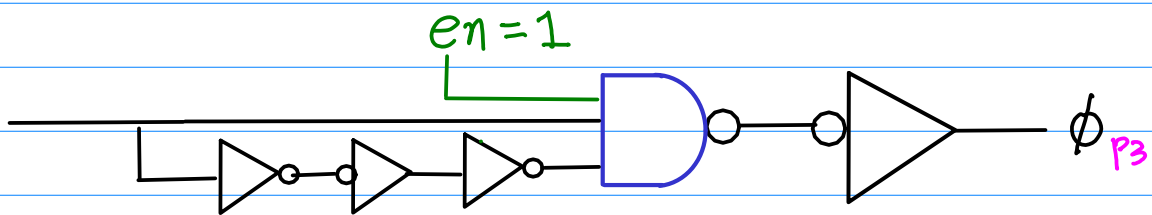
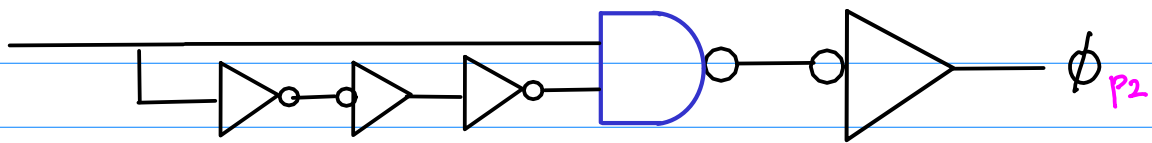
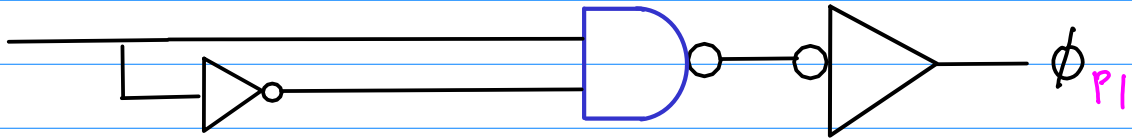
★ Complementary Clocks



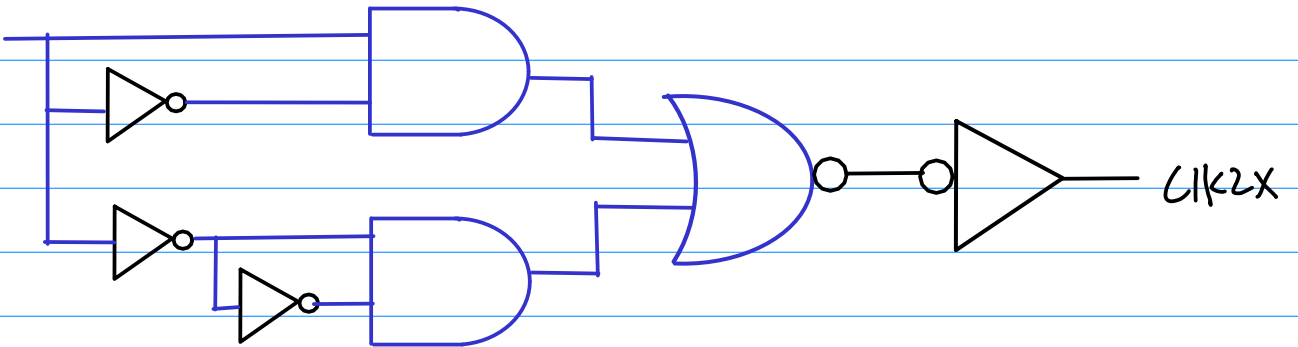
★ delayed clocks



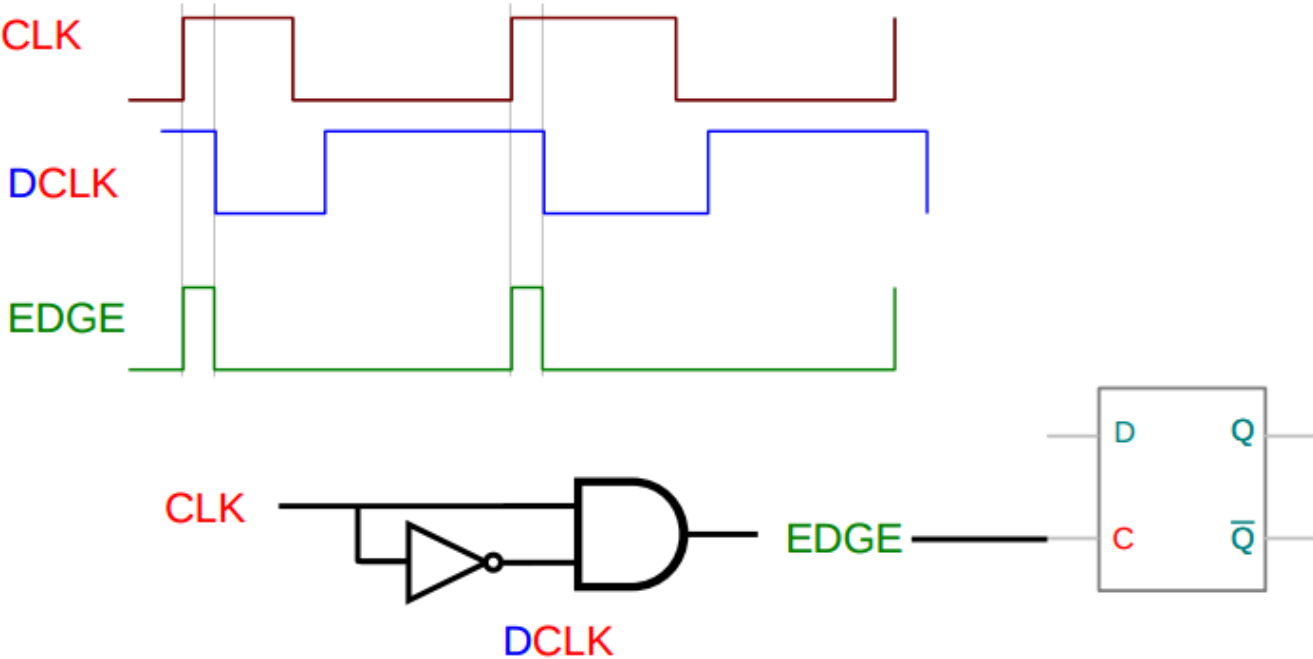
★ Pulsed Clocks



★ Clock Doubler



Edge Detector (1)



Edge Detector (2)

