

Logic Circuit Design

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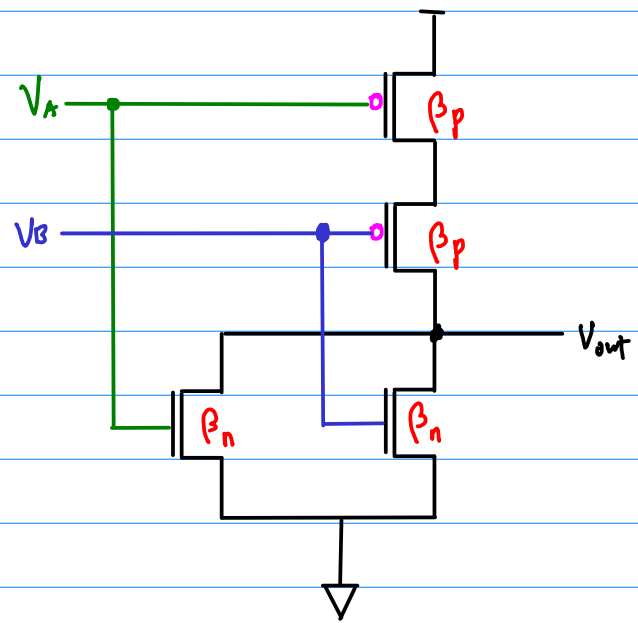
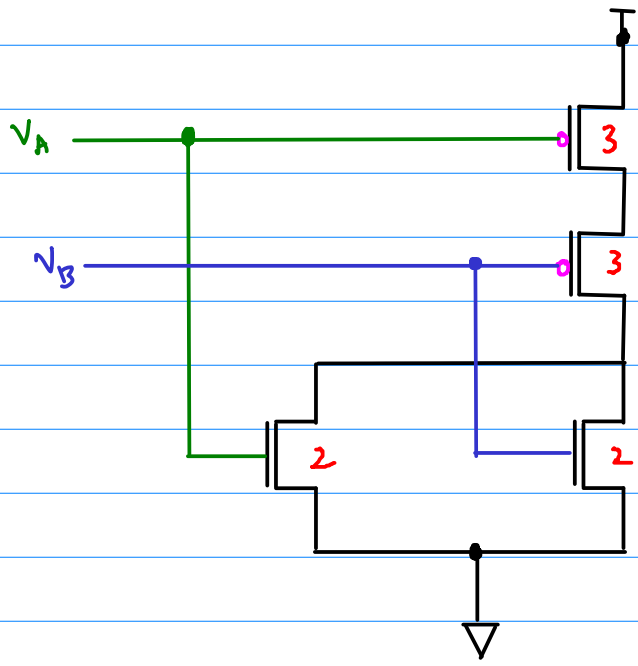
References

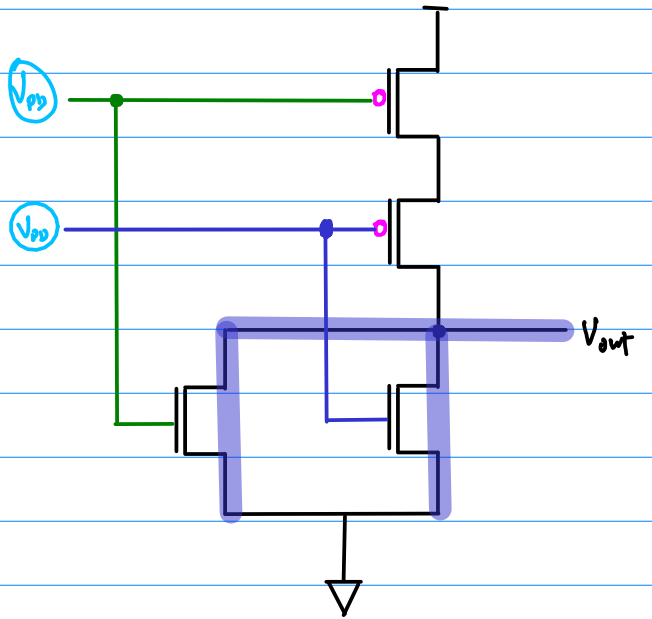
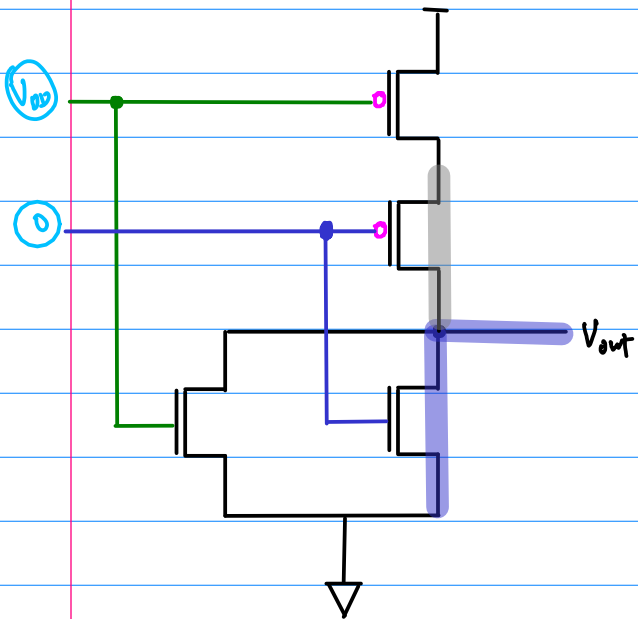
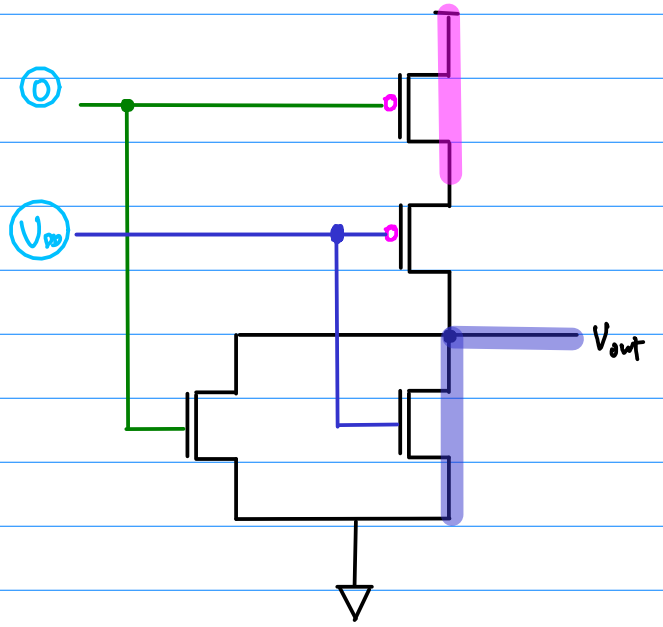
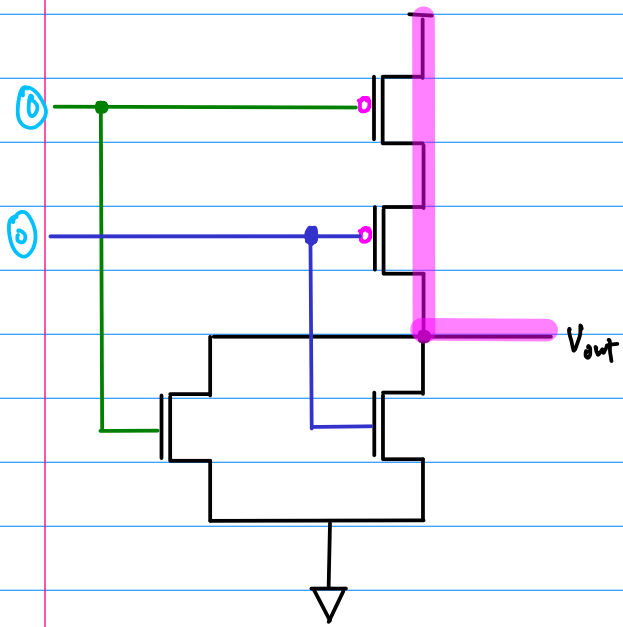
Some Figures from the following sites

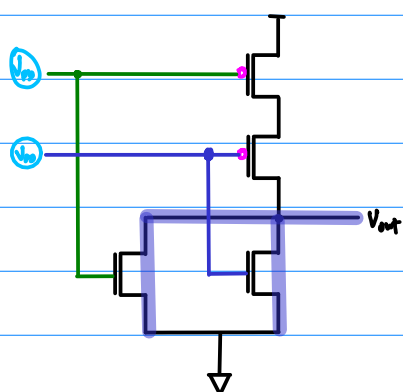
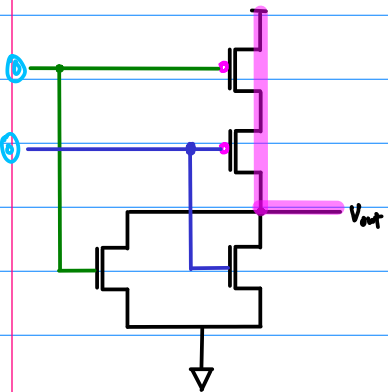
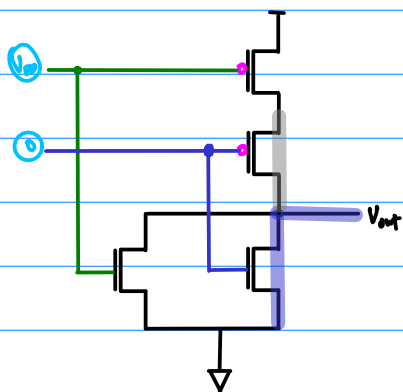
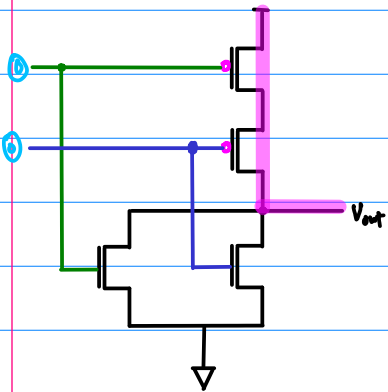
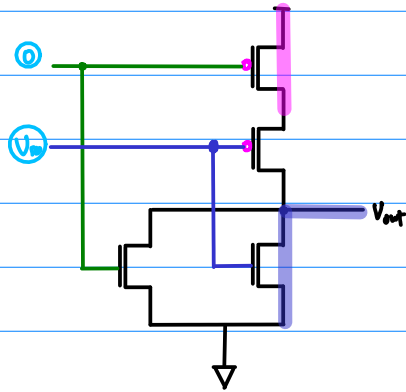
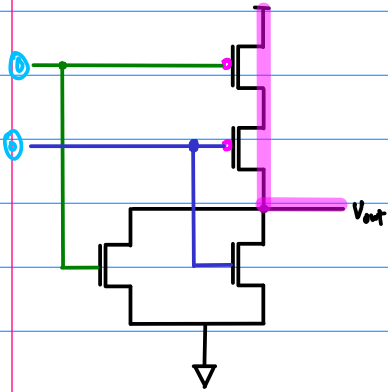
[1] <http://pages.hmc.edu/harris/cmosvlsi/4e/index.html>
Weste & Harris Book Site

[2] Introduction to VLSI Circuits and Systems, Uyemura

[2] en.wikipedia.org







	V_A	V_B	V_{out}
	0	0	V_{DD}
Ⓒ	0	V_{DD}	0
Ⓑ	V_{DD}	0	0
Ⓐ	V_{DD}	V_{DD}	0

