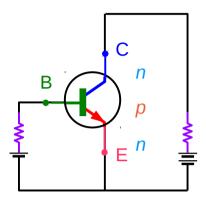
BJT Topologies – VD Bias

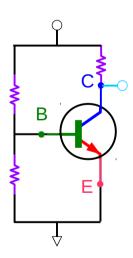
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Input Characteristic Curves
Output Characteristic Curves

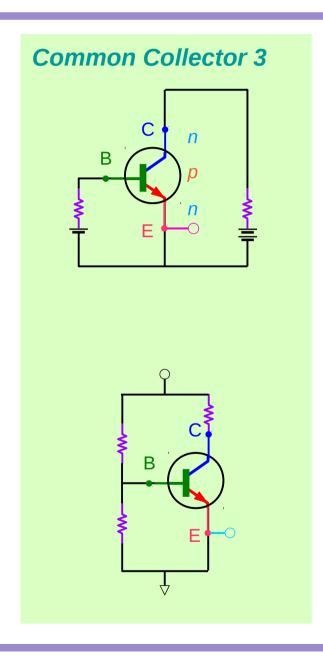
VD Biased CE

Common Emitter



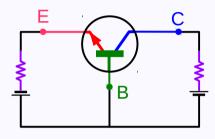


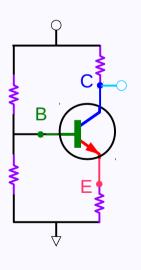
VD Biased CC

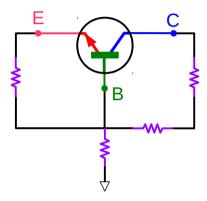


VD Biased CB

Common Base







AC Equivalent Circuits

Input Characteristic Curves
Output Characteristic Curves

References

- [1] http://en.wikipedia.org/
- [2] J.H. McClellan, et al., Signal Processing First, Pearson Prentice Hall, 2003