Overviews of Carry and Overflow Flags

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Outline

Based on

- Overview
 - Overview

Based on

"Self-service Linux: Mastering the Art of Problem Determination",

Mark Wilding

"Computer Architecture: A Programmer's Perspective", Bryant & O'Hallaron

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Compling 32-bit program on 64-bit gcc

- gcc -v
- gcc -m32 t.c
- sudo apt-get install gcc-multilib
- sudo apt-get install g++-multilib
- gcc-multilib
- g++-multilib
- gcc -m32
- objdump -m i386

TOC: Overview

- Carry flag and overflow flag
- Signed and unsigned computations
- Flags for an <u>unsigned</u> number
- Flags for a signed number
- Detecting errors in usigned and signed arithmetic
- The verb to overflow v.s. the overflow flag

Carry flag and overflow flag

- considering carry and overflow flags in x86
- do not confuse the carry flag with the overflow flag in integer arithmetic.
- the ALU always sets these flags appropriately when doing any integer math.
- these flags can occur on its own, or both together.

Signed and unsigned computations

- the CPU's ALU <u>doesn't</u> care or know whether <u>signed</u> or <u>unsigned</u> computations are performed;
- the <u>ALU</u> just performs integer arithmetic and sets the flags appropriately.
- It's up to the <u>programmer</u> to know which flag to check after the arithmetic is done.

Flags for an unsigned number

- if a word is treated as an unsigned number,
 - the carry flag must be used to check if the result is fit into n-bit or (n+1)-bit number
 - the overflow flag is irrelevant to an unsigned number arithmetic

Flags for a signed number

- if a word is treated as an signed number,
 - the carry flag is *irrelevant* to an signed number arithmetic
 - the overflow flag must be used to check if the result is wrong or not

Detecting errors in usigned and signed arithmetic (1)

	unsigned integer arithmetic	signed integer arithmetic
CF Carry Flag	detects overflows	
	extends an <i>n-bit</i> result	
	into an $(n+1)$ -bit result	
OF Overflow Flag		detects <i>overflows</i>
		errors
		the result cannot be used

Detecting errors in usigned and signed arithmetic (2)

- unsigned integer arithmetic overflow is indicated by the carry flag
 - P + P CF=1 \rightarrow carry out the result is too large for an *n-bit* integer
 - P-P CF=1 \rightarrow borrow in the result is too small for an *n-bit* integer
- signed integer arithmetic overflow is indicated by the overflow flag
 - $P + P \rightarrow N$ OF=1 \rightarrow overflow the result is not correct
 - $N + N \rightarrow P$ OF=1 \rightarrow overflow the result is <u>not</u> correct
- P (positive), N (negative)

https://stackoverflow.com/questions/47333458/assembly-x86-64-setting-carry-flag-fe

Detecting errors in usigned and signed arithmetic (3)

- unsigned integer arithmetic *overflow* is indicated by the carry flag
 - the *overflowed n*-bit result can be extended into (n+1)-bit result by using the carry flag
- signed integer arithmetic overflow is indicated by the overflow flag
 - the overflowed n-bit result cannot be used

https://stackoverflow.com/questions/47333458/assembly-x86-64-setting-carry-flag-fe

The verb to overflow v.s. the overflow flag (1)

- Do not confuse the <u>English verb</u> to overflow with the <u>overflow flag</u> in the ALU.
- The <u>verb</u> to overflow is used casually to indicate that some math result doesn't fit in the number of bits available;
- it could be integer math, or floating-point math, or whatever.
- The overflow flag is set specifically by the ALU
 it isn't the same as the casual English verb "to overflow"

The verb to overflow v.s. the overflow flag (2)

- In English, we may say
 "the binary/integer math overflowed the number of bits available for the result, causing the carry flag to come on".
- Note how this English usage of the verb "to overflow" is not the same as saying the overflow flag is on".
- A math result can <u>overflow</u> (the <u>verb</u>) the number of bits available <u>without</u> turning on the ALU <u>overflow flag</u>

Addition of *n*-bit numbers

n	bits	addened	Α	$\{a_{n-1}, a_{n-2}, \cdots, a_1, a_0\}$
n	bits	augend	В	$\{b_{n-1},b_{n-2},\cdots,b_1,b_0\}$
(n+1)	bits	carry bits	C	$\{c_n, c_{n-1}, c_{n-2}, \cdots, c_1, c_0\}$
n	bits	sum bits	S	$\{s_{n-1}, s_{n-2}, \cdots, s_1, s_0\}$

external carry bits : c_n carry out, c_0 carry in



Computing Carry and Overflow Flags

CF (carry flag) and OF (overflow flag) computation

ADD (addition)	SUB (subtraction)
$CF = c_n$	$CF = \overline{C_n}$
$0F = c_n \bigoplus c_{n-1}$	$0F = c_n \bigoplus c_{n-1}$
a 2's complement addition $A + B = A + B + 0$	a transformed addition $A - B = A + \overline{B} + 1$
$\{c_n, s_{n-1}\} = a_{n-1} + b_{n-1} + c_{n-1}$	$\{c_n, s_{n-1}\} = a_{n-1} + \overline{b_{n-1}} + c_{n-1}$
${c_{n-1}, s_{n-2}} = a_{n-2} + b_{n-2} + c_{n-2}$	$\{c_{n-1}, s_{n-2}\} = a_{n-2} + \overline{b_{n-2}} + c_{n-2}$

https://www.csie.ntu.edu.tw/~cyy/courses/assembly/12fall/lectures/handouts/lec14_