# Ripple Carry Adder (1A)

Young W. Lim 11/8/21 Copyright (c) 2021 - 2014 Young W. Lim.

Permission is granted to copy, distribute and/or modify this document under the terms of the GNU Free Documentation License, Version 1.2 or any later version published by the Free Software Foundation; with no Invariant Sections, no Front-Cover Texts, and no Back-Cover Texts. A copy of the license is included in the section entitled "GNU Free Documentation License".

Please send corrections (or suggestions) to youngwlim@hotmail.com.

This document was produced by using OpenOffice and Octave.

Young W. Lim 11/8/21

# CMOS Level Full Adder



https://upload.wikimedia.org/wikiversity/en/1/18/ RCA.Note.H.1.20151215.pdf

# **CMOS Level Full Adder**



https://upload.wikimedia.org/wikiversity/en/1/18/ RCA.Note.H.1.20151215.pdf

# Half Adder Implementation



a find control = 0 find cont

https://people.eecs.berkeley.edu/~newton/Classes/CS150sp98/lectures/week6\_2/sld007.htm

**Ripple Carry Adder** (1D) CMOS Level

### **CMOS NAND Gate**



https://people.eecs.berkeley.edu/~newton/Classes/CS150sp98/lectures/week6\_2/sld007.htm

**Ripple Carry Adder** (1D) CMOS Level

**Ripple Carry Adder** (1D) CMOS Level

#### References

- [1] en.wikipedia.org
- [2] D.M. Harris, S. L. Harris, "Digital Design and Computer Architecture"
- [3] http://www.aoki.ecei.tohoku.ac.jp/arith/mg/algorithm.html