

Basic Layout Design Techniques (H1)

20160629

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Based on

Uyemura

Introduction to VLSI Circuits and Systems

Rabaey

Digital Integrated Circuits

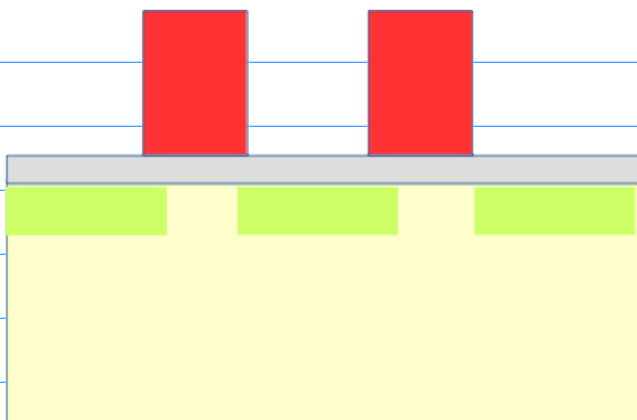
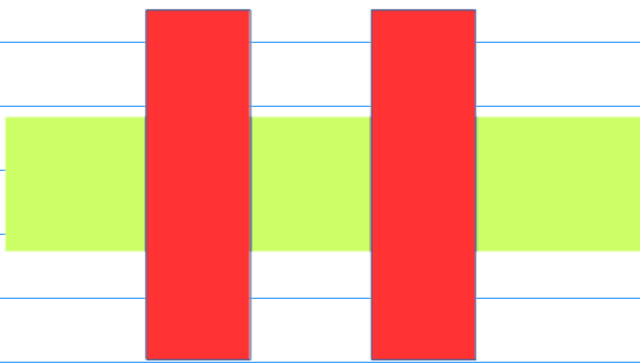
Weste & Harris

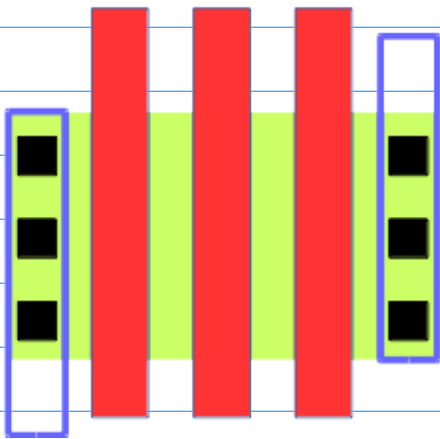
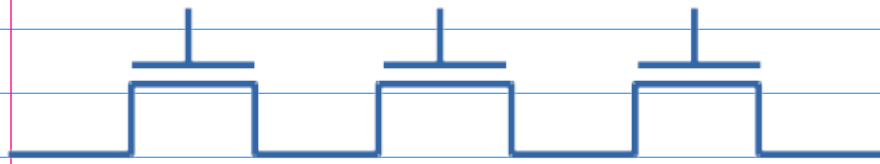
CMOS VLSI Design

Weste & Eshraghian

Principles of CMOS VLSI Design

Series MOSFET Layout





Parallel MOSFET Layout

