Dadda Tree (H1) 20170603 Copyright (c) 2015 Young W. Lim. Permission is granted to copy, distribute and/or modify this document under the terms of the GNU Free Documentation License, Version 1.2 or any later version published by the Free Software Foundation; with no Invariant Sections, no Front-Cover Texts, and no Back-Cover Texts. A copy of the license is included in the section entitled "GNU Free Documentation License".

| References |
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| Some Figures from the following sites . |
| [1] http://pages.hmc.edu/harris/cmosvlsi/4e/index.html Weste & Harris Book Site |
| [2] en.wikipedia.org |
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| Dadda multiplier is a hardware multiplier design invented by computer scientisg Dadda in 1965. It is similar to the Wallace multiplier, but it is slightly faster (for a grand sizes) and requires fewer gates (for all but the smallest operand sizes). [1] act, Dadda and Wallace multipliers have the same 3 steps: 1. Multiply (logical AND) each bit of one of the arguments, by each bit of the other yielding n^2 results. Depending on position of the multiplied bits, the wires carry different weights, for example wire of bit carrying result of a_2b_3 is 32. 2. Reduce the number of partial products to two by layers of full and half adders. 3. Group the wires in two numbers, and add them with a conventional adder. https://en.wikipedia.org/wiki/Dadda_multiplier | perand sizes) and requires fewer gates (for all but the smallest operand sizes of fact, \overline{D} adda and \overline{W} all \overline{D} and \overline{D} and \overline{D} are multipliers have the same 3 steps: 1. Multiply (logical AND) each bit of one of the arguments, by each bit of the yielding n^2 results. Depending on position of the multiplied bits, the wire different weights, for example wire of bit carrying result of a_2b_3 is 32. 2. Reduce the number of partial products to two by layers of full and half a 3. Group the wires in two numbers, and add them with a conventional added. | r (for al).[1] e other, es carry |
|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------------------------------|
| 1. Multiply (logical AND) each bit of one of the arguments, by each bit of the other yielding n^2 results. Depending on position of the multiplied bits, the wires carry different weights, for example wire of bit carrying result of a_2b_3 is 32. 2. Reduce the number of partial products to two by layers of full and half adders. 3. Group the wires in two numbers, and add them with a conventional adder. | 1. Multiply (logical AND) each bit of one of the arguments, by each bit of the yielding n^2 results. Depending on position of the multiplied bits, the wire different weights, for example wire of bit carrying result of a_2b_3 is 32. 2. Reduce the number of partial products to two by layers of full and half a 3. Group the wires in two numbers, and add them with a conventional added. | e other, es carry dders. |
| Multiply (logical AND) each bit of one of the arguments, by each bit of the other yielding n² results. Depending on position of the multiplied bits, the wires carry different weights, for example wire of bit carrying result of a₂b₃ is 32. Reduce the number of partial products to two by layers of full and half adders. Group the wires in two numbers, and add them with a conventional adder. | 1. Multiply (logical AND) each bit of one of the arguments, by each bit of the yielding n^2 results. Depending on position of the multiplied bits, the wire different weights, for example wire of bit carrying result of a_2b_3 is 32. 2. Reduce the number of partial products to two by layers of full and half a 3. Group the wires in two numbers, and add them with a conventional added. | s carry |
| 3. Group the wires in two numbers, and add them with a conventional adder. | 3. Group the wires in two numbers, and add them with a conventional adde | |
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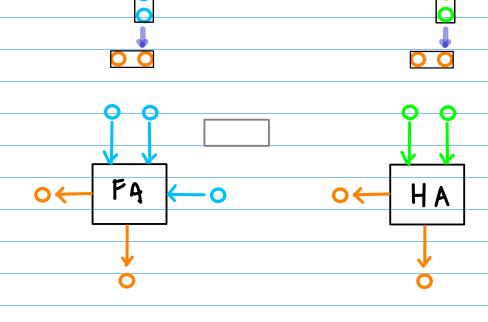
However, unlike Wallace multipliers that reduce as much as possible on each layer,

Dadda multipliers do as few reductions as possible. Because of this, Dadda multipliers

have a less expensive reduction phase, but the numbers may be a few bits longer, thus requiring slightly bigger adders.

To achieve this, the structure of the second step is governed by slightly more complex rules than in the Wallace tree. As in the Wallace tree, a new layer is added if any weight is carried by three or more wires. The reduction rules for the Dadda tree, however, are as follows:

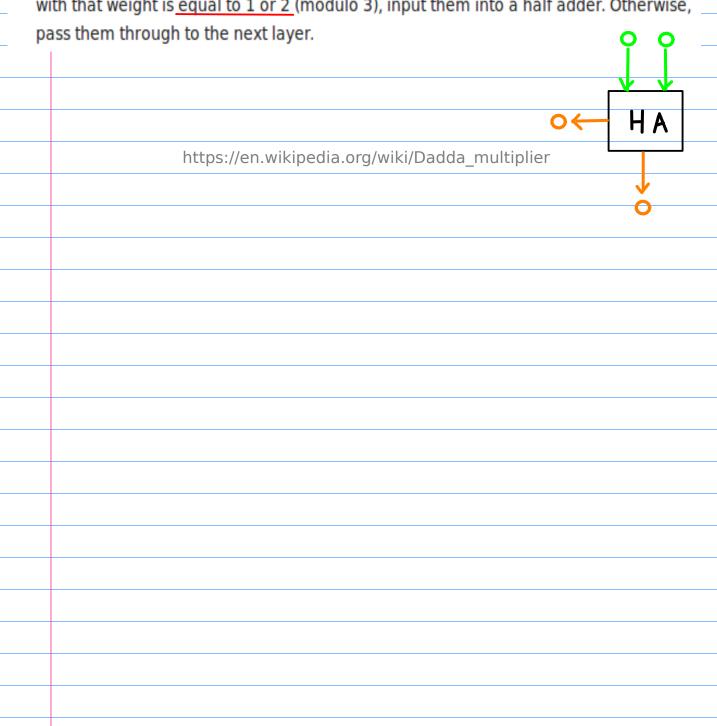
https://en.wikipedia.org/wiki/Dadda_multiplier

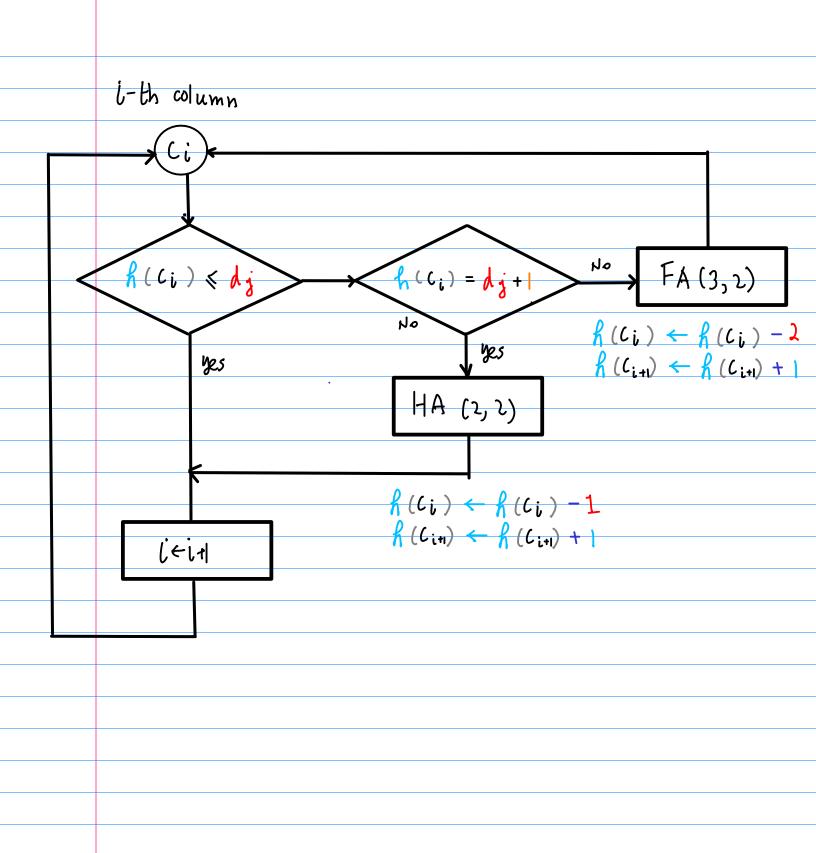


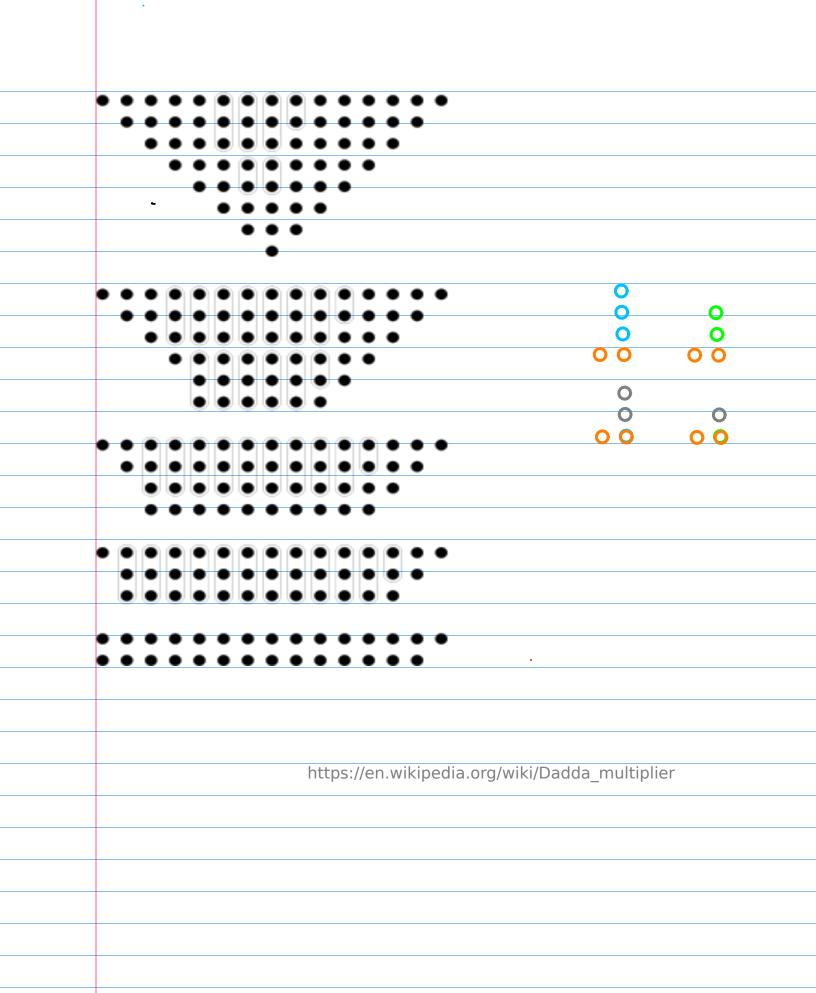
| rule | achieve this, the structure of the second step is governed by slightly more complex es than in the Wallace tree. As in the Wallace tree, a new layer is added if any weight is ried by three or more wires. The reduction rules for the Dadda tree, however, are as |
|-----------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| foll | ows: |
| (3) • | Take any three wires with the same weights and input them into a full adder. The |
| | result will be an output wire of the same weight and an output wire with a higher |
| | weight for each three input wires. |
| (ν) . | If there are two wires of the same weight left and the current number of output wires |
| | with that weight is equal to 2 (modulo 3), input them into a half adder. Otherwise, pass |
| | them through to the next layer. |
| (1) | If there is just one wire left, connect it to the next layer. |
| Thi | s step does only as many adds as necessary, so that the number of output weights |
| sta | ys close to a multiple of 3, which is the ideal number of weights when using full adders |
| as | 3:2 compressors. |
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| | https://en.wikipedia.org/wiki/Dadda multiplier |
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However, when a layer carries at most three input wires for any weight, that layer will be the last one. In this case, the Dadda tree will use half adder more aggressively (but still not as much as in a Wallace multiplier), to ensure that there are only two outputs for any weight. Then, the second rule above changes as follows:

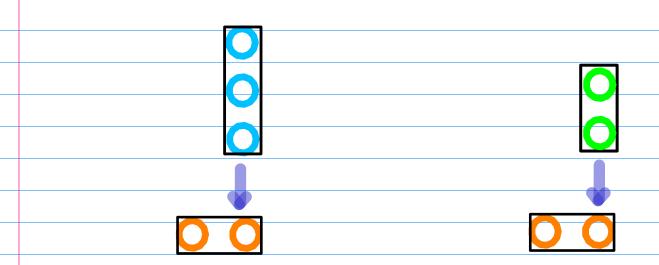
· If there are two wires of the same weight left, and the current number of output wires with that weight is equal to 1 or 2 (modulo 3), input them into a half adder. Otherwise,

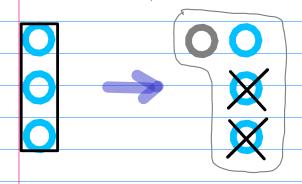


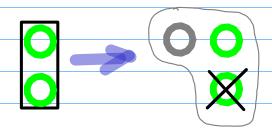




(2, 2) HA

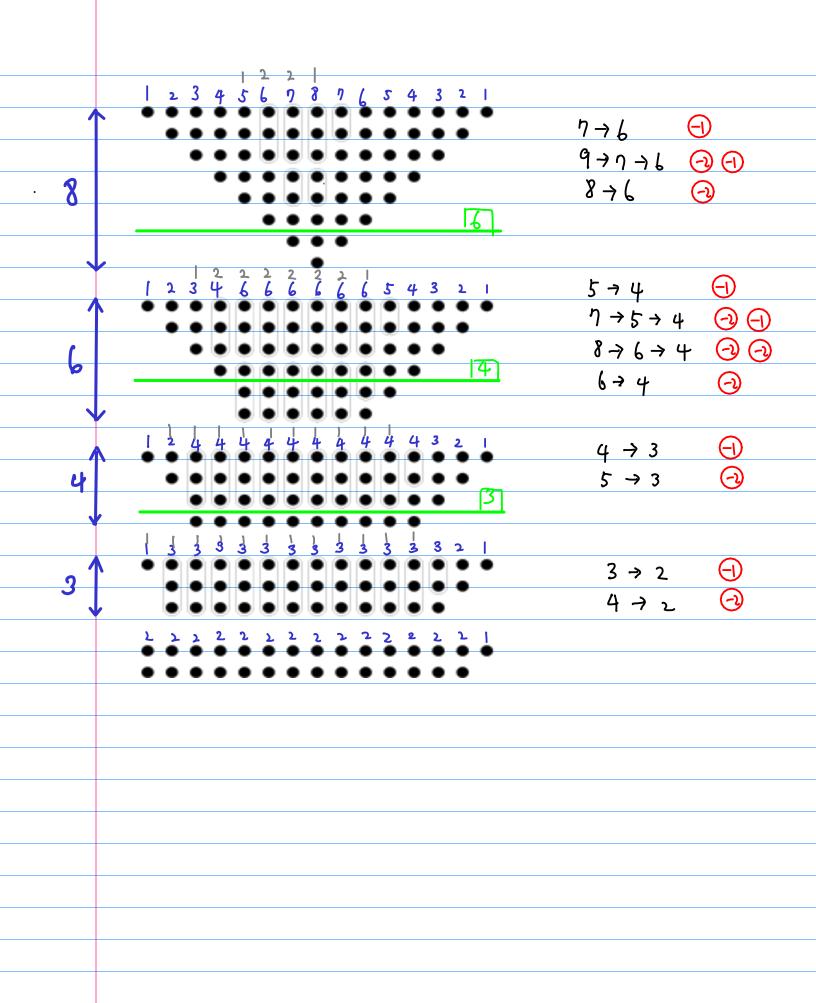


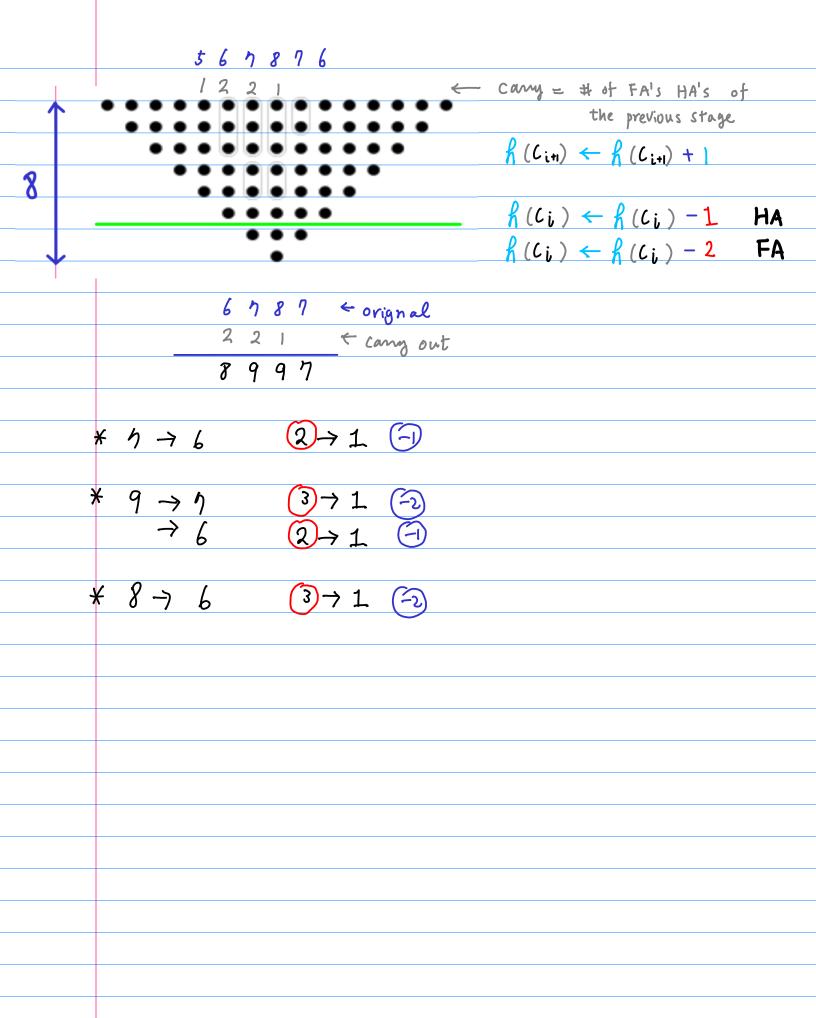


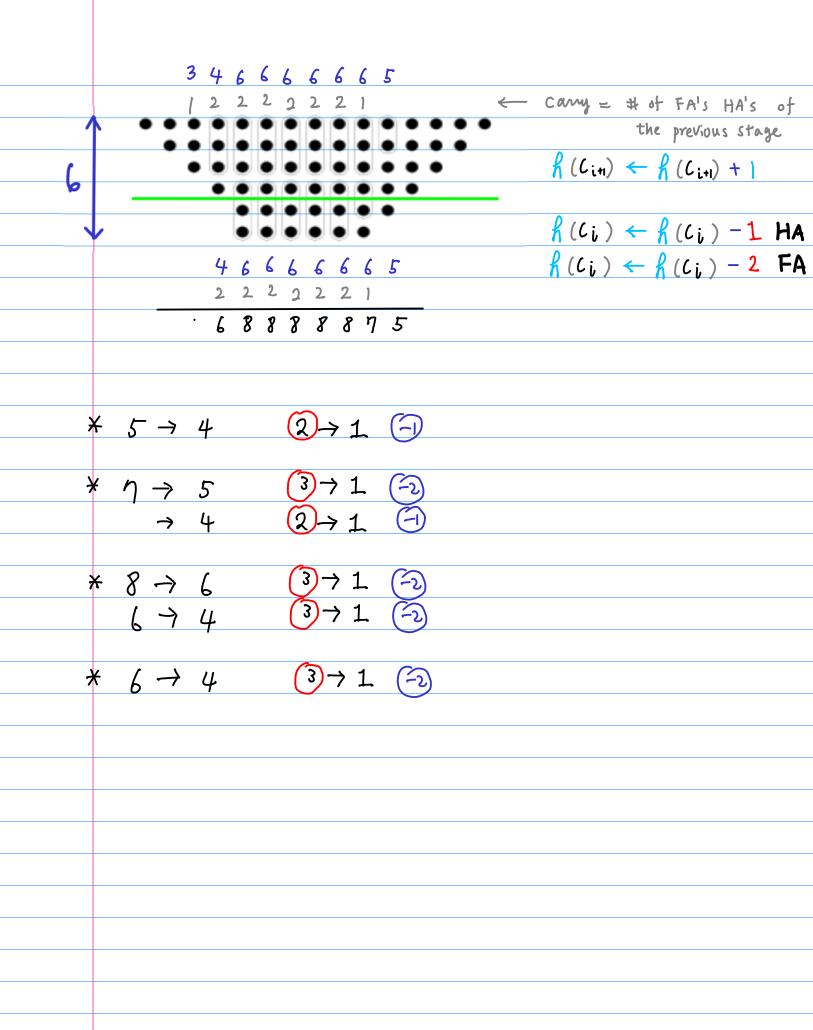


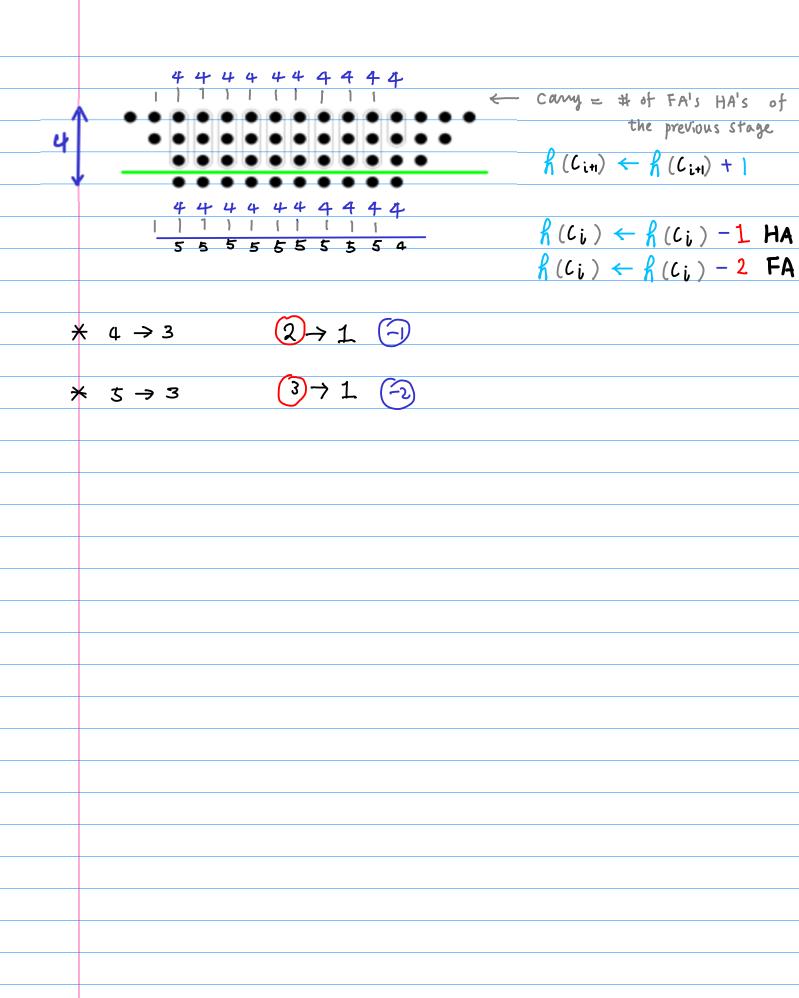
$$\frac{f(C_{i}) \leftarrow f(C_{i}) - \lambda}{f(C_{i+1}) \leftarrow f(C_{i+1}) + 1}$$

$$\frac{f(C_{i}) \leftarrow f(C_{i}) - 1}{f(C_{i+1}) \leftarrow f(C_{i+1}) + 1}$$









 \star 4 \rightarrow 2 $3\rightarrow$ 1 (2)



$$9 \rightarrow 7 \rightarrow 6$$

$$-3 \quad -1$$

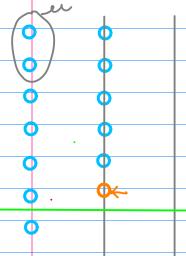
$$\uparrow_1 \quad \uparrow_1$$

$$-2 \quad -1$$

$$FA \quad HA$$



$$\begin{array}{c}
8 \rightarrow 6 \\
-3 \\
\hline
+1 \\
\hline
-2
\end{array}$$



Maximum Height Sequence dj

The progression of the reduction is controlled by a maximum-height sequence d_j , defined by:

$$d_1 = 2$$
 and $d_{j+1} = floor(1.5 * d_j)$.

This yields a sequence like so:

$$d_1=2, d_2=3, d_3=4, d_4=6, d_5=9, d_6=13, \dots$$

maximum height sequence

$$d_{1} = 2$$

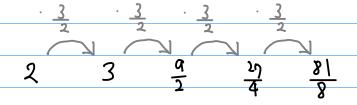
$$d_{2} = \left[\frac{3}{2} \cdot 2\right] = 3$$

$$d_{3} = \left[\frac{3}{3} \cdot 3\right] = 4$$

$$d_{4} = \left[\frac{3}{3} \cdot 4\right] = 6$$

$$d_{5} = \left[\frac{3}{2} \cdot 6\right] = 9$$

$$d_{6} = \left[\frac{3}{2} \cdot 9\right] = 13$$



4.xx 6.xx 10.xx

$$d_{1} = 2$$

$$d_{2} = \left[\frac{3}{2} \cdot 2\right] = 3$$

$$d_{3} = \left[\frac{3}{2} \cdot 3\right] = 4$$

$$d_{4} = \left[\frac{3}{2} \cdot 4\right] = 6$$

$$d_{5} = \left[\frac{3}{2} \cdot 6\right] = 9$$

$$d_{4} = \left[\frac{3}{2} \cdot 9\right] = 13$$

$$\begin{bmatrix} 13 \times \frac{2}{3} \end{bmatrix} = \begin{bmatrix} \frac{26}{3} \end{bmatrix} = 9$$

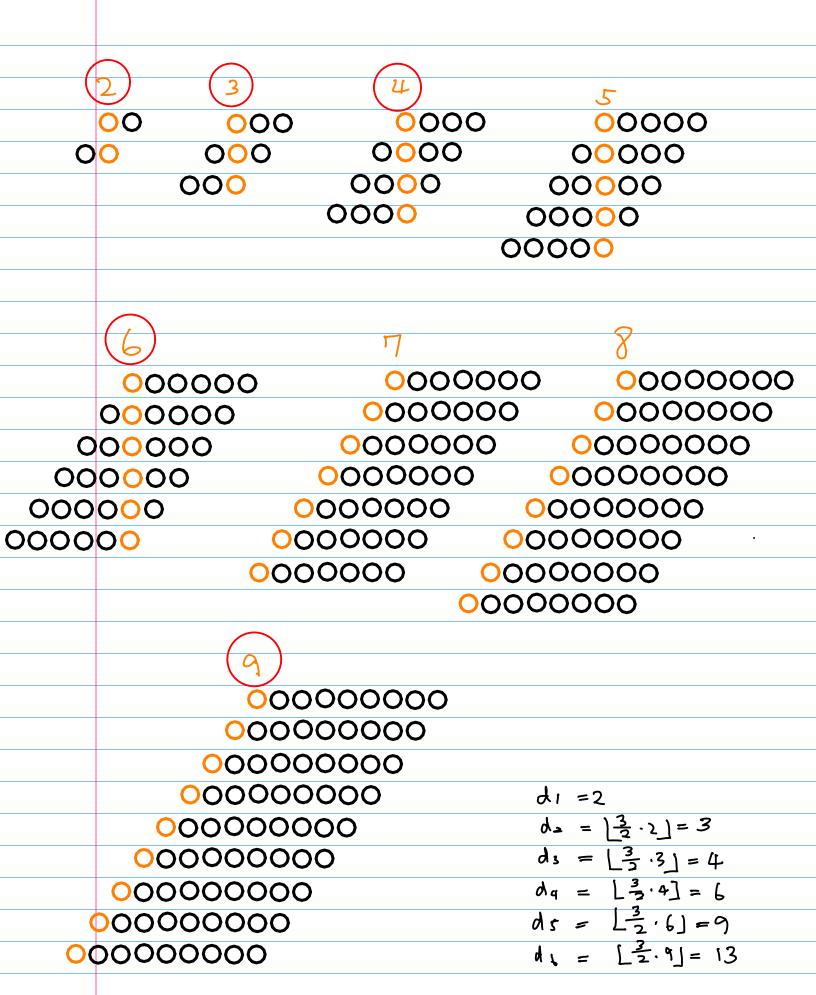
$$\begin{bmatrix} 9 \times \frac{2}{3} \end{bmatrix} = 6$$

$$\begin{bmatrix} 6 \times \frac{2}{3} \end{bmatrix} = 4$$

$$\begin{bmatrix} 4 \times \frac{2}{3} \end{bmatrix} = \begin{bmatrix} \frac{2}{3} \end{bmatrix} = 3$$

$$\begin{bmatrix} 3 \times \frac{2}{3} \end{bmatrix} = 2$$

| d, = 2 | 00 | | | | | |
|---------------------|----|----------|----------|--|---|------|
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| | | | | | | |
| d ₂ = 3. | 00 | <u>o</u> | 0 | | | |
| | 00 | | O | | | |
| | O | | | | | |
| | | <u> </u> | | | | |
| $d_3 = 4$ | 00 | Q | 0 | | | |
| | 00 | | O | | | |
| | 00 | | O | | | |
| | O | | | | | |
| | | <u>~</u> | | | | |
| d4 = b | 00 | O | 0 | | | |
| 5 7 2 | 00 | 0 | O | | | |
| | 00 | | 0 | | | |
| | 00 | | O | | • | |
| | 00 | | | | | |
| | O | | | | | |
| | | | | | | |
| d4 = 9 | 00 | Q | 0 | | | |
| 8 → 3 | 00 | | 0 | | | |
| | 00 | 0 | √ | | | |
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$$d_{1} = 2$$

$$d_{2} = \left[\frac{3}{2} \cdot 2\right] = 3$$

$$d_{3} = \left[\frac{3}{3} \cdot 3\right] = 4$$

$$d_{4} = \left[\frac{3}{3} \cdot 4\right] = 6$$

$$d_{5} = \left[\frac{3}{2} \cdot 6\right] = 9$$

$$d_{6} = \left[\frac{3}{2} \cdot 9\right] = 13$$

```
d_6=13, d_5=9, d_9=6, d_3=4, d_2=3, d_1=2
              CIS CI4 CB CI2 CII CIO C9 C8 C7 C6 C5 C4 C3 (2 C1
                                                                     C_{\eta} = 6 + |
                                                                        =d_4+1
                                                                                    d_2 = [\frac{3}{2} \cdot 2] = 3
                                                                                     d_{3} = \begin{bmatrix} \frac{3}{2} & 3 \end{bmatrix} = 4
   648
                                                                                      dq = [\frac{3}{3}, 4] = 6
                                                                                      ds = \lfloor \frac{3}{2}, 6 \rfloor = 9
                                                                                      d_1 = \left[\frac{3}{2}, 9\right] = 13
                                                                     C5= 4+
                                                                       = d_3 + |
4 6
                                                                       Cy = 3 + |
                                                                        - dz+1
                                                                      C3 = 2+
                                                                           =d_1+1
```

Maximum Height Se quence



the progression of reduction

- controlled by a maximum height sequence dj

$$d_1 = 2$$

$$d_{j+1} = \lfloor 1.5 \times d_j \rfloor$$

$$d_{1} = 2$$

$$d_{2} = \left[\frac{3}{2} \cdot 2\right] = 3$$

$$d_{3} = \left[\frac{3}{3} \cdot 3\right] = 4$$

$$d_{4} = \left[\frac{3}{3} \cdot 4\right] = 6$$

$$d_{5} = \left[\frac{3}{2} \cdot 6\right] = 9$$

$$d_{4} = \left[\frac{3}{2} \cdot 9\right] = 13$$

Largest value j dj < max (n, n2)

The initial value of j is chosen as the largest value such that $d_j < max(n_1, n_2)$, where n_1 and n_2 are the number of bits in the input multiplicand and multiplier. The larger of the two bit lengths will be the maximum height of each column of weights after the first stage of multiplication. For each stage j of the reduction, the goal of the algorithm is the reduce the height of each column so that it is less than or equal to the value of d_j .

$$8 \times 8$$
 multiplication
 $N_1 = N_2 = 8$

$$j = 4$$
 $dj = (3, 3) = 3$

$$db = 13$$

$$ds = 9$$

$$d4 = 6$$

$$d3 = 4$$

$$d2 = 3$$

$$d_{2} = \left[\frac{3}{2} \cdot 2\right] = 3$$

$$d_{3} = \left[\frac{3}{2} \cdot 3\right] = 4$$

$$d_{4} = \left[\frac{3}{2} \cdot 4\right] = 6$$

$$d_{5} = \left[\frac{3}{2} \cdot 6\right] = 9$$

$$d_1 = \begin{bmatrix} \frac{3}{2} \cdot 9 \end{bmatrix} = 13$$

Stage j → | decreasing order dj: tanget height

For each stage from j...1, reduce each column starting at the lowest-weight column, c_0 according to these rules:

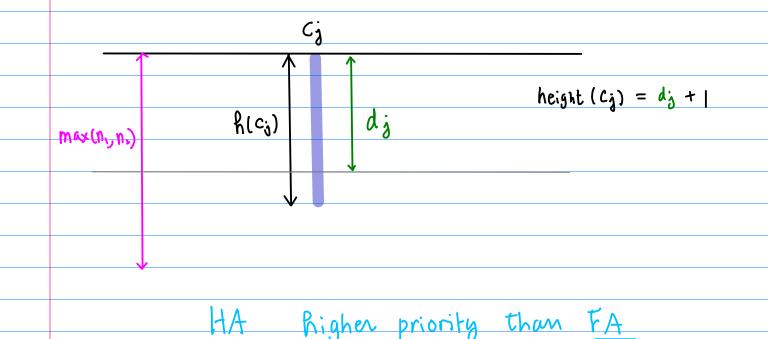
Column i increasing order

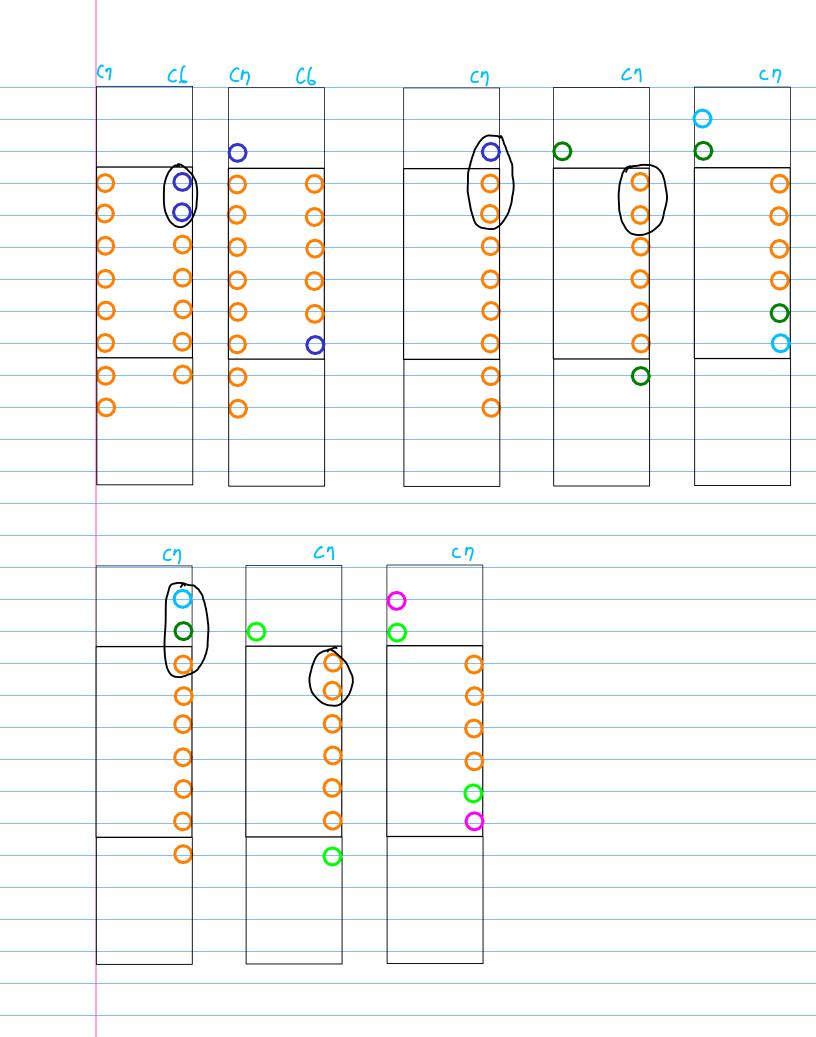
- If height(c_i) ≤ d_j the column does not require reduction, move to column c_{i+1}
- 2. If $height(c_i) = d_j + 1$ add the top two elements in a half-adder, placing the result at the bottom of the column and the carry at the top of column c_{i+1} , then move to column c_{i+1}

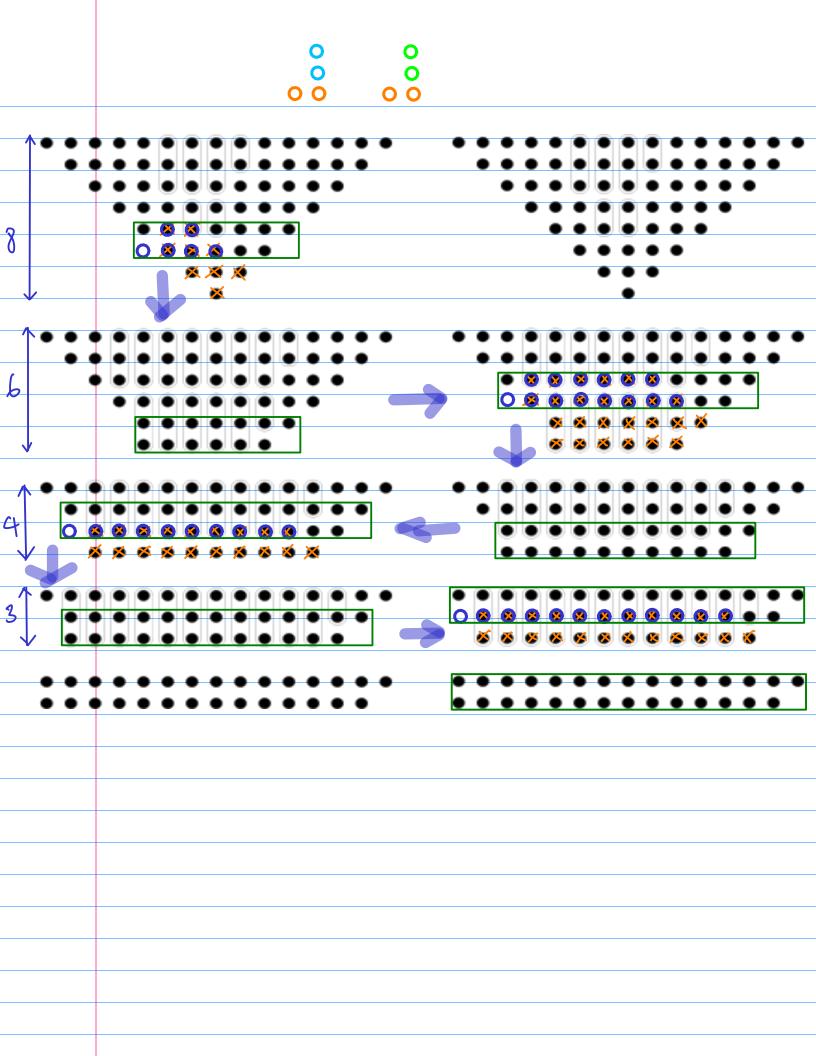
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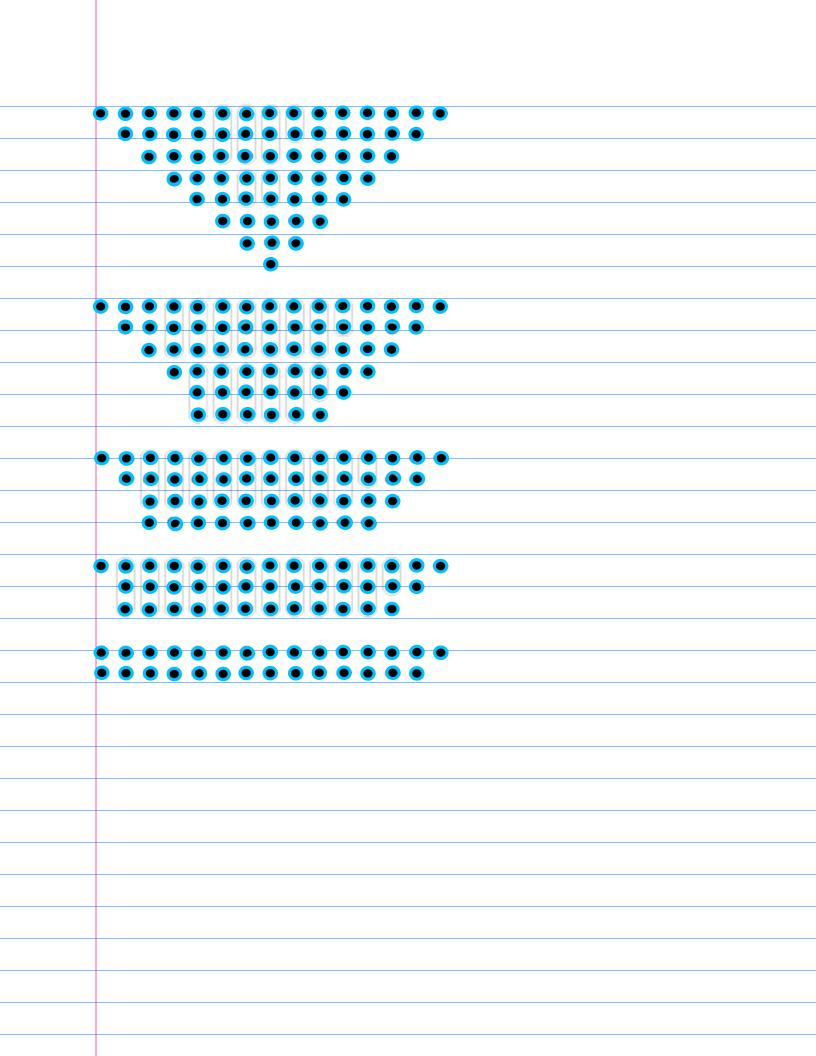
reduction

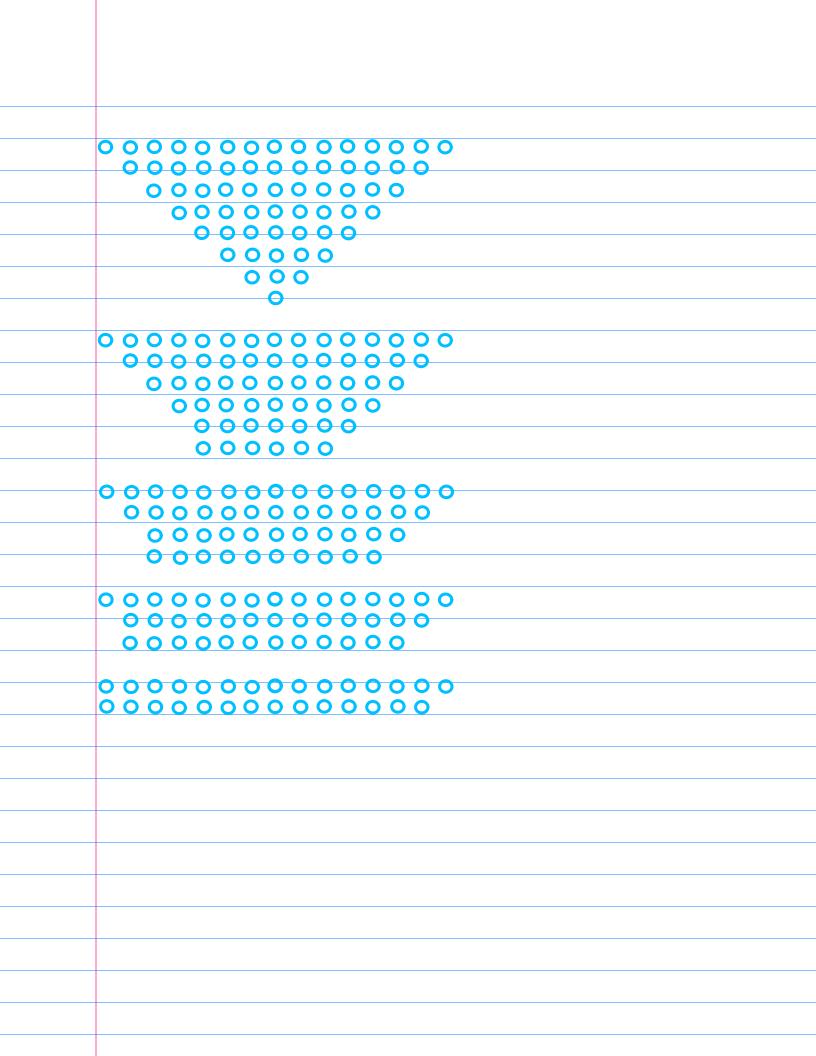
Else, add the top three elements in a full-adder, placing the result at the bottom of the column and the carry at the top of column c_{i+1}, restart c_i at step 1

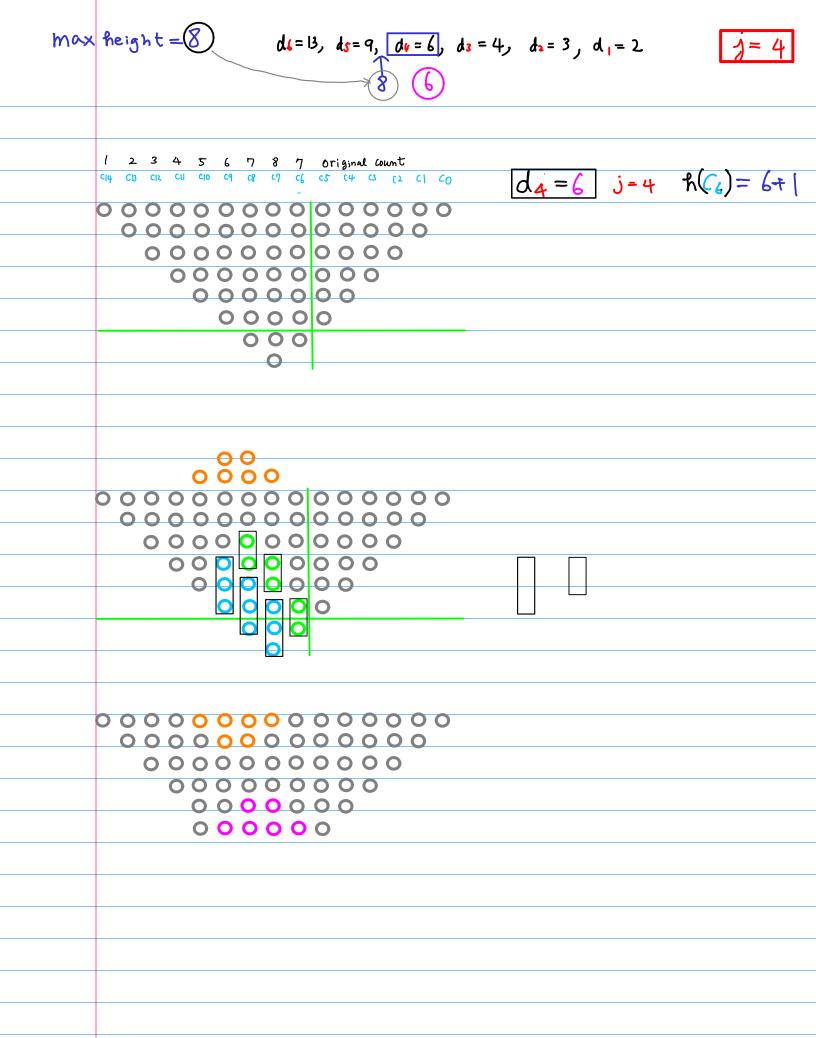


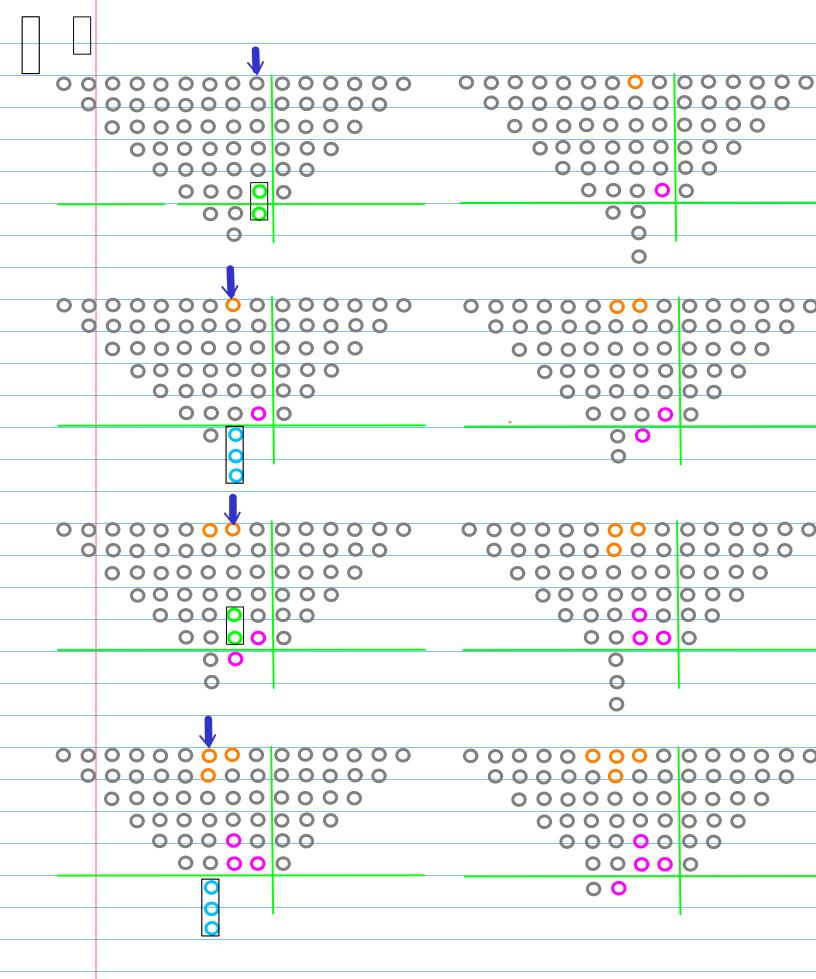


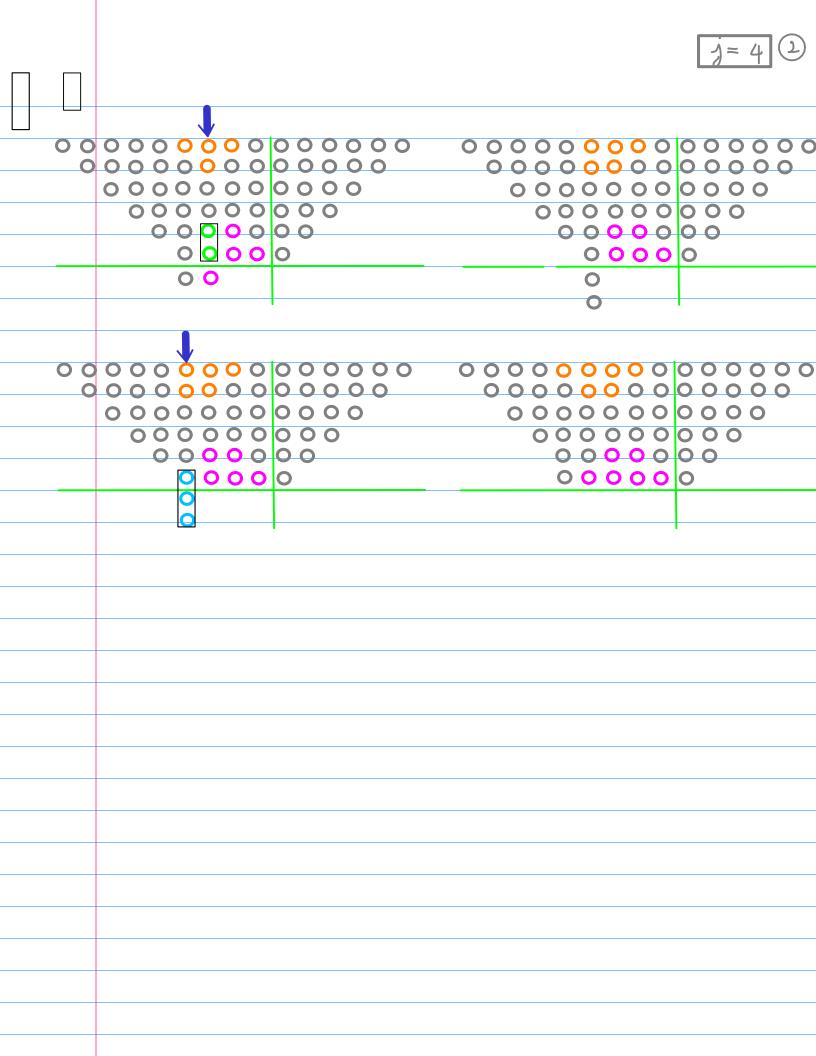






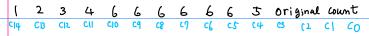




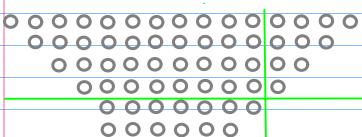


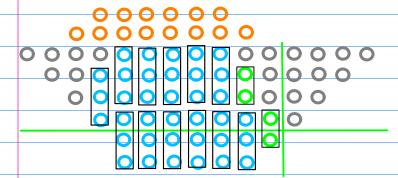
max height = 6 $d_{1}=13$, $d_{2}=9$, $d_{3}=4$, $d_{2}=3$, $d_{1}=2$

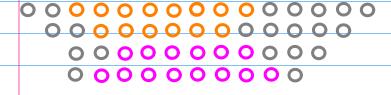




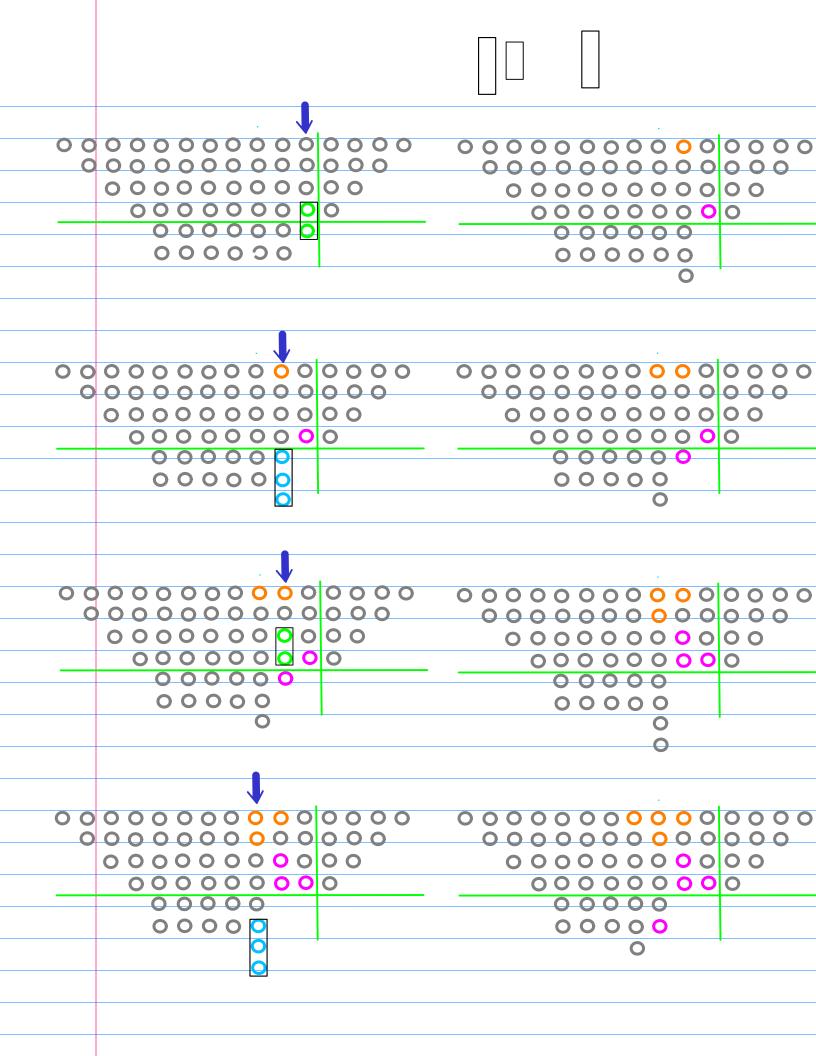
 $|d_3 = 4|$ j=3 $\Re(c_4) = 4+1$

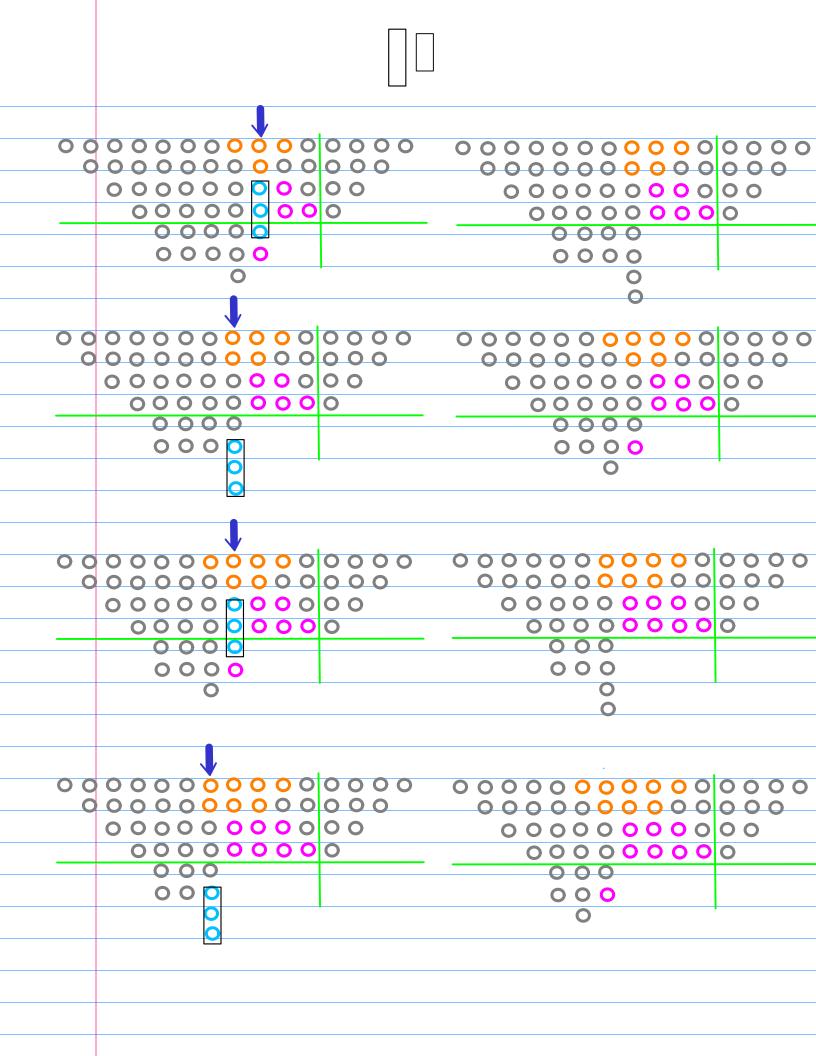


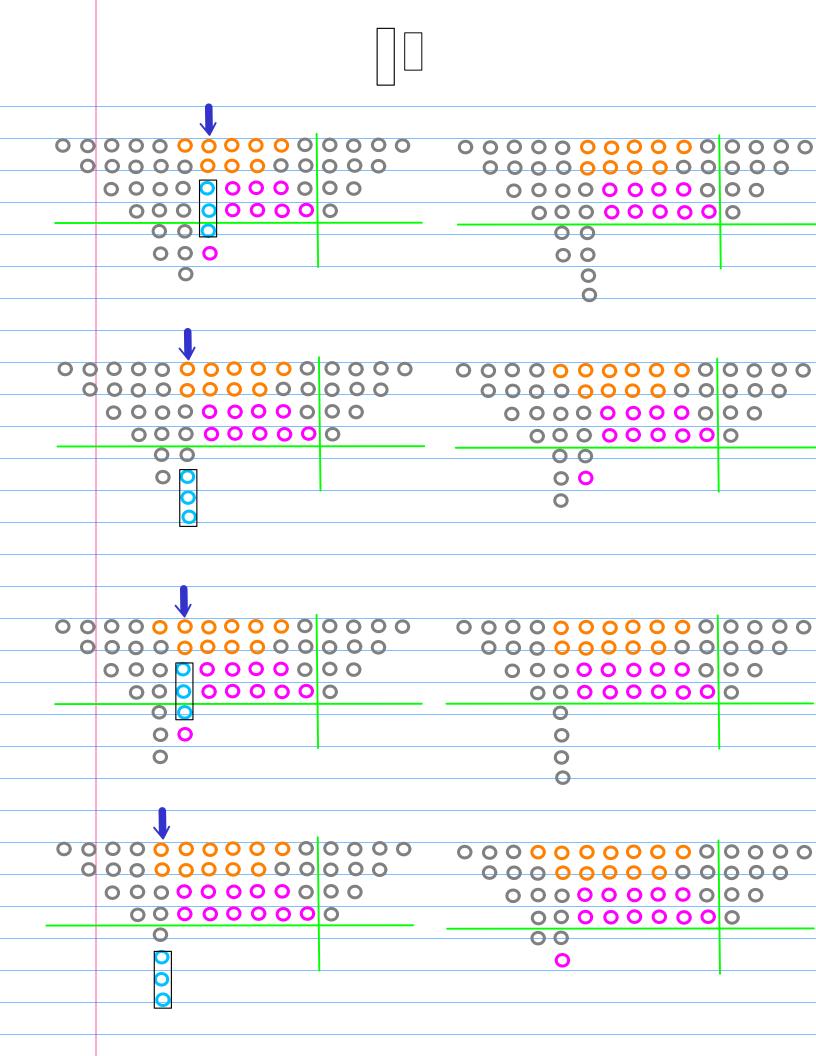


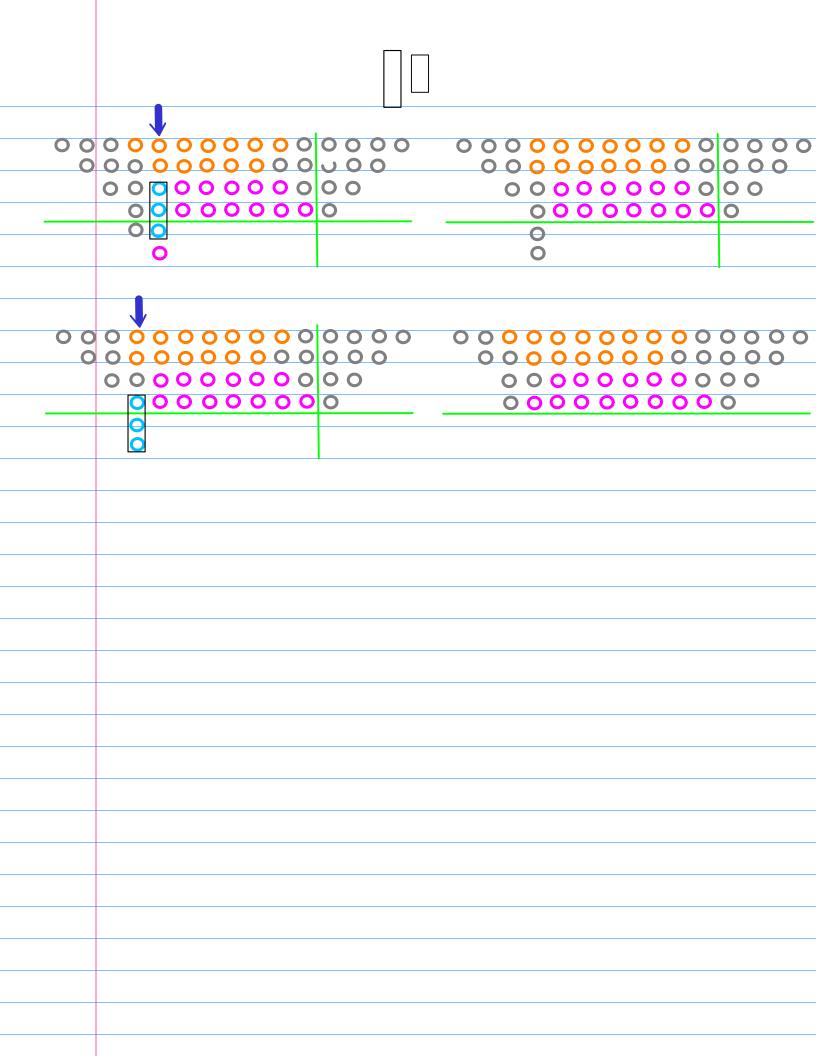








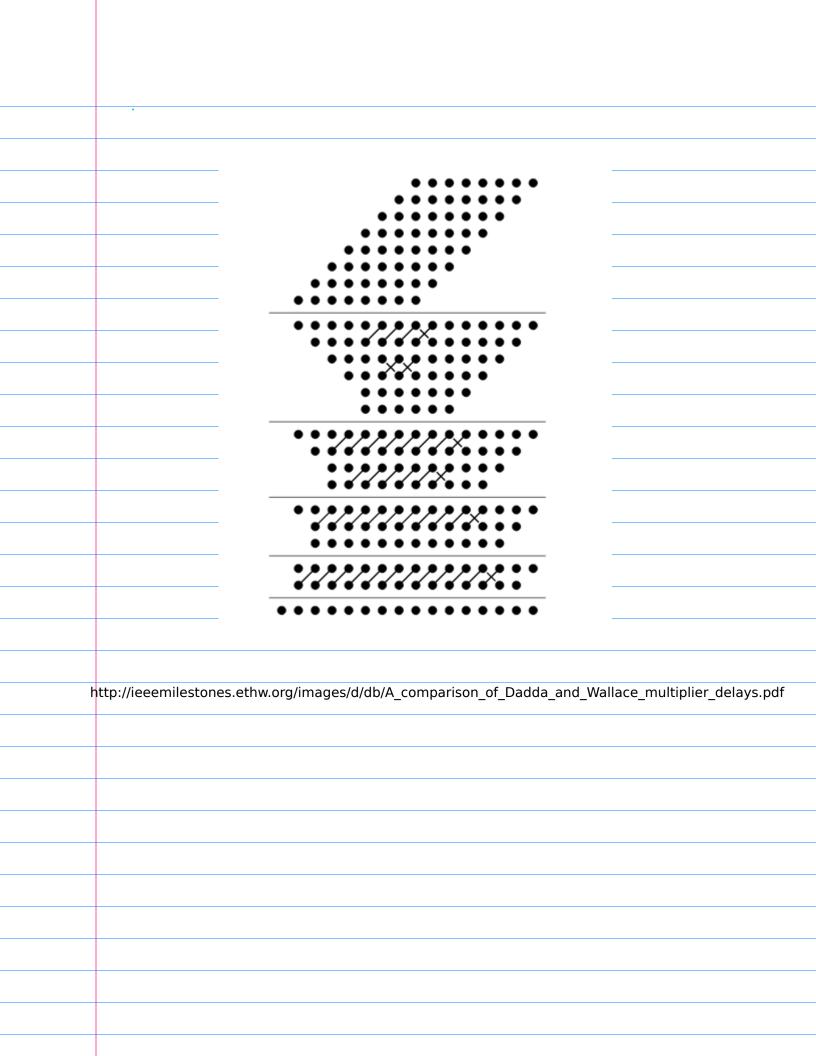




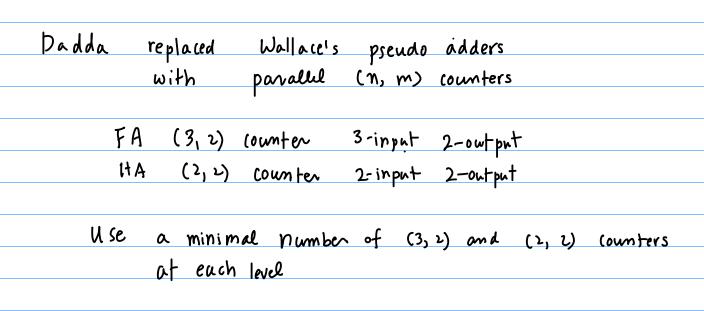
max heigh = 4 $d_s = 3$, $d_s = 9$, $d_s = 6$, $d_s = 4$, $d_s = 3$, $d_1 = 2$ j=2 h(C3) = 3 +1 1 2 4 4 4 4 4 4 4 4 4 4 4 0 0 cg c8 c7 c6 c5 c4 c3 (2 c) C0

max height = 3 $d_1 = 13$, $d_2 = 9$, $d_3 = 4$, $d_2 = 3$, $d_1 = 2$

| 1 3 | 3 3 | 3 3 | 33. | 3 3 3 | 3 3 | Original count | d1 = 1 | 2 j=1 | R(5) = 2 | + \ |
|-----|-----|-----|-----|-------|-----|----------------|--------|-------|----------|-----|
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A Comparison of Dadda and Wallace multiplier delays Townsend, Swartzlander, Abraham spie03.pdf



Dadda's Reduction Procedure

$$d_1 = 2$$

Repeat until the largest j-th stage reached
in which the original N height matrix contains
at least one column which has more than dj dots

$$d_1 = 2$$

$$d_{-} = \left\lfloor \frac{3}{2} \cdot 2 \right\rfloor = 3$$

$$d_s = \left\lfloor \frac{3}{2} \cdot 3 \right\rfloor = 4$$

$$d_4 = [-3.4] = 6$$

$$ds = \left[\frac{3}{2}, 6\right] = 9$$

$$d_1 = \left[\frac{3}{2}, 9\right] = 13$$

the languist

max height=8

$$d_{4}$$
 d_{3} , $d_{7} = 9$, $d_{6} = 6$, $d_{3} = 4$, $d_{2} = 3$, $d_{1} = 2$

Start 4-th stage
$$d_0 = 6$$

 $3-rd$ stage $d_3 = 4$ | matrix height
 $2-nd$ stage $d_2 = 3$
 $1-St$ stage $d_1 = 2$

| | Dadda's Reduction Procedure |
|----|---------------------------------------------------|
| 2 | in the j-th stage |
| | place (3, 2) and (2, 2) (ounters |
| | as required to achieve a reduced matrix |
| | from the end |
| | Reduce |
| | Donly the columns with more than dj dots |
| | @ or the columns which will have more than didots |
| | as they receive Carries from the less significant |
| | (3,2) and (2,2) (ounters |
| | |
| | |
| 3. | j = j - l |
| | repeat Step 2 |
| | until a matrix with a height of two one generated |
| | (<u>j</u> =1) |
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