

Capacitor in an AC circuit

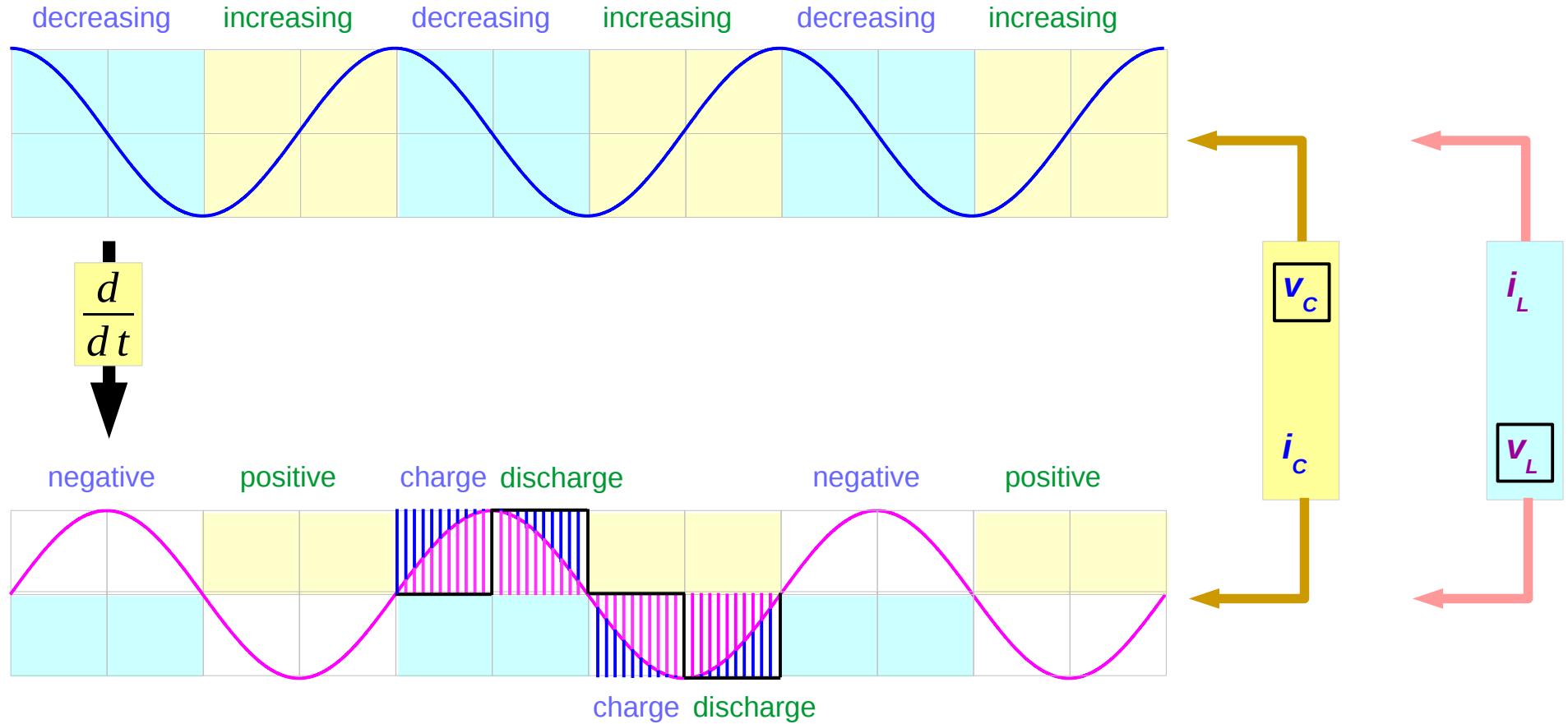
Copyright (c) 2011 – 2017 Young W. Lim.

Permission is granted to copy, distribute and/or modify this document under the terms of the GNU Free Documentation License, Version 1.2 or any later version published by the Free Software Foundation; with no Invariant Sections, no Front-Cover Texts, and no Back-Cover Texts. A copy of the license is included in the section entitled "GNU Free Documentation License".

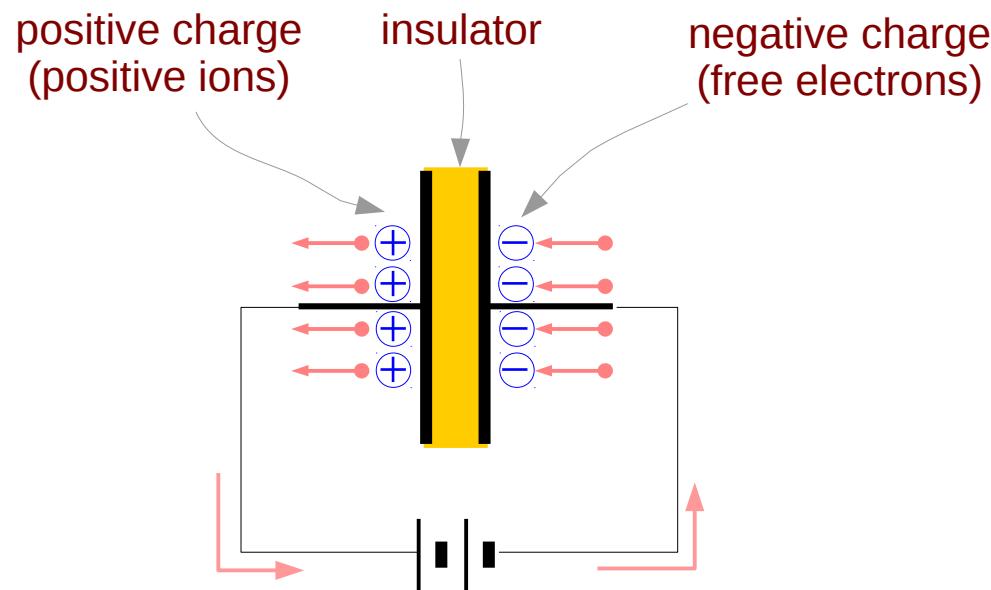
Please send corrections (or suggestions) to youngwlim@hotmail.com.

This document was produced by using OpenOffice and Octave.

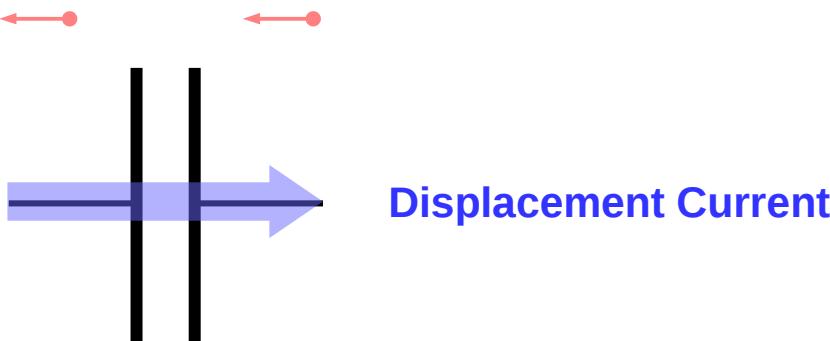
Everchanging signal pairs



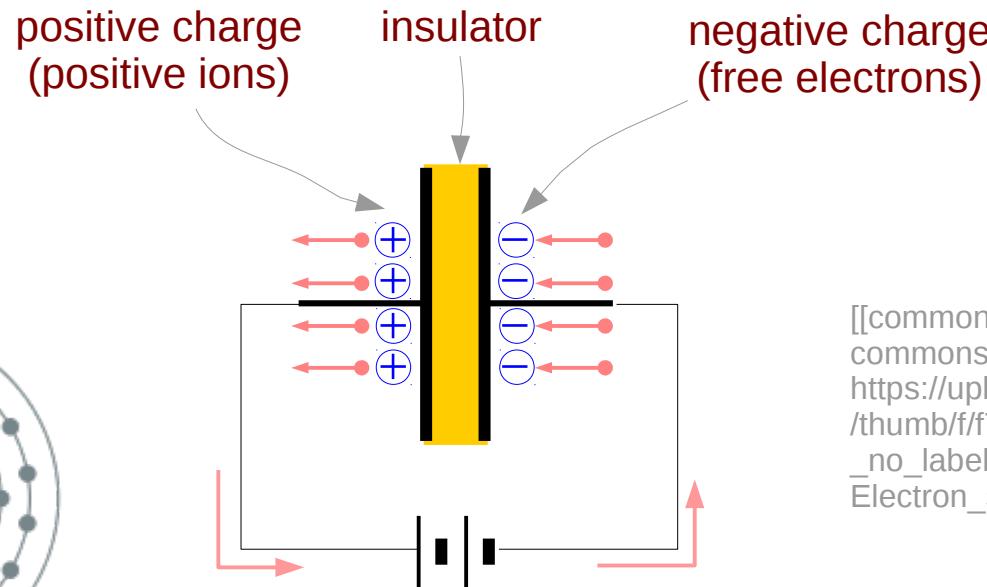
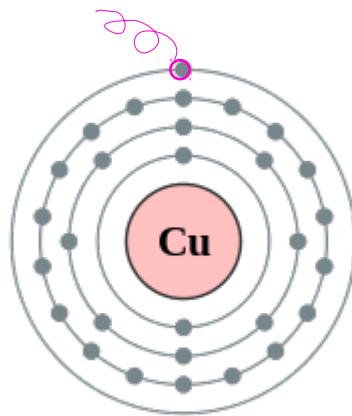
Capacitor Current



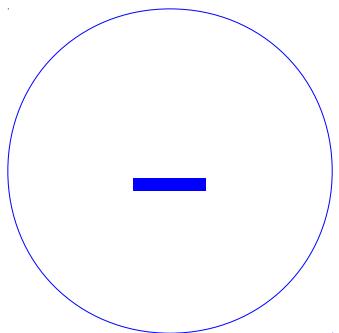
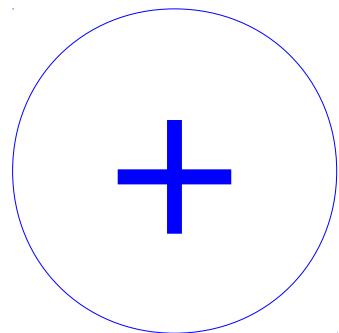
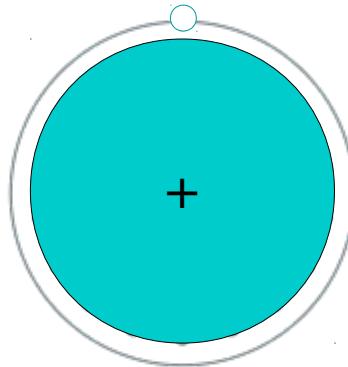
Think as electrons move to the left



Positive ions and free electrons



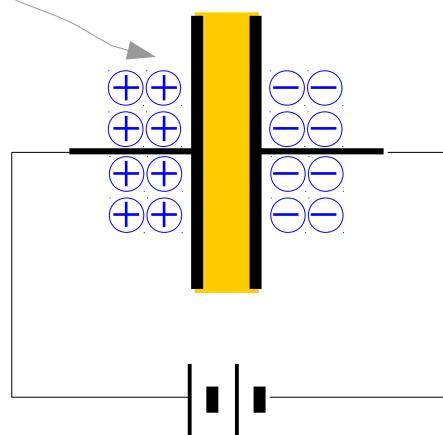
[[commons:User crap]] (original work by commons:User:Greg Robson)
https://upload.wikimedia.org/wikipedia/commons/thumb/f/f7/Electron_shell_029_Copper_-_no_label.svg/200px-Electron_shell_029_Copper_-_no_label.svg.png



Three States

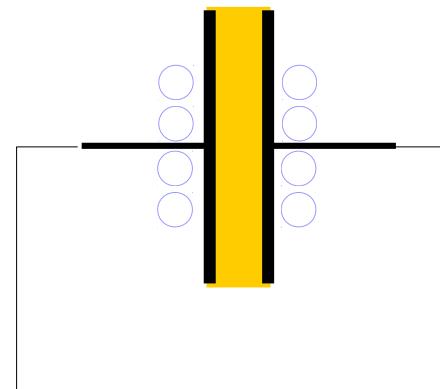
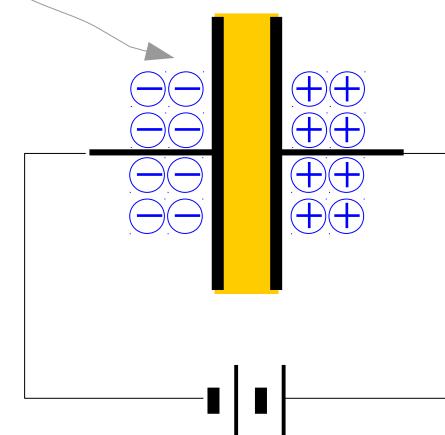
positive charge
(positive ions)

Positive Charged State



negative charge
(free electrons)

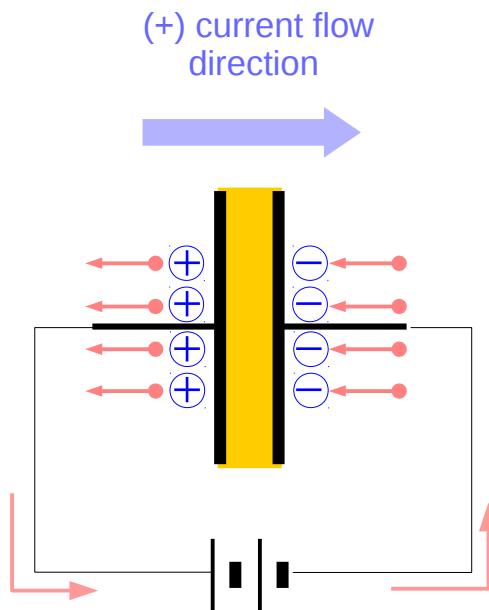
Negative Charged State



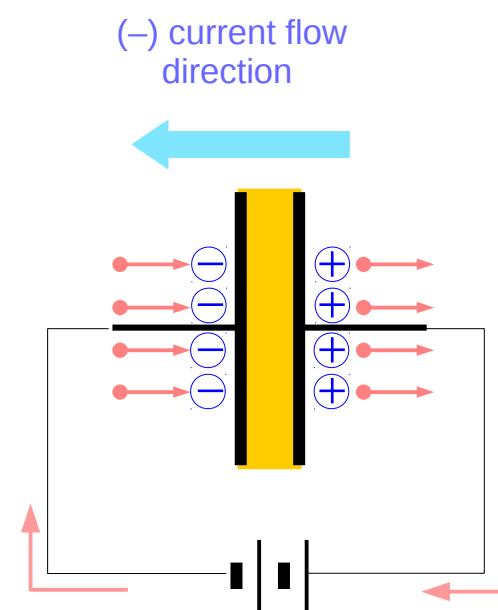
Fully Discharged State

Inter-State Current Flowing

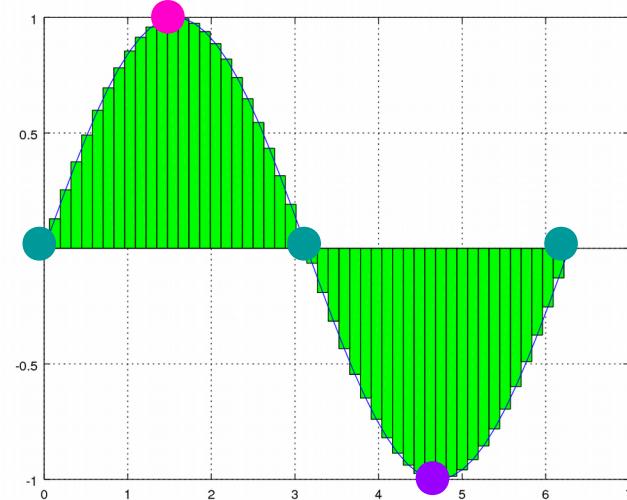
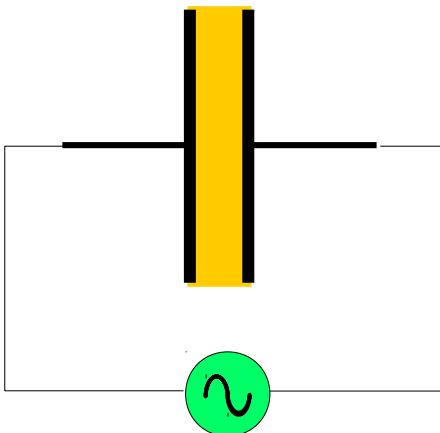
Positive Charging



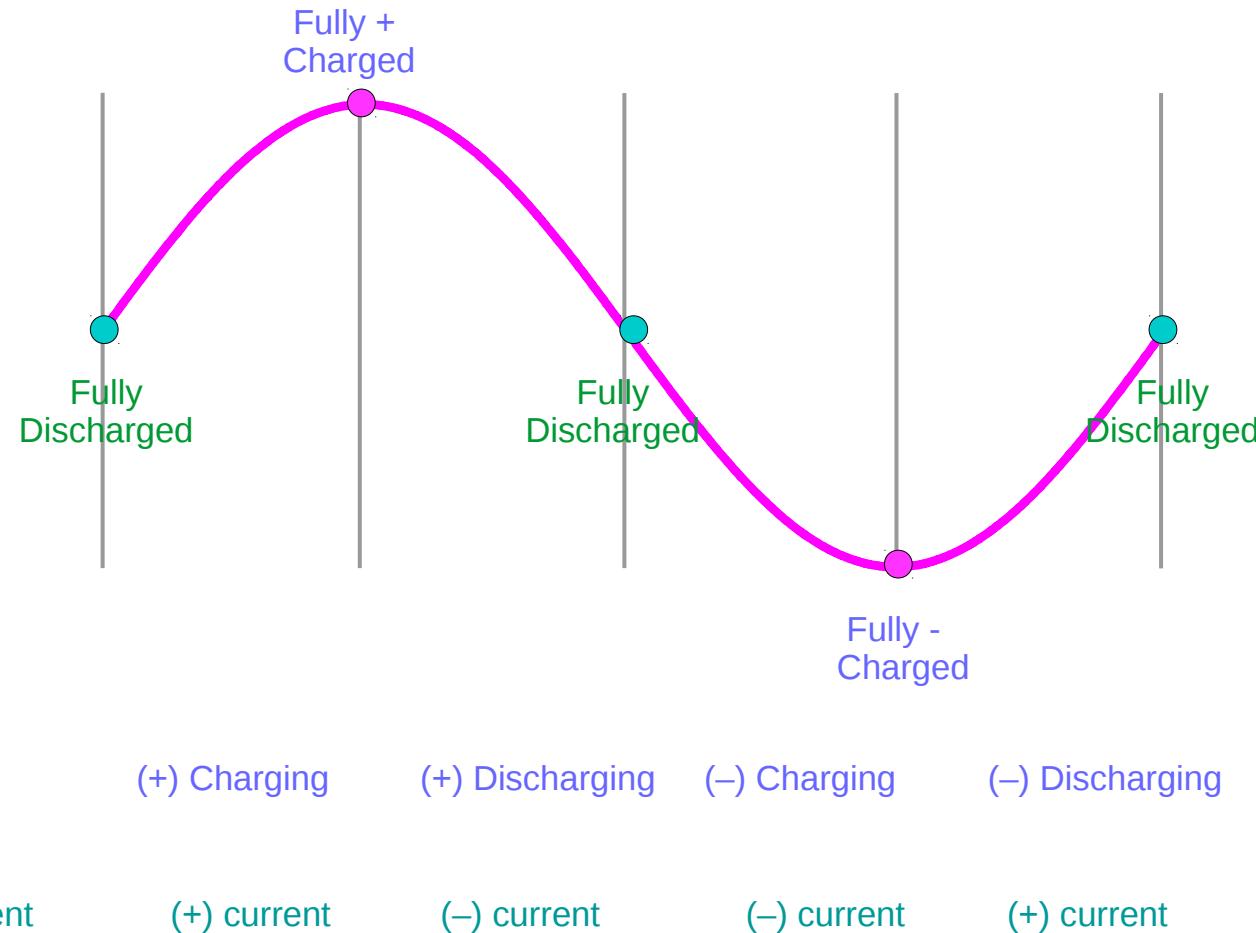
Negative Charging



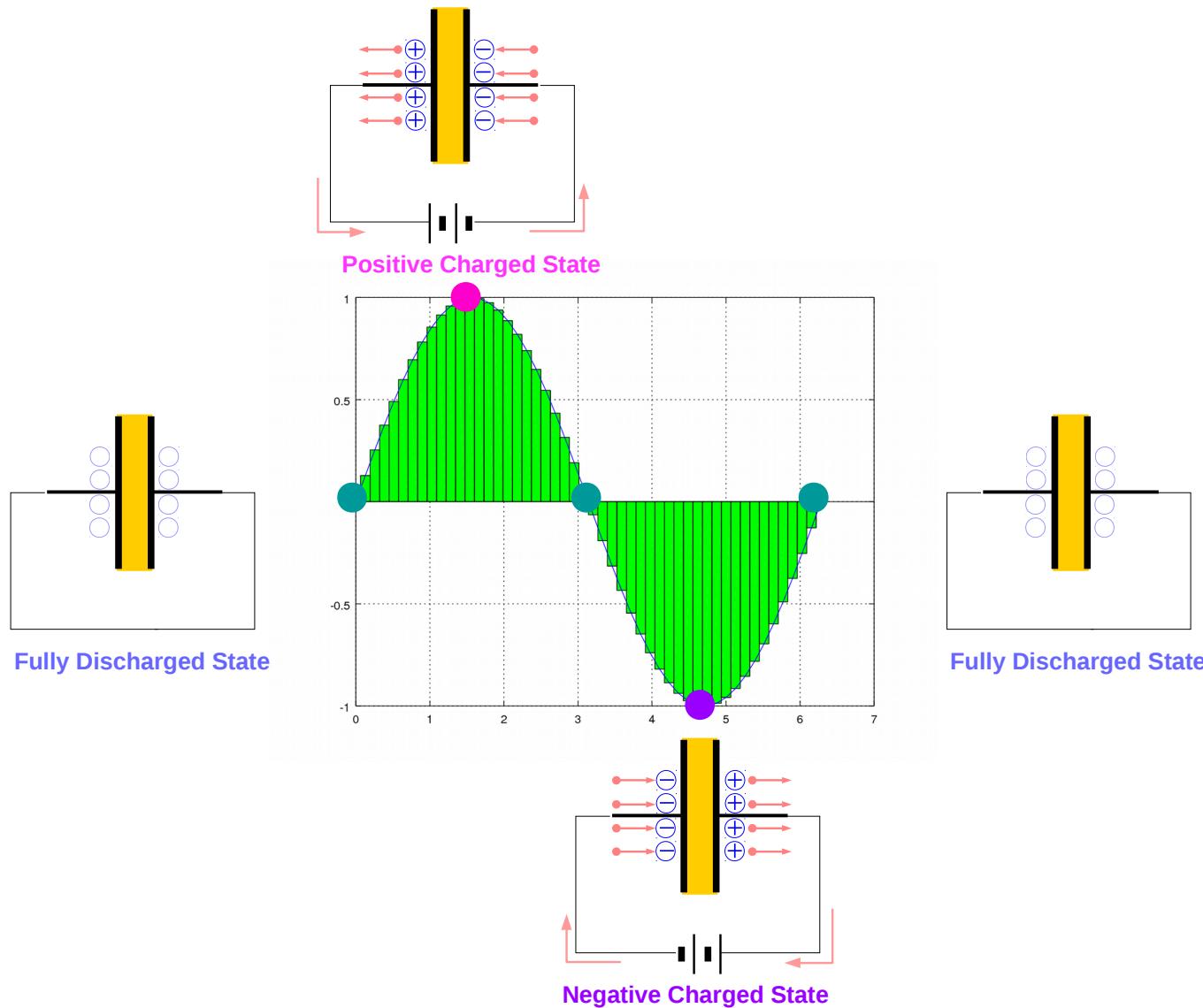
An AC Voltage Source



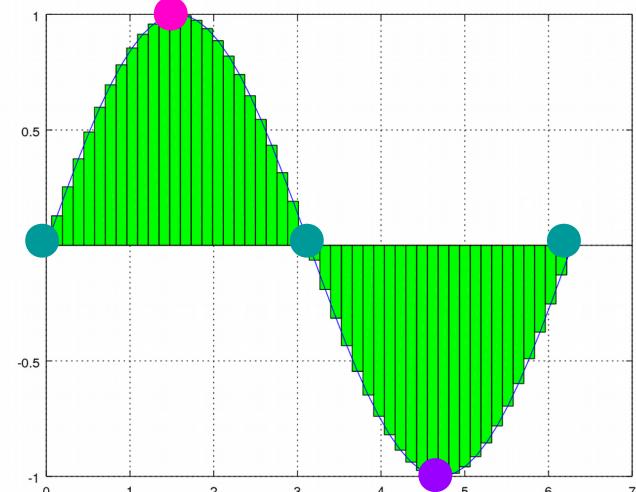
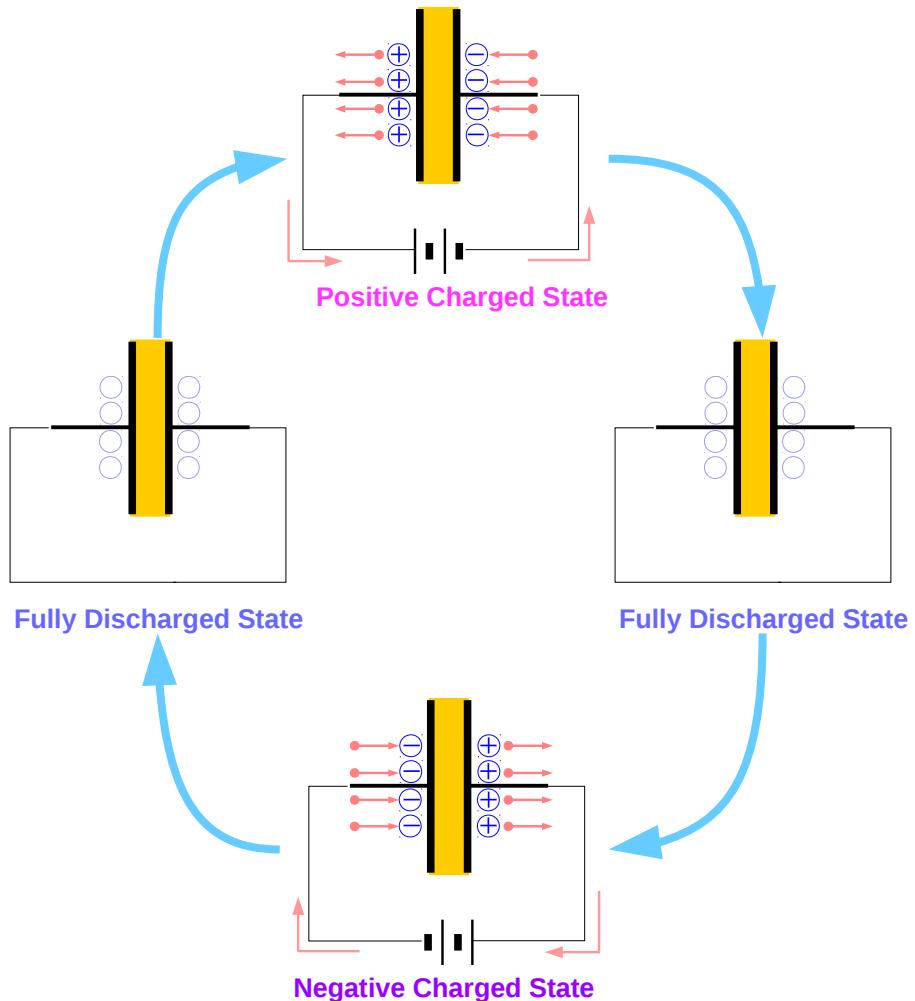
Fully Charged and Fully Discharged



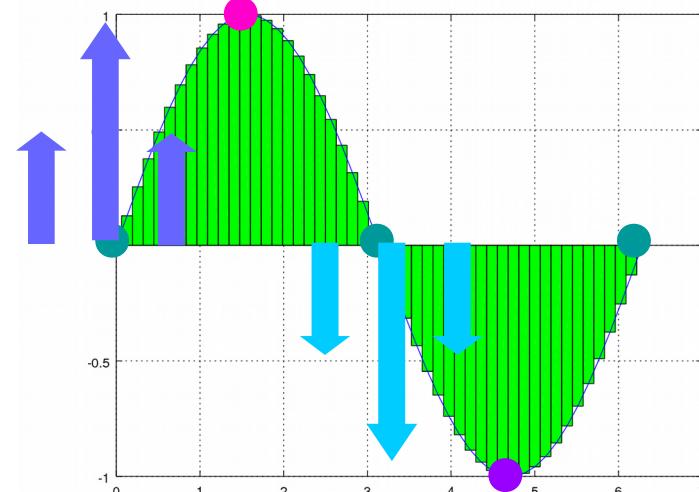
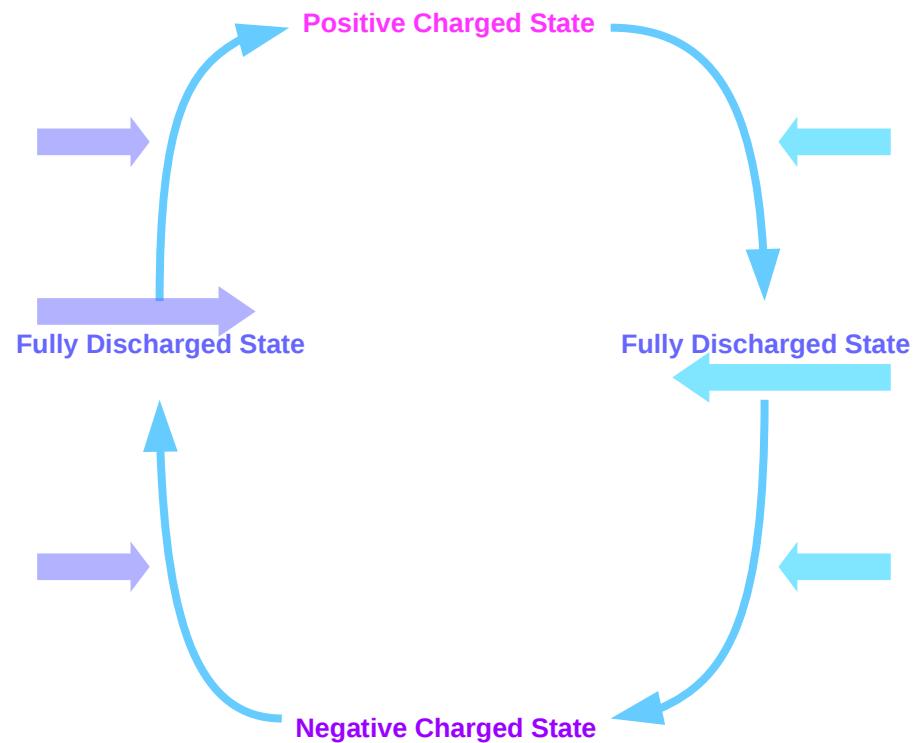
A Cycle



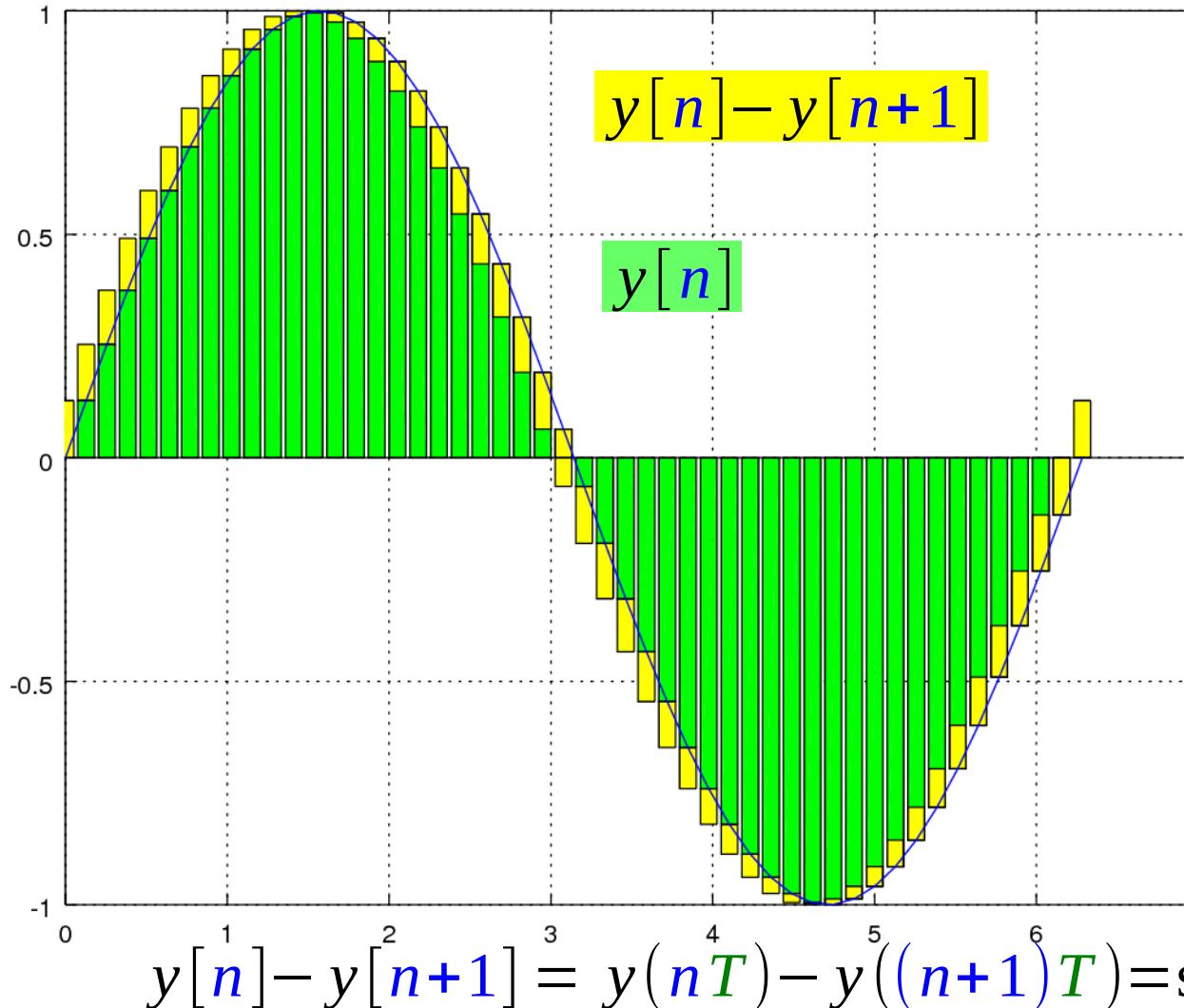
State Transition Diagram



Current Flow



Fully Charged and Fully Discharged

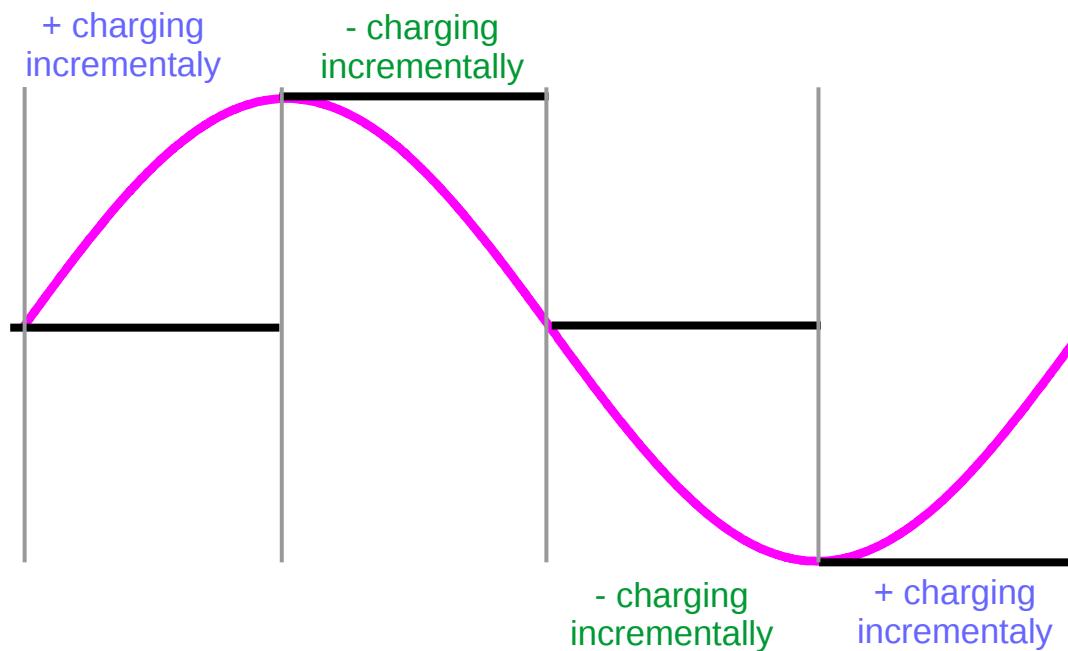


```
h = bar(t1, [y1' y2'],  
"stacked")  
set(h(1), "facecolor", "g");  
set(h(2), "facecolor", "y");  
hold on  
plot(t1, y1)  
axis([0 7 -1 1]);
```

Continuous Charging and Discharging Operations

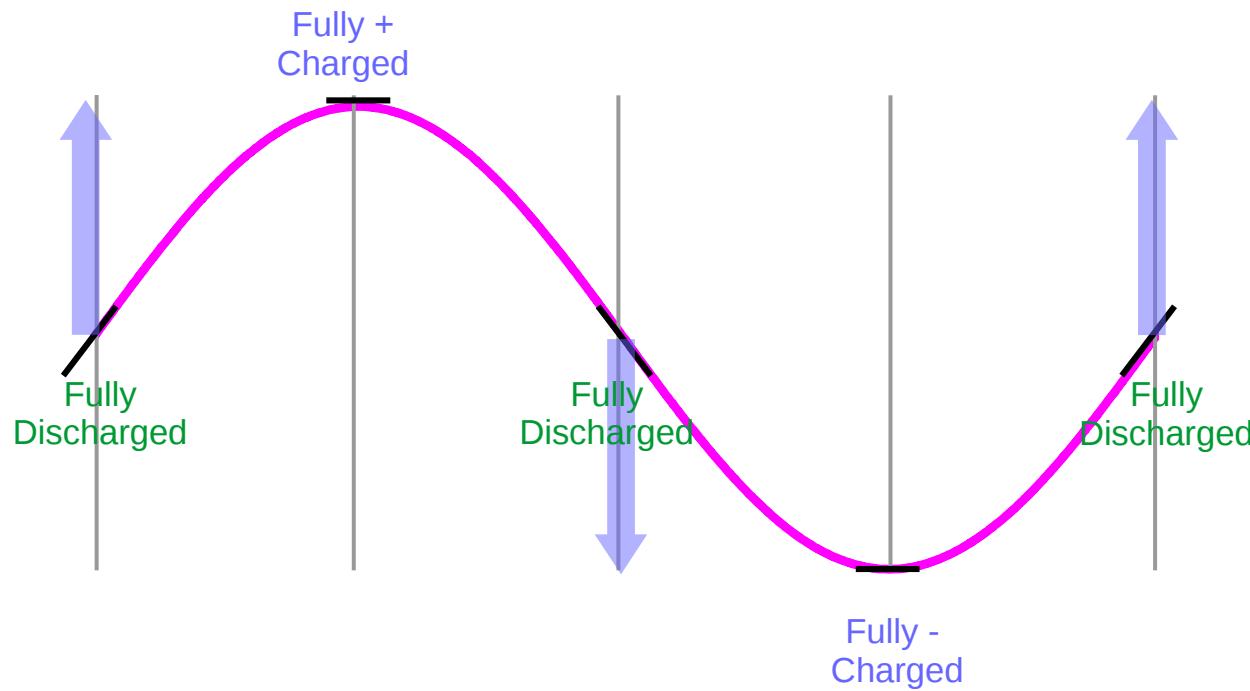
Incremental Voltage Increment $\rightarrow +$ Charging incrementally

Incremental Voltage Decrement $\rightarrow -$ Charging incrementally

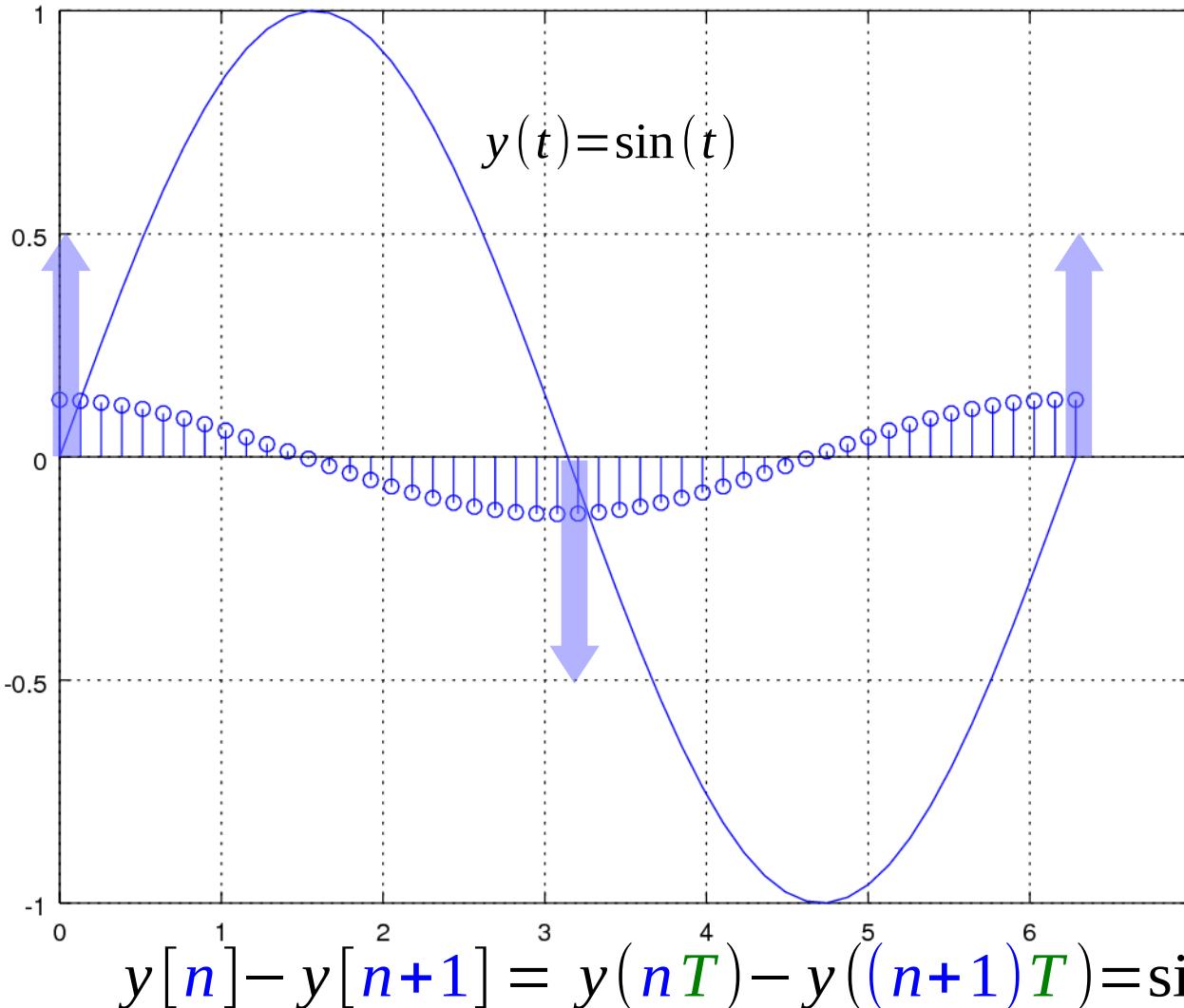


Fully Discharged : Large Current

Incremental Voltage Increment → Continuous Charging
Incremental Voltage Decrement → Continuous Discharging

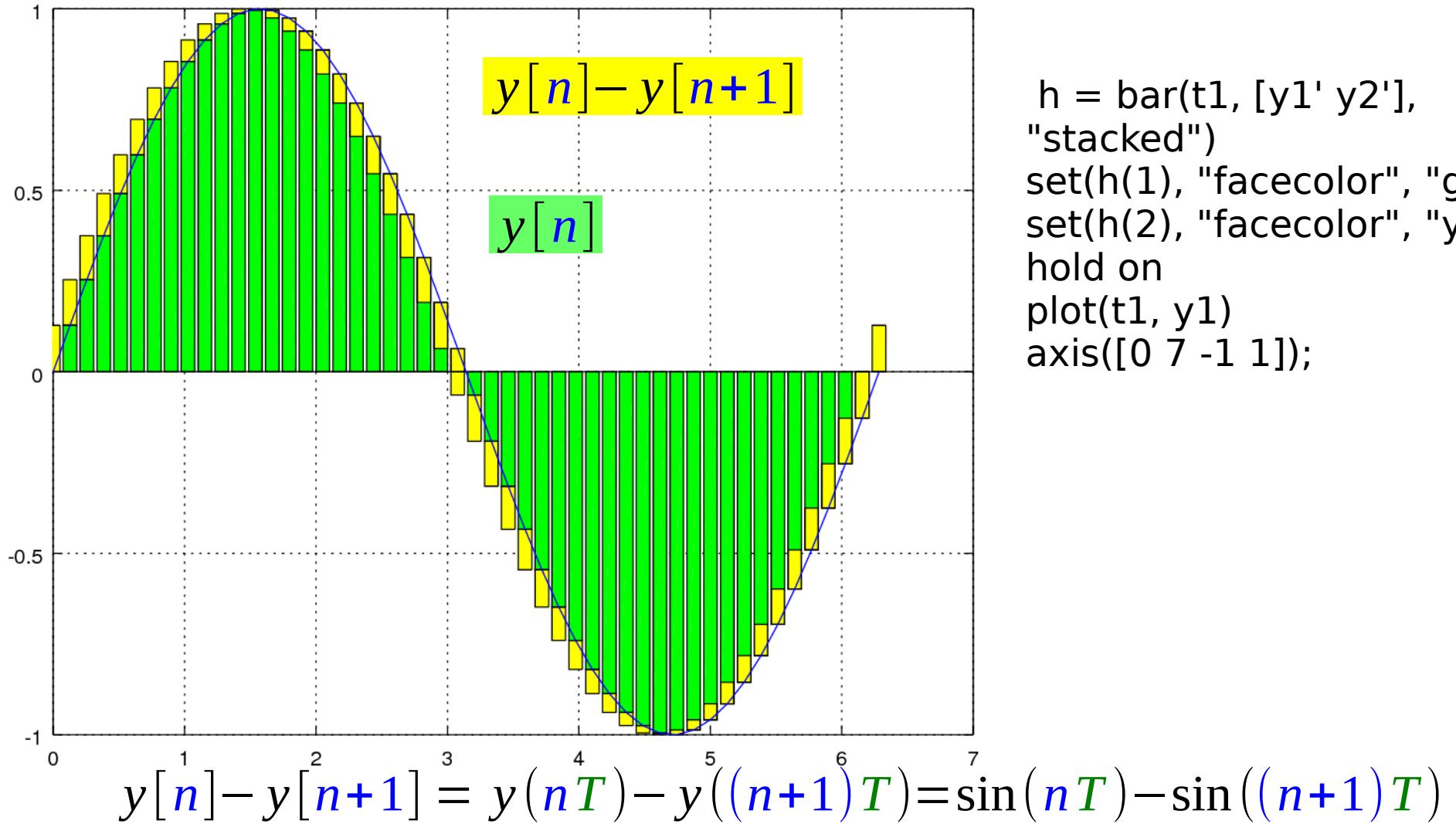


$y[n+1] - y[n]$

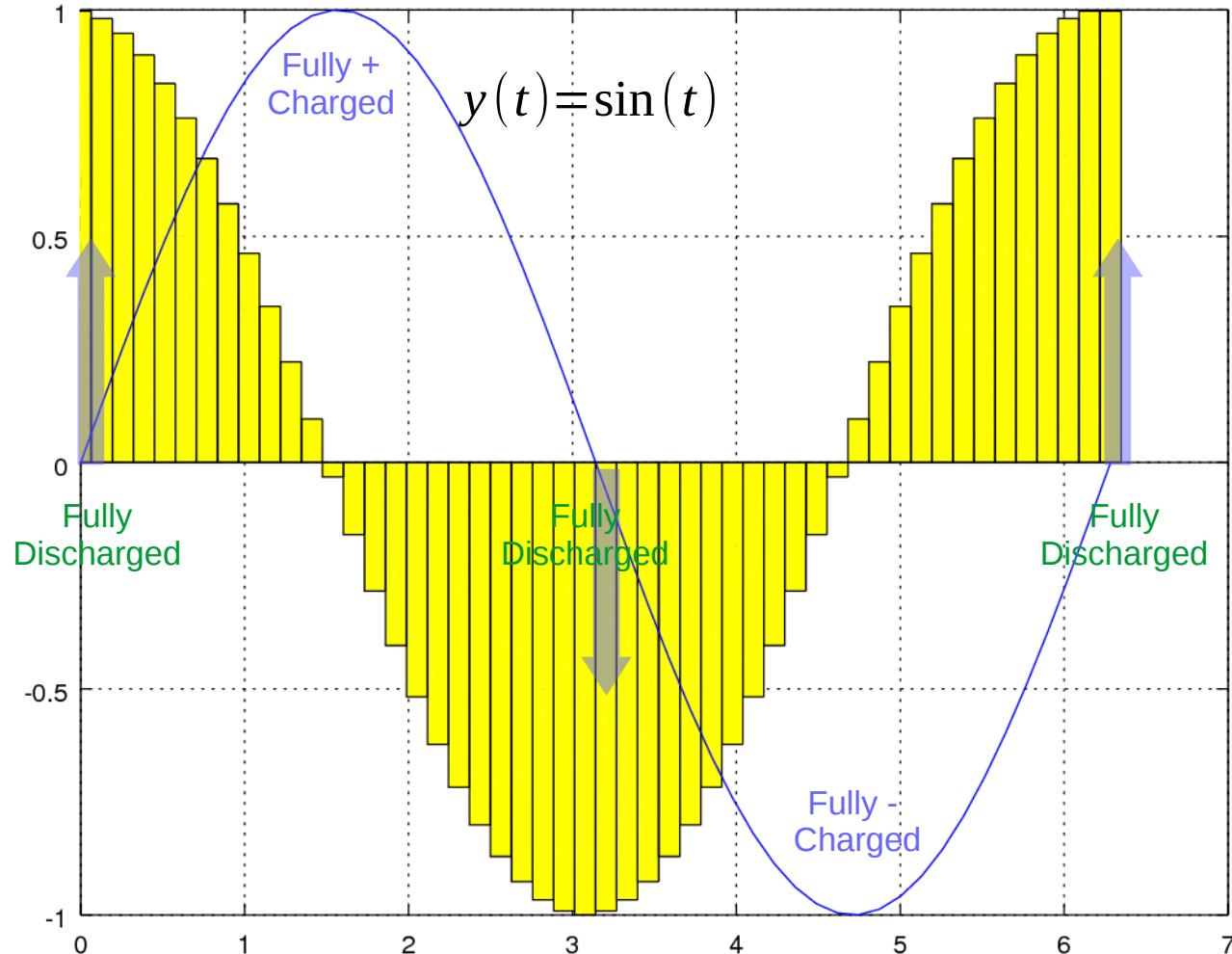


```
t = linspace(0, pi*2, 50);
t1 = t;
t2 = t + t(2);
y1 = sin(t1);
y2 = sin(t2) - sin(t1);
stem(t1, y2)
hold on
plot(t1, y1)
```

Fully Charged and Fully Discharged



Fully Charged and Fully Discharged

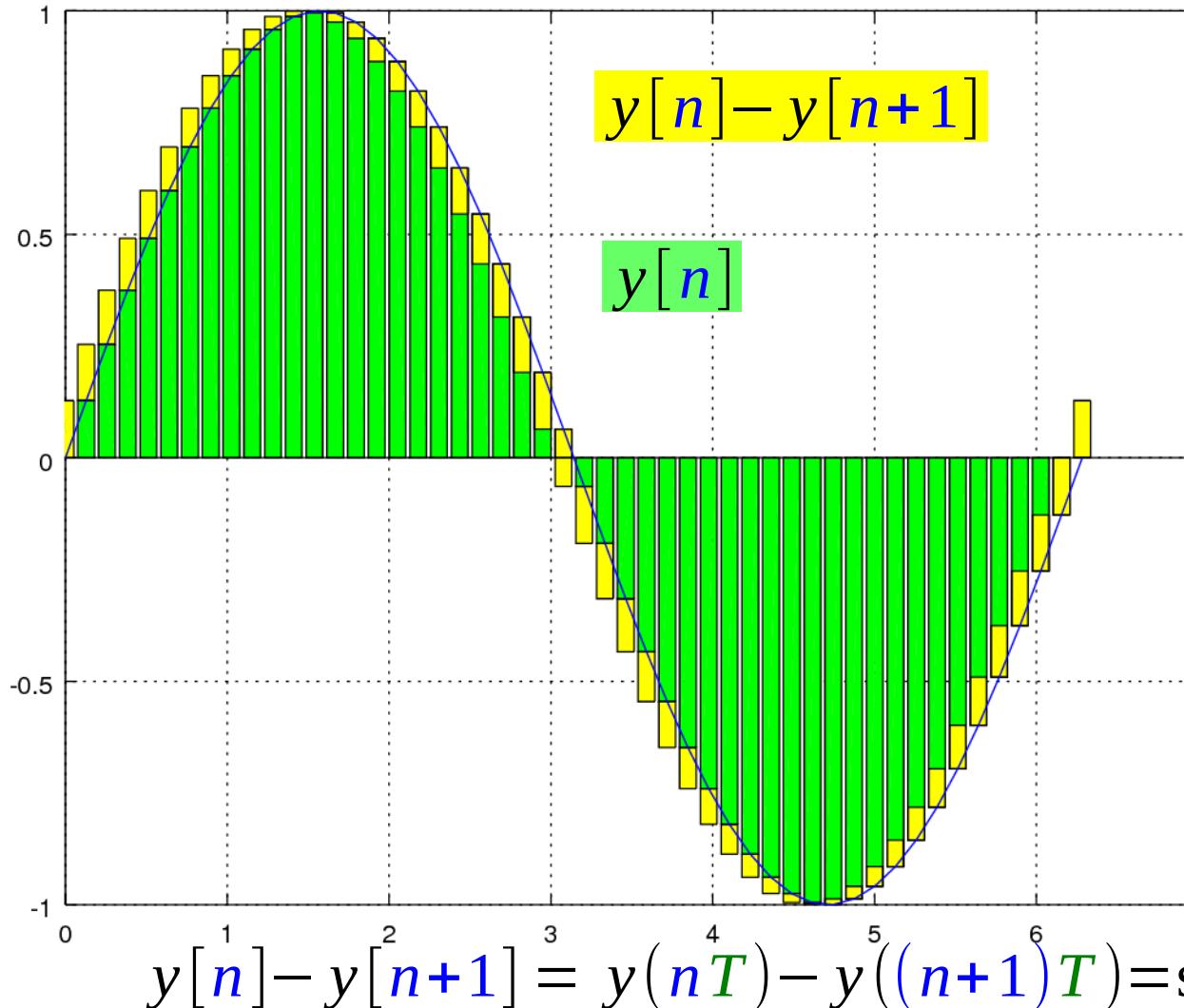


```
h = bar(t1, y2/t(2), "hist")
set(h(1), "facecolor", "y");
hold on
plot(t1, y1)
axis([0 7 -1 1]);
```

$$\frac{y[n] - y[n+1]}{T}$$

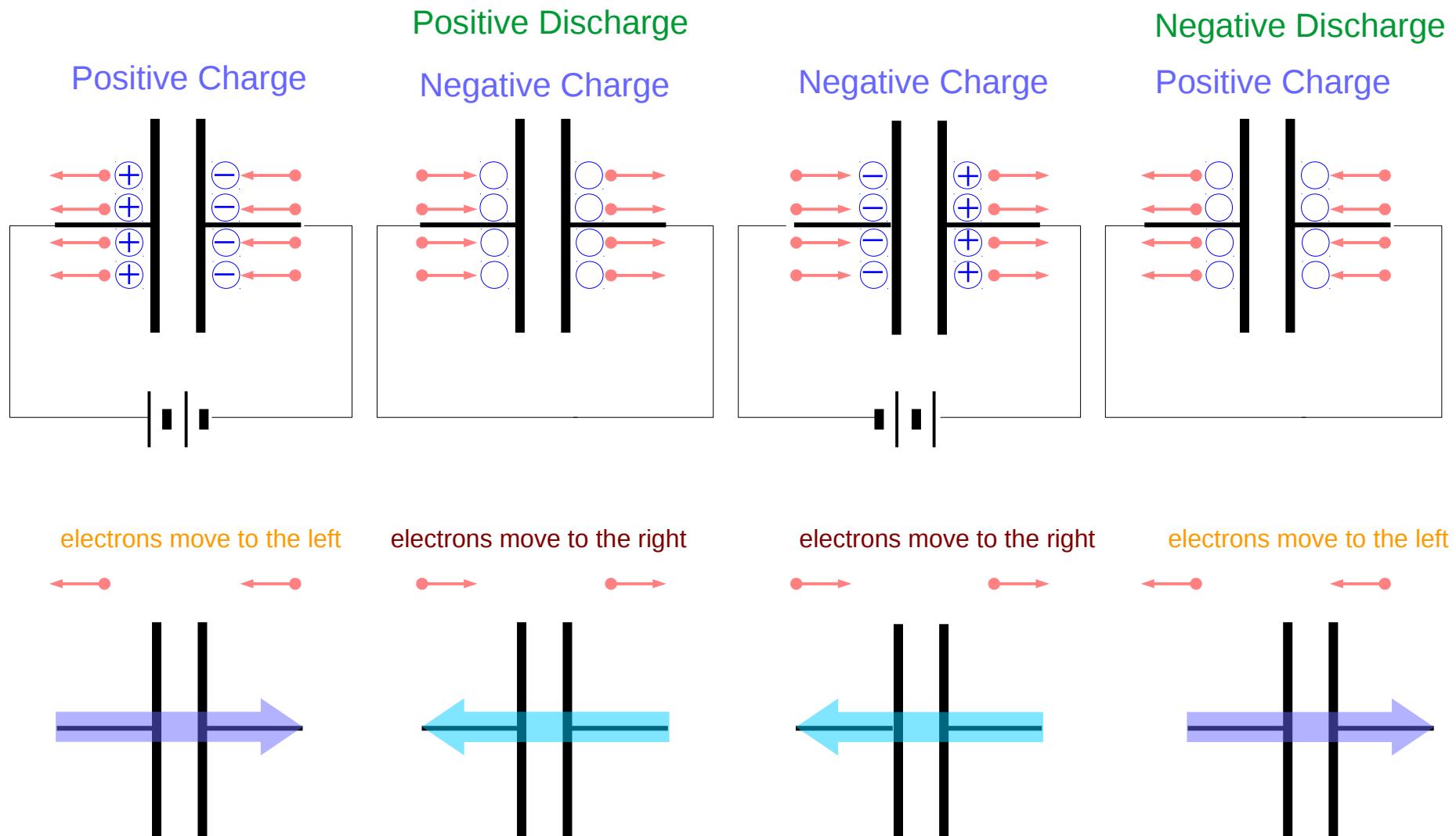
$$\propto \frac{dy}{dt}$$

Fully Charged and Fully Discharged

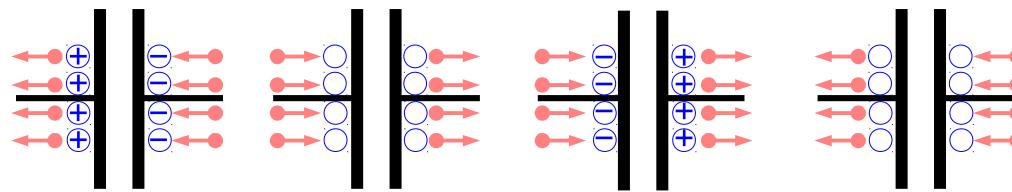


```
h = bar(t1, [y1' y2'],  
"stacked")  
set(h(1), "facecolor", "g");  
set(h(2), "facecolor", "y");  
hold on  
plot(t1, y1)  
axis([0 pi]);
```

Everchanging signal pairs

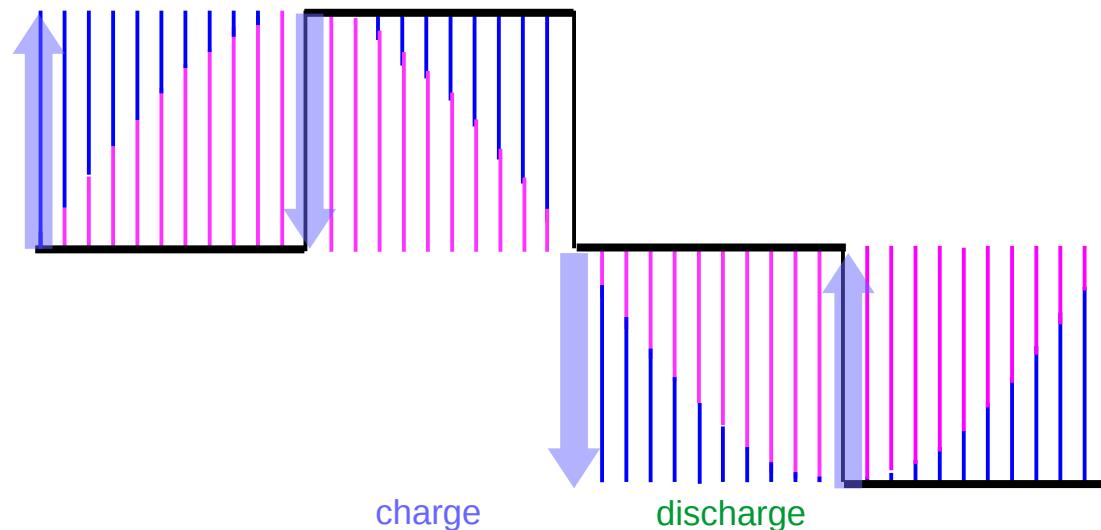


Everchanging signal pairs



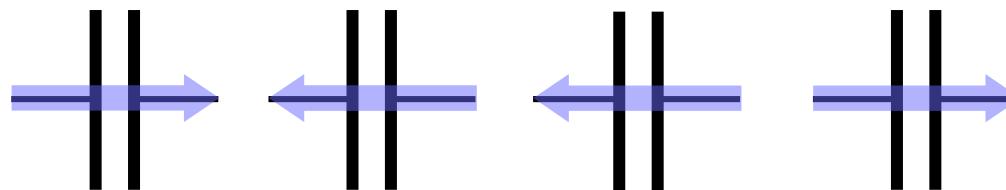
charge

discharge

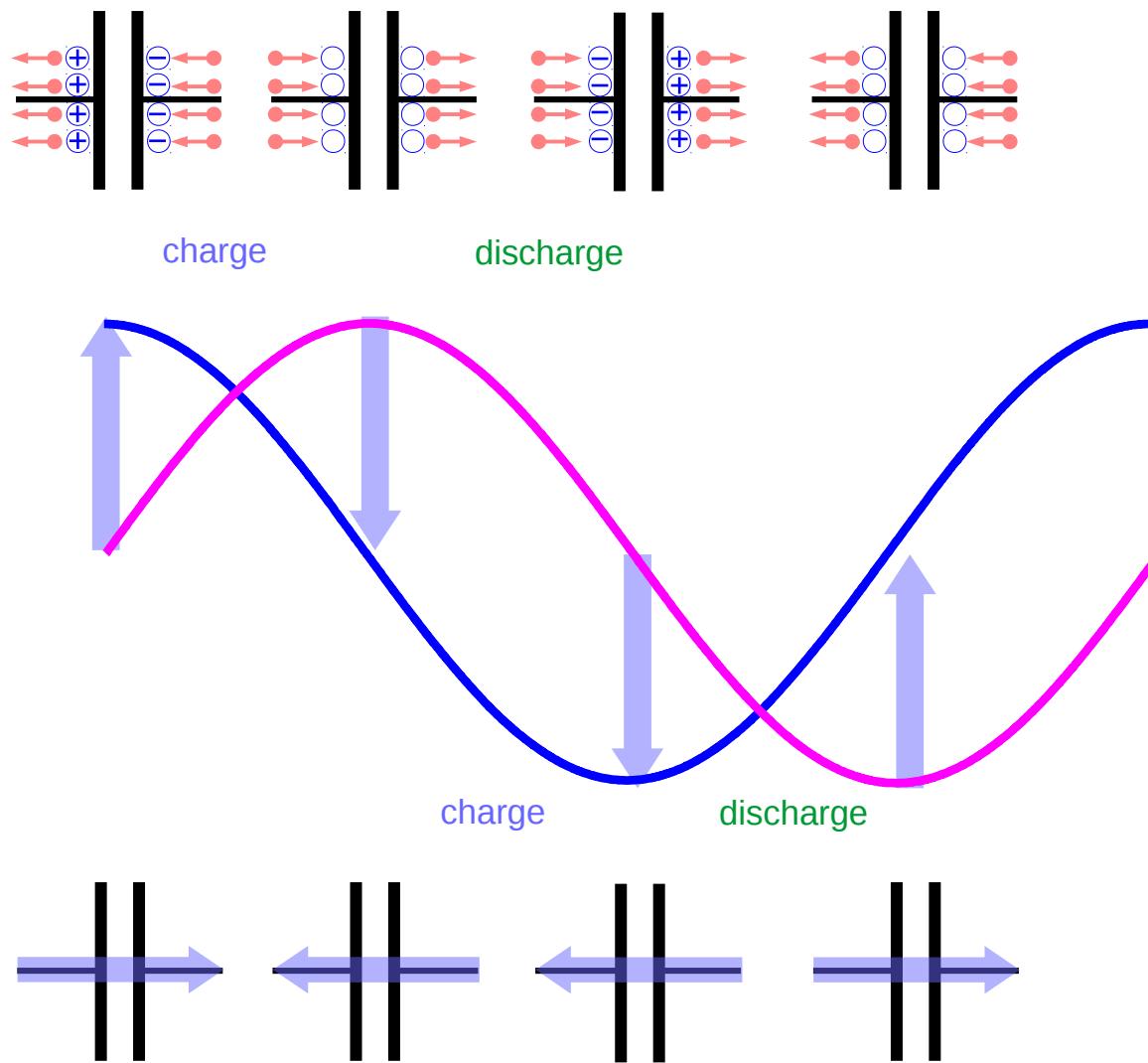


charge

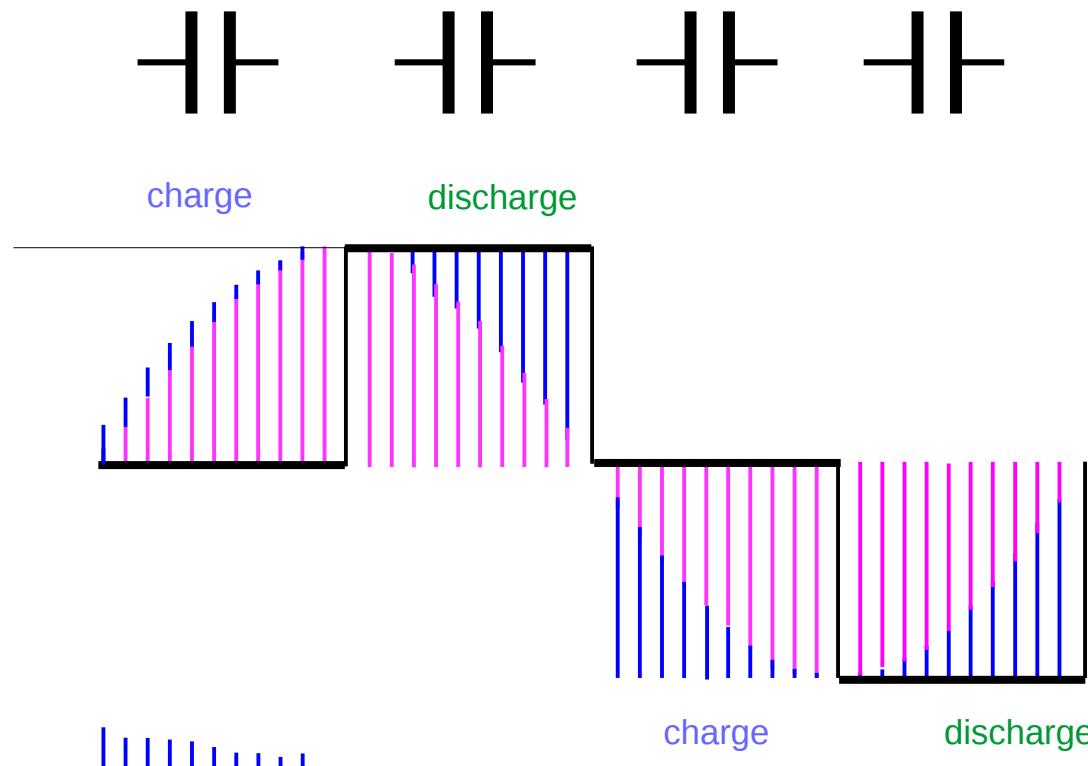
discharge



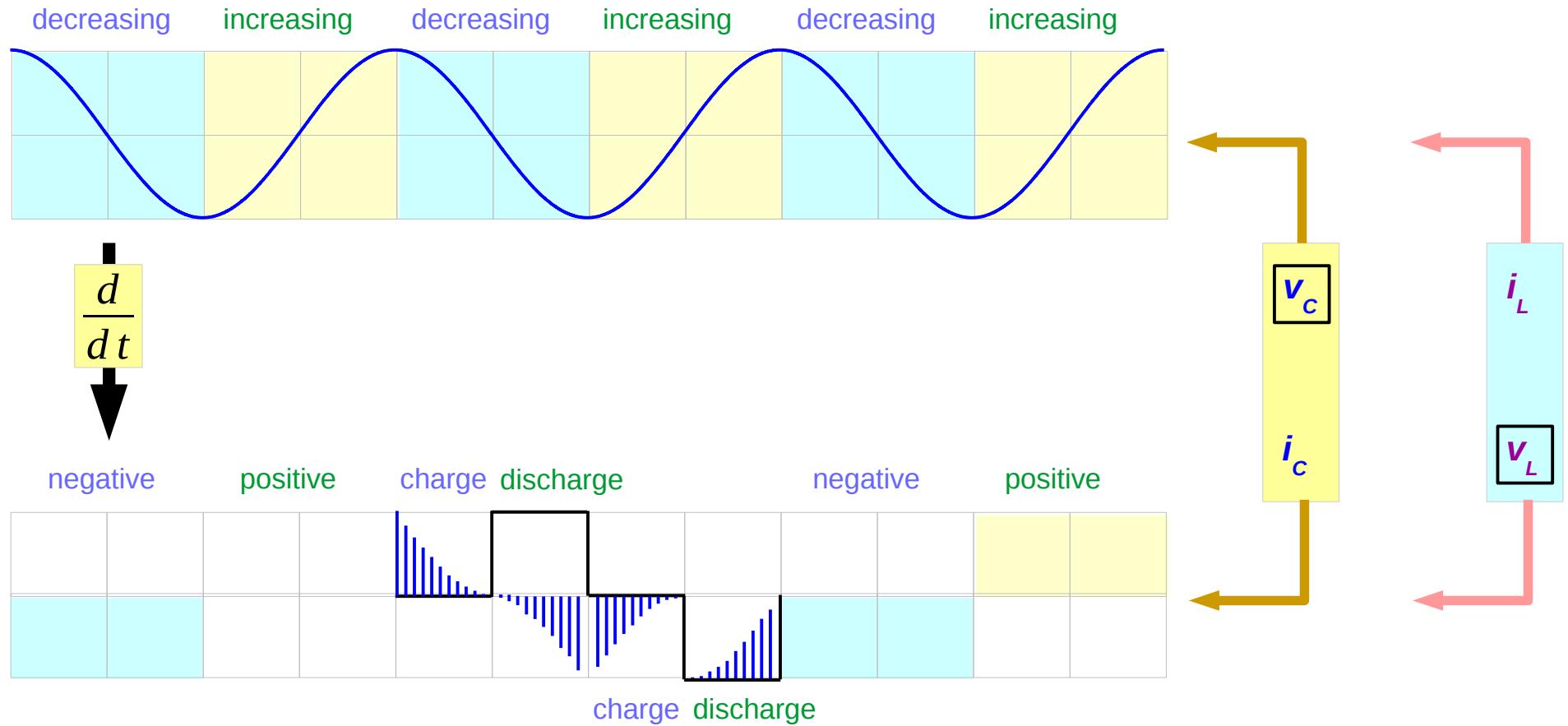
Everchanging signal pairs



Everchanging signal pairs

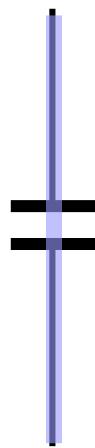


Everchanging signal pairs



I leads V by 90°

*Initial
charge*

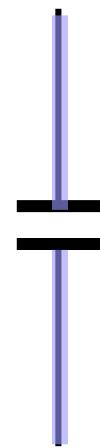


SHORT

$$V = 0$$

$I : \text{peak}$

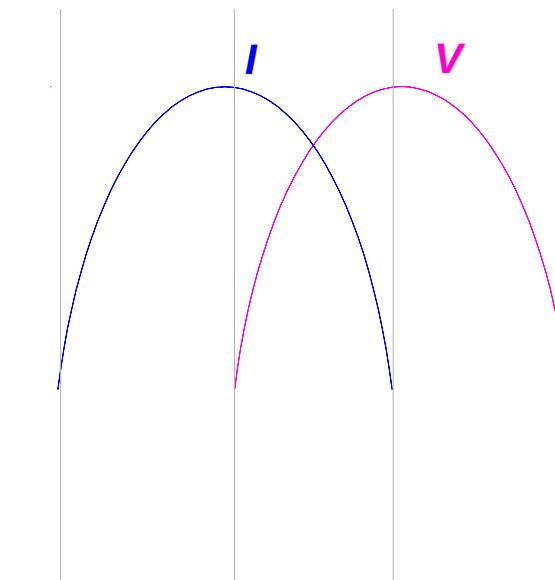
*Full
charge*



OPEN

$$I = 0$$

$V : \text{peak}$



References

- [1] <http://en.wikipedia.org/>
- [2] J.H. McClellan, et al., Signal Processing First, Pearson Prentice Hall, 2003