Boolean Functions (8B)

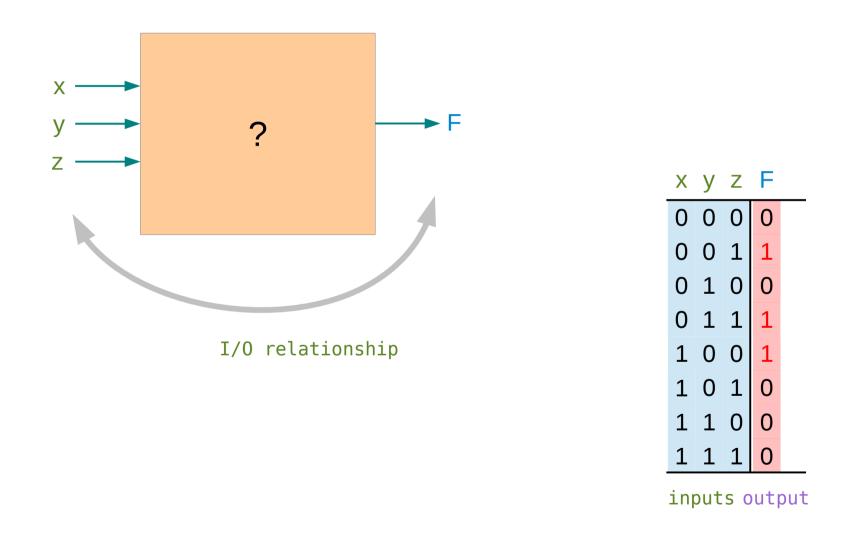
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Please send corrections (or suggestions) to youngwlim@hotmail.com.

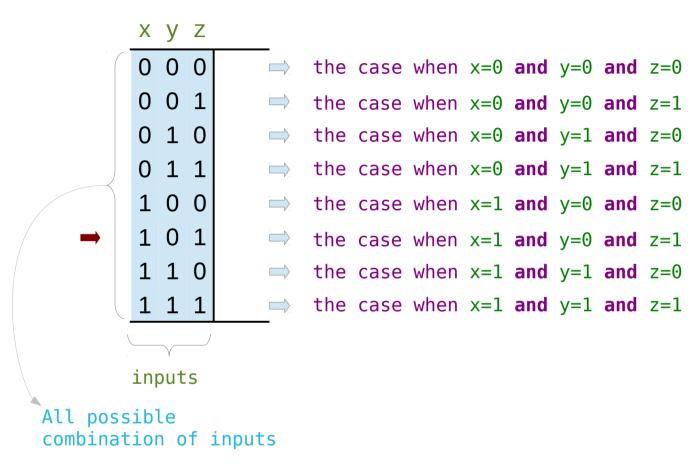
This document was produced by using LibreOffice and Octave.

Truth Table



All possible input cases

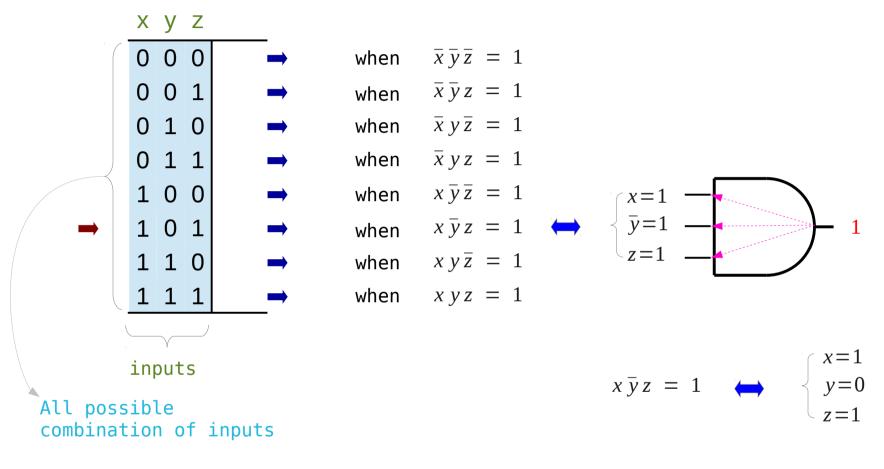
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All possible input cases using **minterms**

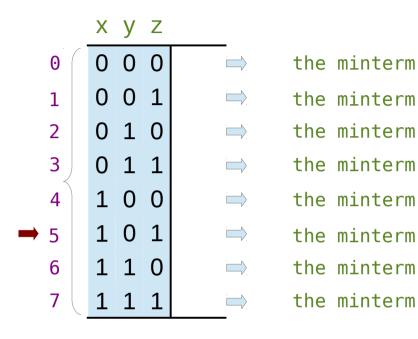
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For the output of an **and** gate to be 1, all inputs must be 1

Naming **minterms**

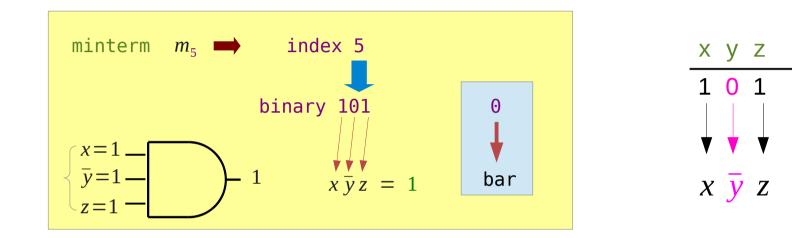
https://en.wikiversity.org/wiki/The_necessities_in_Digital_Design



index

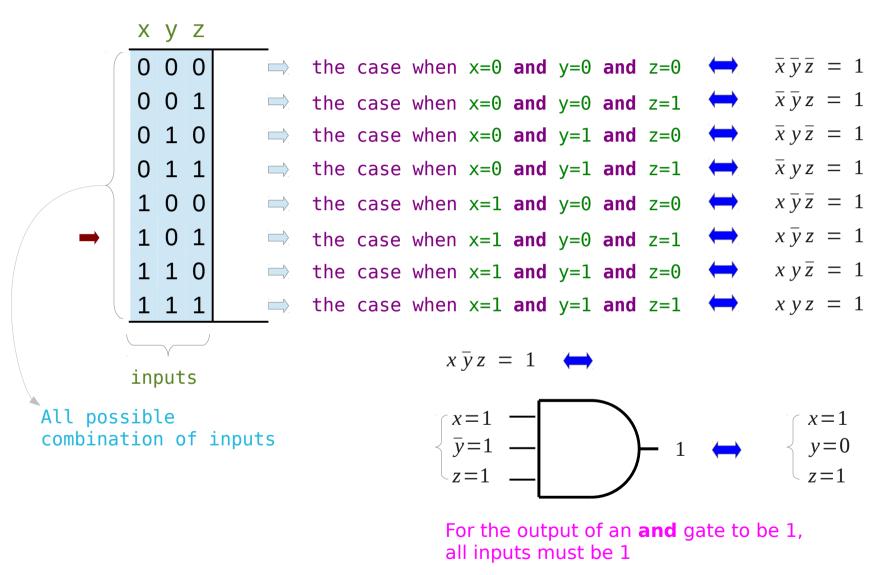
m_0	=	$\bar{x} \bar{y} \bar{z} = 1$
m_1	=	$\bar{x} \bar{y} z = 1$
<i>m</i> ₂	=	$\bar{x} y \bar{z} = 1$
<i>m</i> ₃	=	$\bar{x} y z = 1$
m_4	=	$x \bar{y} \bar{z} = 1$
m_5	=	$x \bar{y} z = 1$
m_6	=	$x y \overline{z} = 1$
<i>m</i> ₇	=	x y z = 1

Computing **minterms**

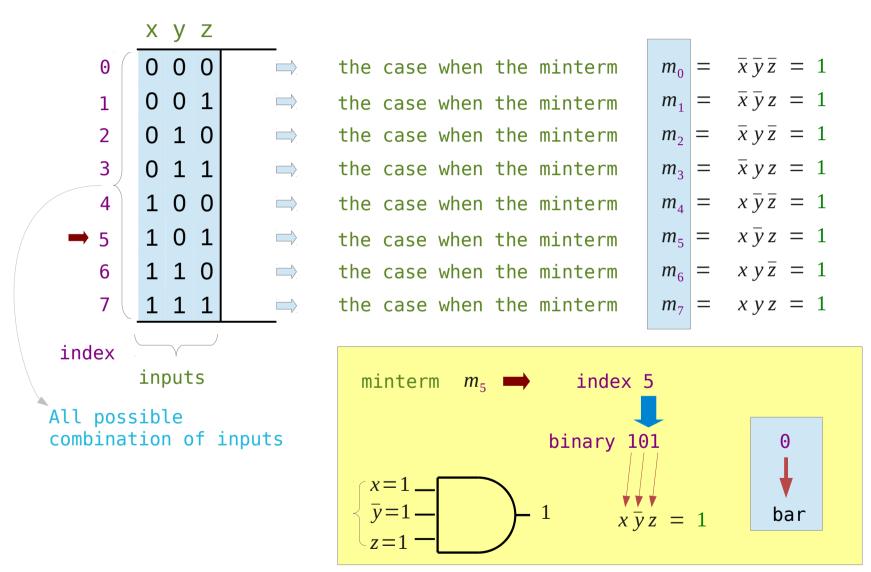


$$m_5 = x \bar{y} z$$

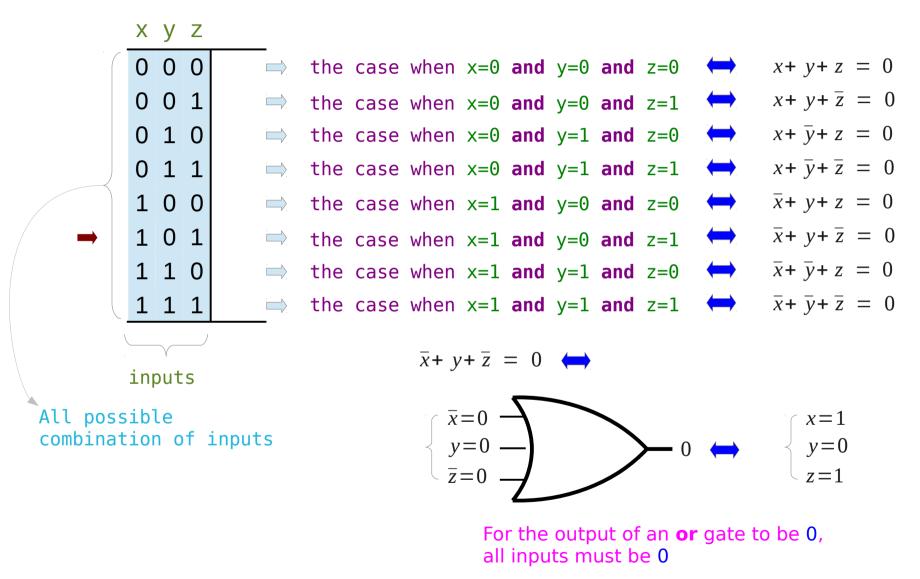
Truth Table and **minterms** (1)



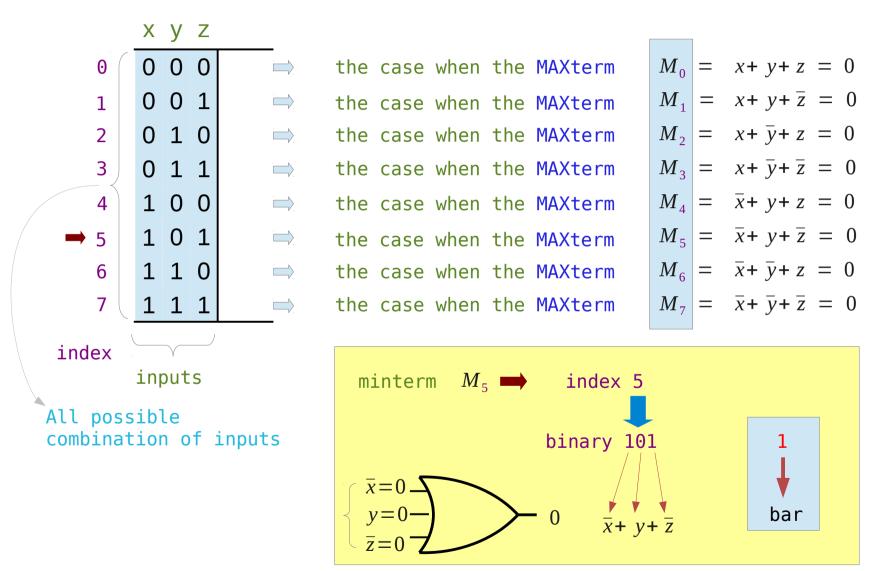
Truth Table and **minterms** (2)



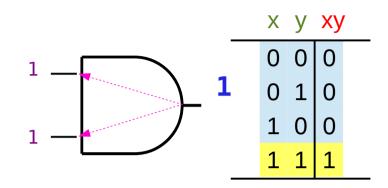
Truth Table and MAXterms (1)

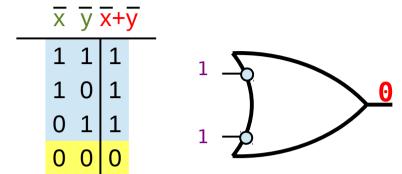


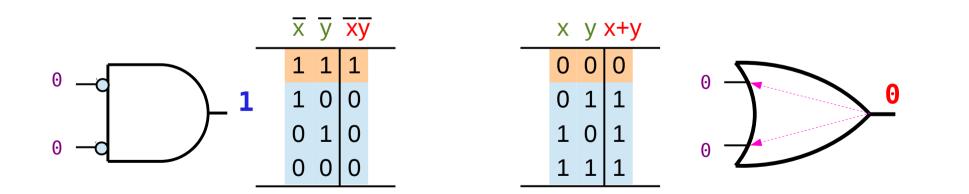
Truth Table and MAXterms (2)



Maxterm and minterm Conditions

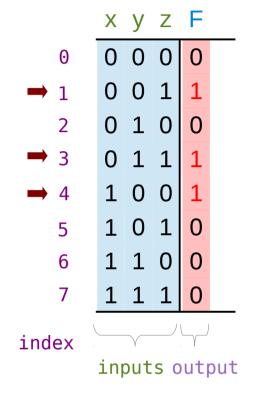






Boolean functions defined by a truth table

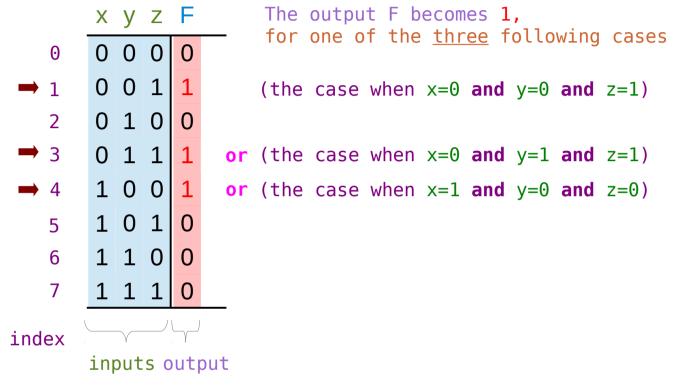
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All possible combination of inputs

When the output becomes 1

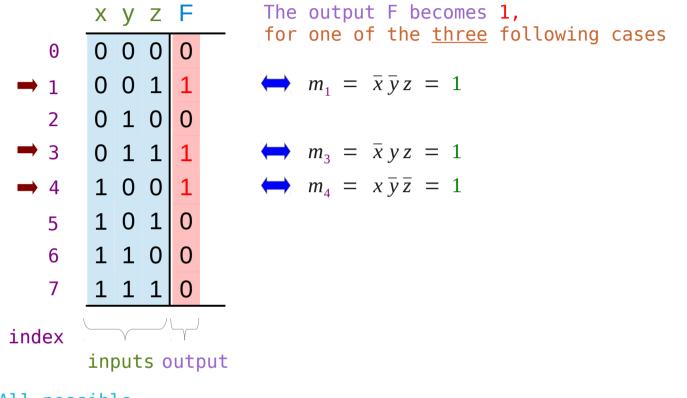
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All possible combination of inputs

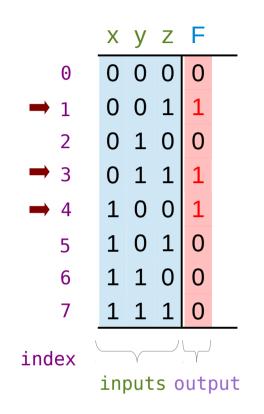
Function output values and minterms

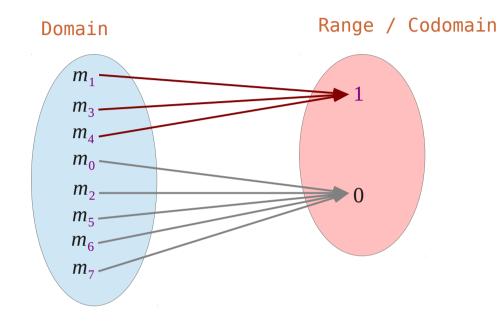
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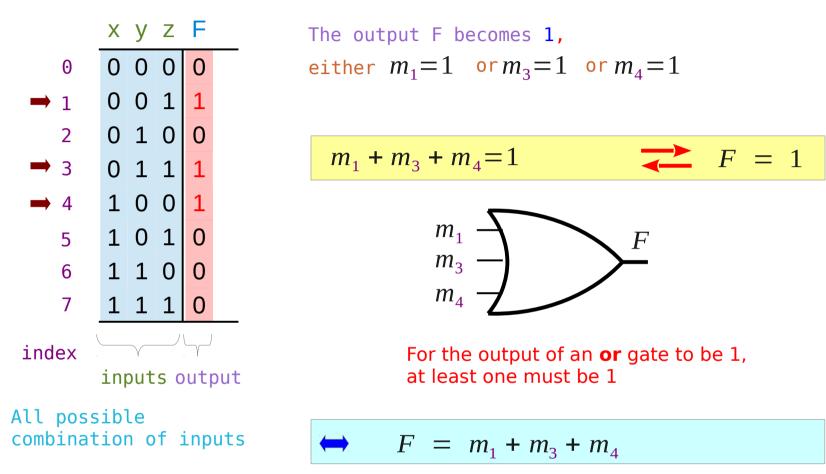
All possible combination of inputs

Mapping Set Diagram

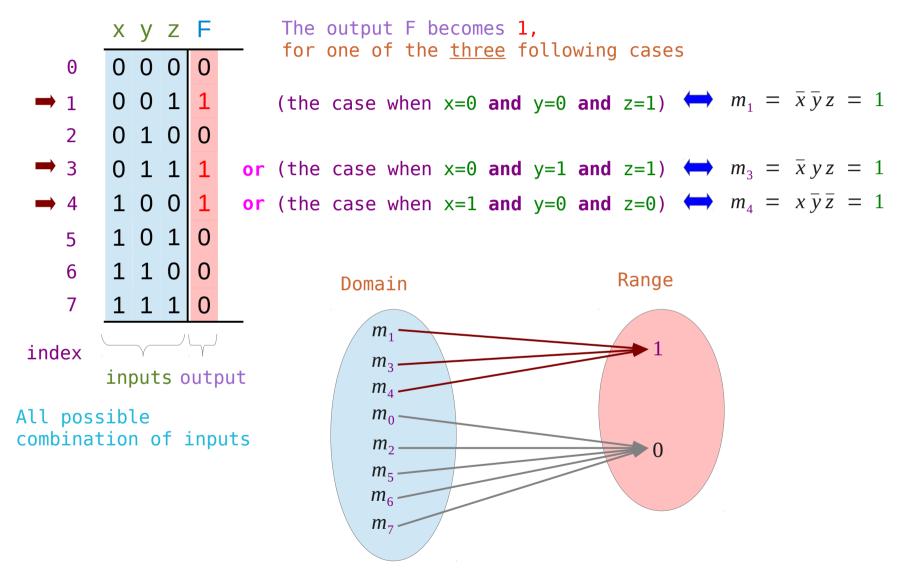




Boolean function definition using **minterms**

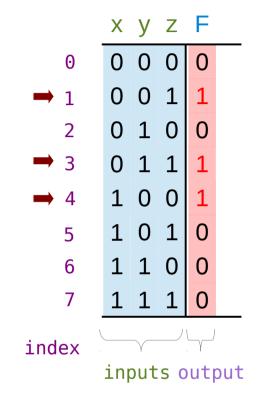


Boolean Function with **minterms** (1)

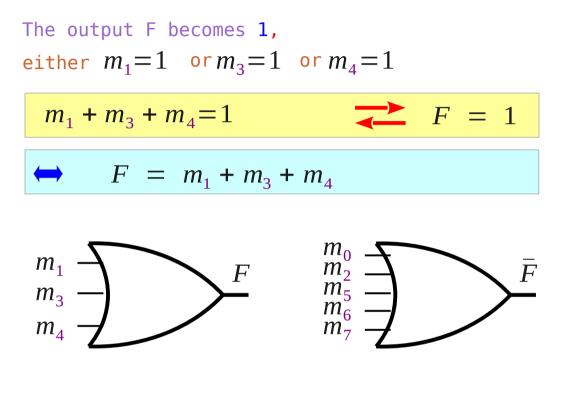


Boolean Function with **minterms** (2)

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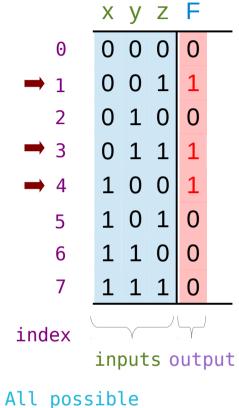


For the output of an **or** gate to be 1, at least one must be 1

Logic (8B)
Boolean Functions

Boolean Function with **minterms** (3)

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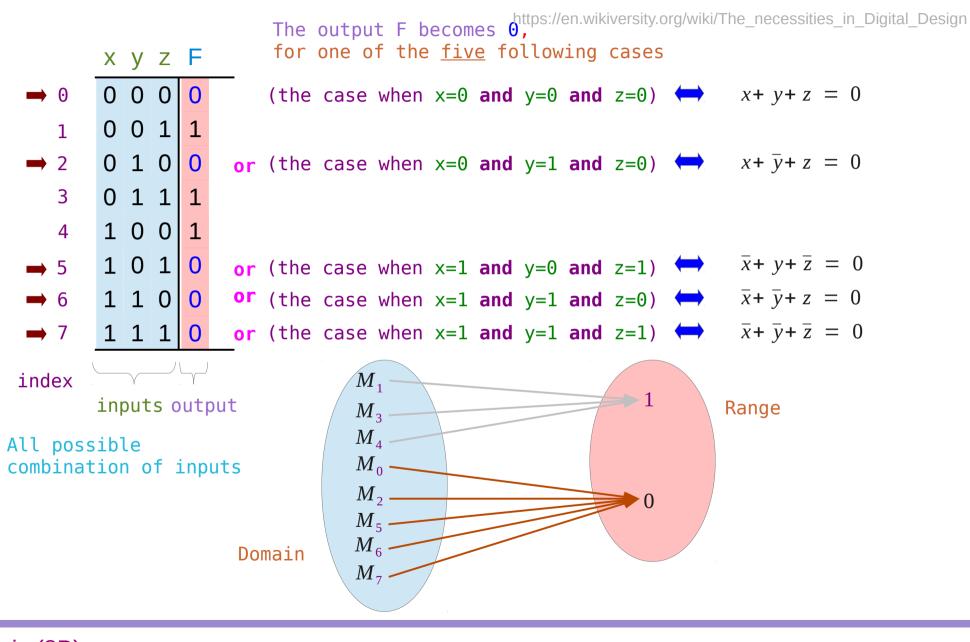
combination of inputs

The output F becomes 1,
either
$$m_1=1$$
 or $m_3=1$ or $m_4=1$
 $m_1 + m_3 + m_4=1$ \swarrow $F = 1$
 \Leftrightarrow $F = m_1 + m_3 + m_4$
The output F becomes 0,
either $m_0=1$ or $m_2=1$ or $m_5=1$ or $m_6=1$ or m_7
 $m_0+m_2+m_5+m_6+m_7=1$ \swarrow $F = 0$
 \Leftrightarrow $\overline{F} = m_0+m_2+m_5+m_6+m_7$

For the output of an **or** gate to be 1, at least one must be 1

=1

Boolean Function with **Maxterms** (1)

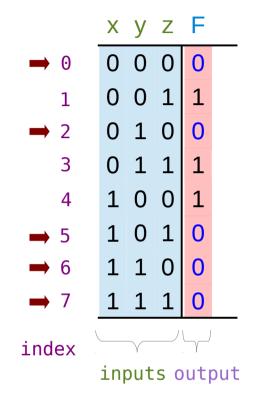


Logic (8B) Boolean Functions

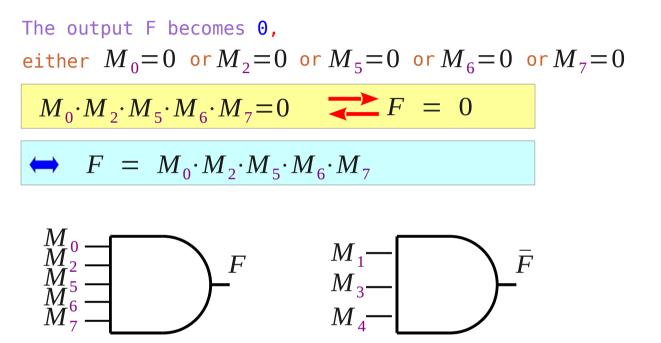
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Boolean Function with **Maxterms** (2)

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All possible combination of inputs

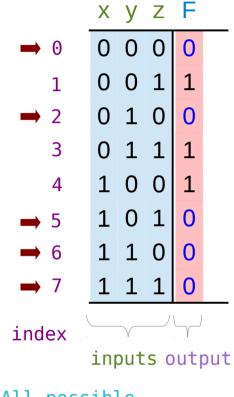


For the output of an **and** gate to be 0, at least one input must be 0



Boolean Function with **Maxterms** (2)

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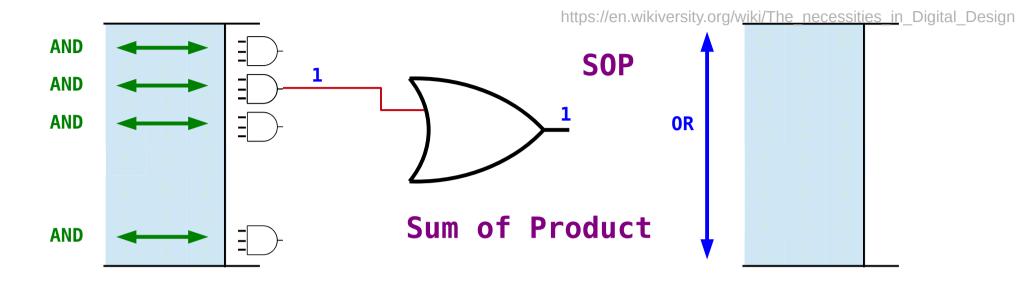
All possible combination of inputs

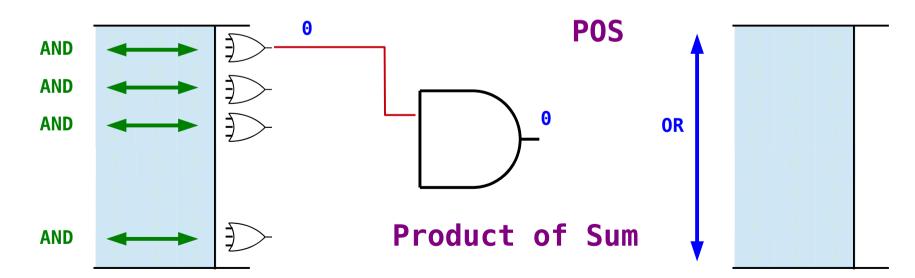
The output F becomes 0, either $M_0=0$ or $M_2=0$ or $M_5=0$ or $M_6=0$ or $M_7=0$ $M_0 \cdot M_2 \cdot M_5 \cdot M_6 \cdot M_7=0$ $\swarrow F = 0$ $\iff F = M_0 \cdot M_2 \cdot M_5 \cdot M_6 \cdot M_7$ The output F becomes 1, either $M_1=0$ or $M_3=0$ or $M_4=0$ $M_1 \cdot M_3 \cdot M_4=0$ $\swarrow F = 1$

 $\iff \overline{F} = M_1 \cdot M_3 \cdot M_4$

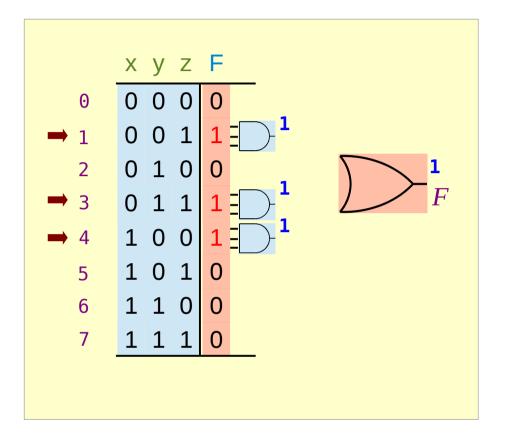
For the output of an **and** gate to be 0, at least one input must be 0

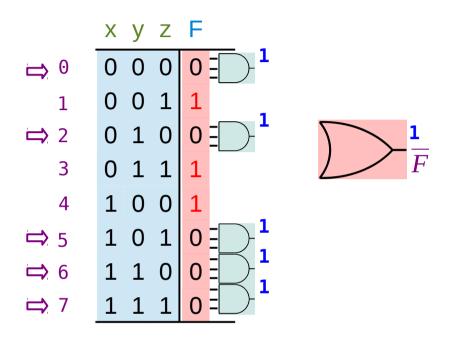
SOP and POS



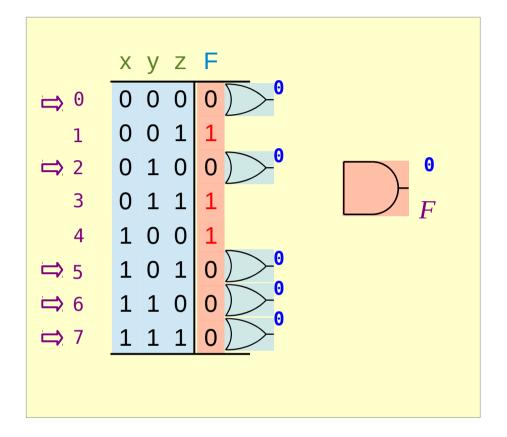


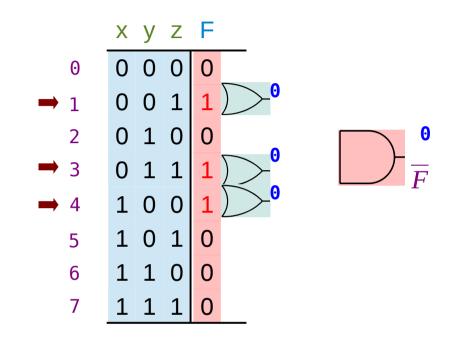
Boolean Function with **minterms**





Boolean Function with Maxterms







References

