

One Hot Design (2A)

Copyright (c) 2011-2016 Young W. Lim.

Permission is granted to copy, distribute and/or modify this document under the terms of the GNU Free Documentation License, Version 1.2 or any later version published by the Free Software Foundation; with no Invariant Sections, no Front-Cover Texts, and no Back-Cover Texts. A copy of the license is included in the section entitled "GNU Free Documentation License".

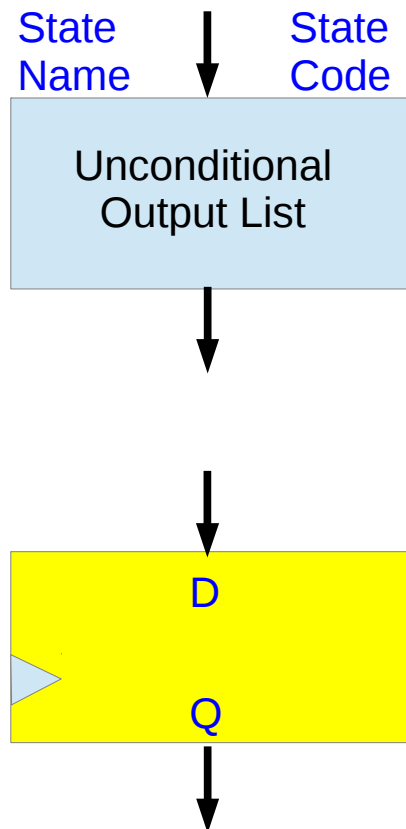
Please send corrections (or suggestions) to youngwlim@hotmail.com.

This document was produced by using LibreOffice and Octave.

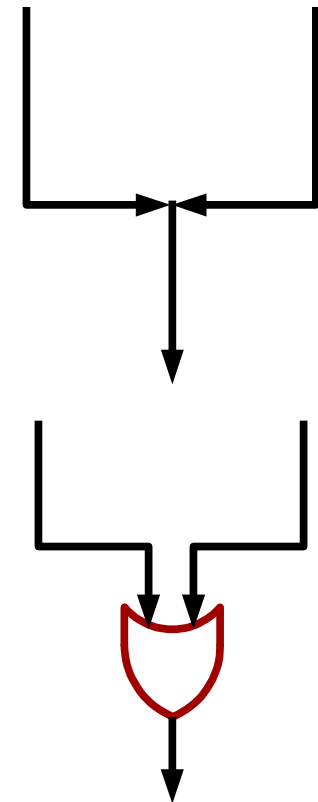
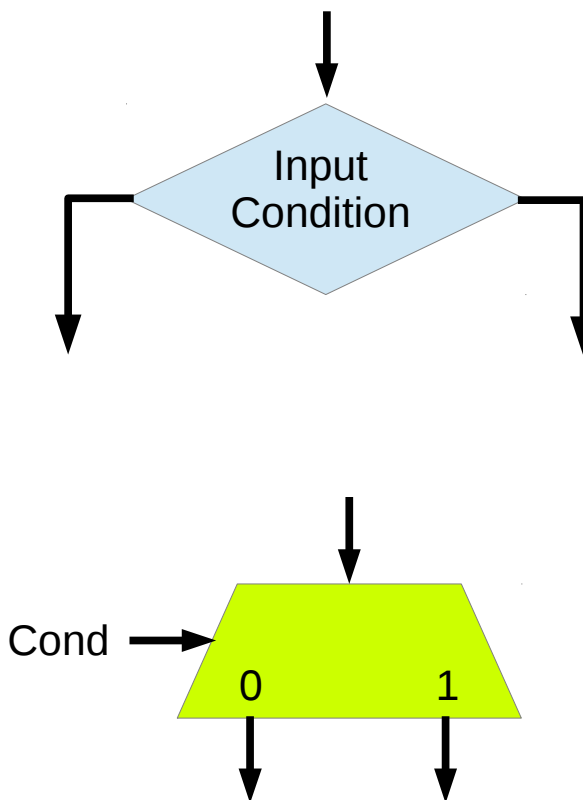
Moore ASM and One Hot Components

State Box

Moore Output



Condition Symbol



Examples

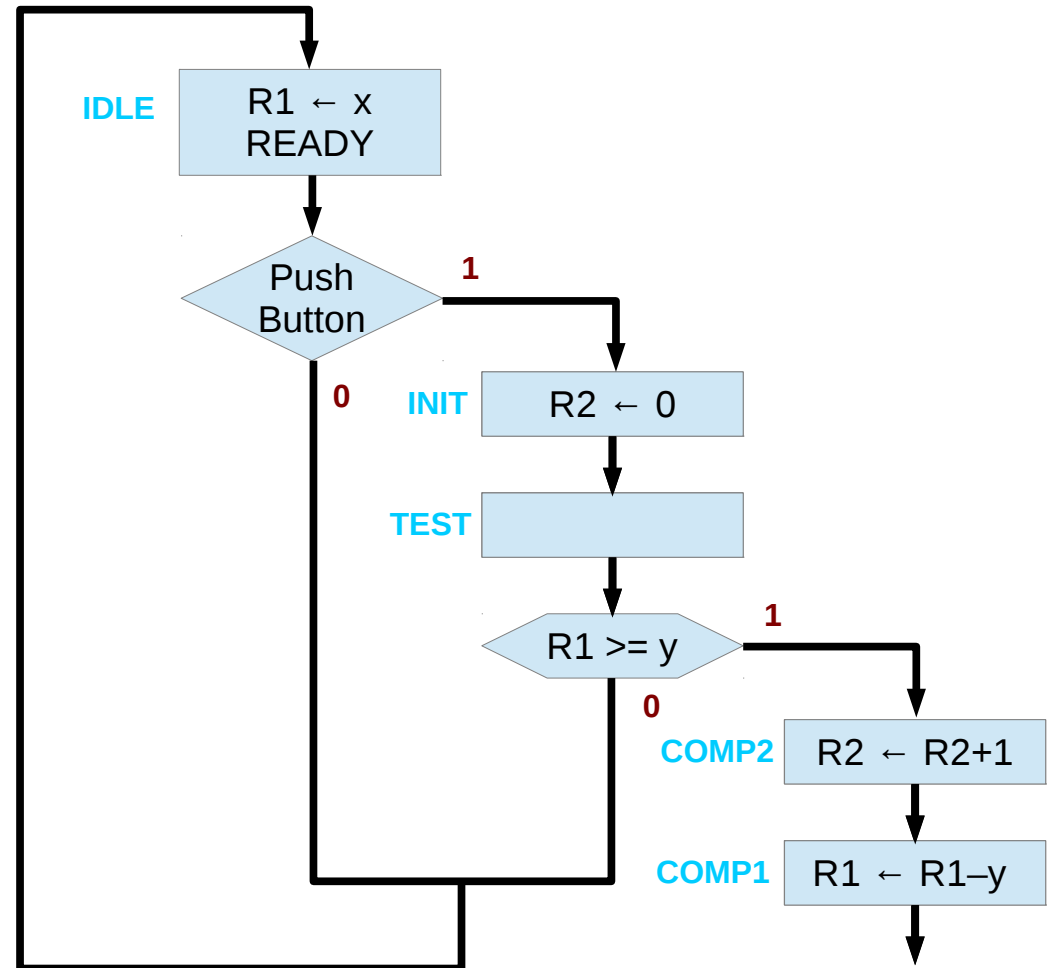
```
int x = 10, y = 3;
```

```
int R1 = x;
```

```
int R2 = 0;
```

```
while (R1 >= y) {  
    R1 = R1 - y;  
    R2 = R2 + 1;  
}
```

```
R1 = x % y  
R2 = x / y
```



Examples

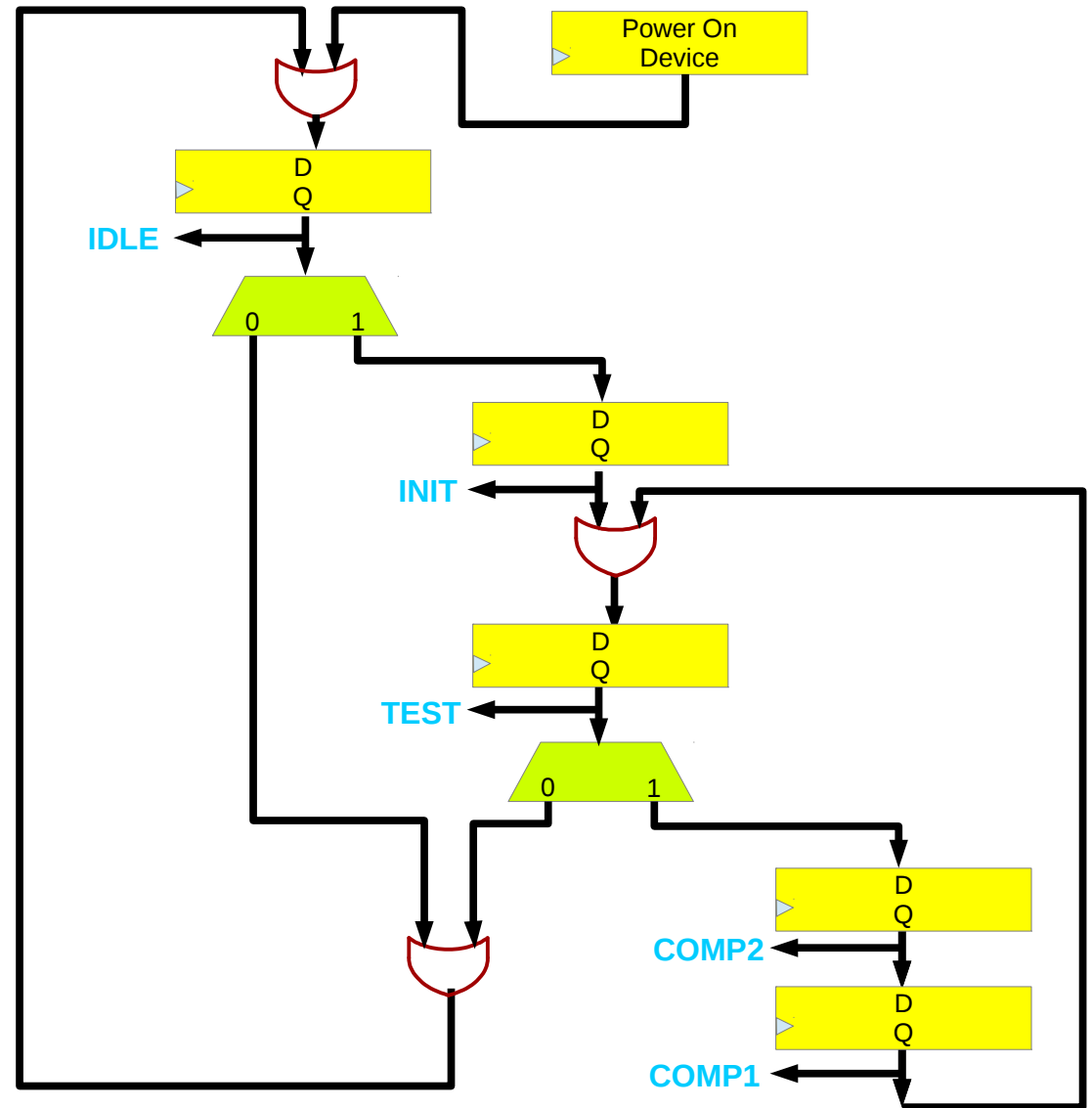
```
int x = 10, y = 3;
```

```
int R1 = x;
```

```
int R2 = 0;
```

```
while (R1 >= y) {  
    R1 = R1 - y;  
    R2 = R2 + 1;  
}
```

```
R1 = x % y  
R2 = x / y
```



References

- [1] <http://en.wikipedia.org/>
- [2] M. M. Mano, C. R. Kime, "Logic and Computer Design Fundamentals", 4th ed.
- [3] D.M. Harris, S. L. Harris, "Digital Design and Computer Architecture"
- [4] M. G. Arnold, "Verilog Digital Computer Design : Algorithms into Hardware", 1999
- [5] F.P. Prosser, D.E. Winkel, "The Art of Digital Design : An Intro to Top-Down Design", 2nd ed, 1986