Capacitors in an AC circuit

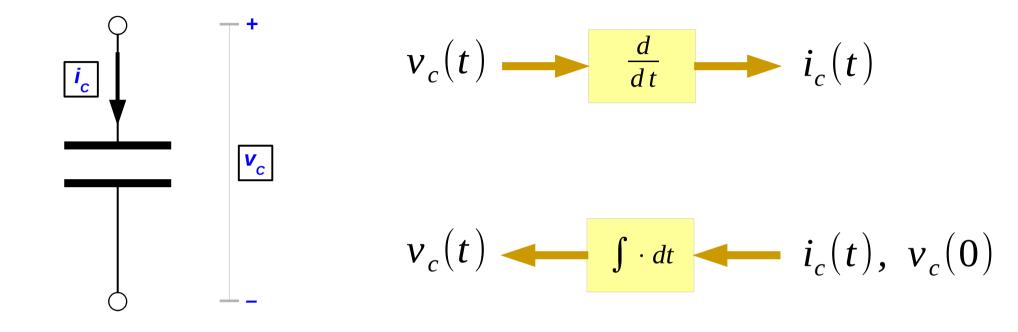
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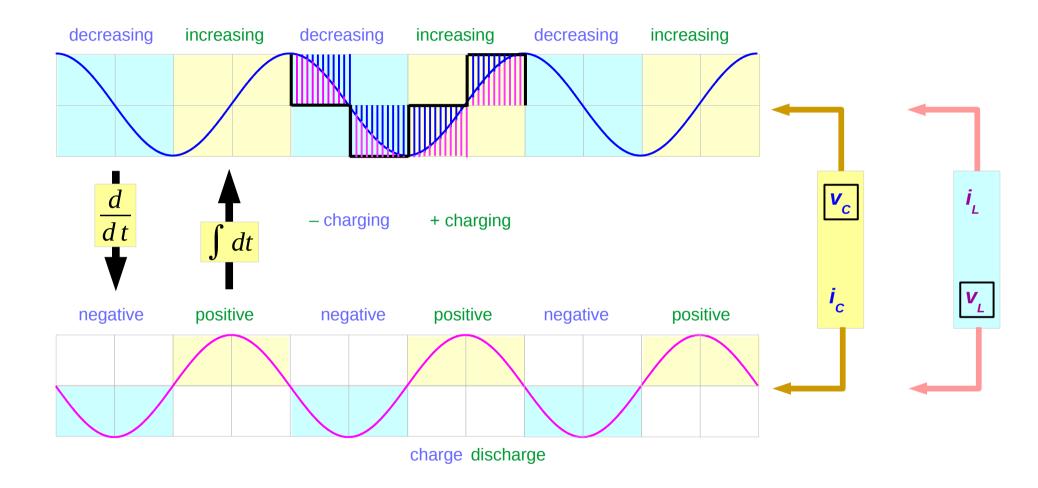
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Please send corrections (or suggestions) to youngwlim@hotmail.com.

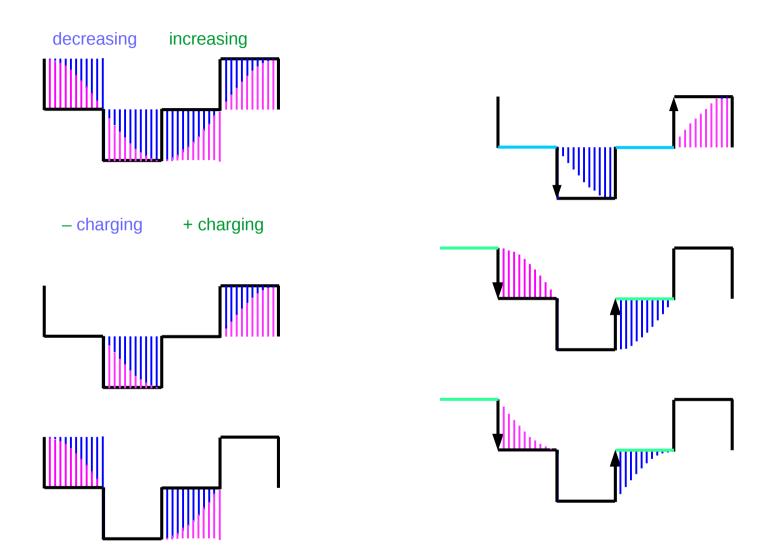
This document was produced by using OpenOffice and Octave.

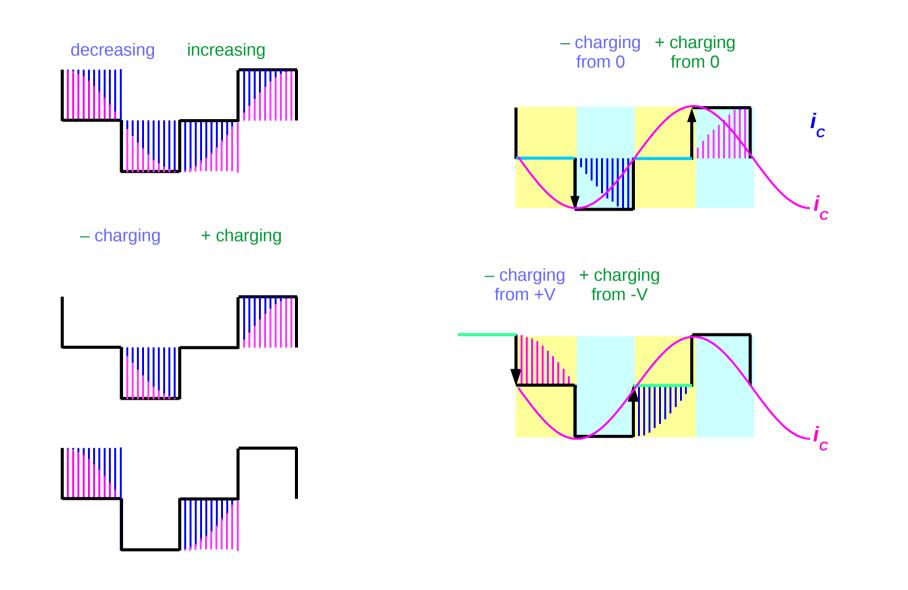
Invertible Functions

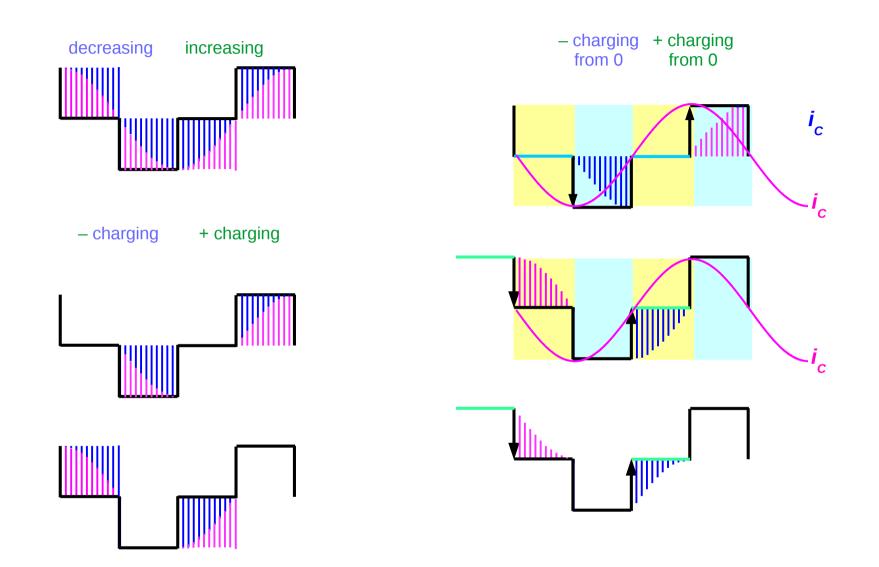




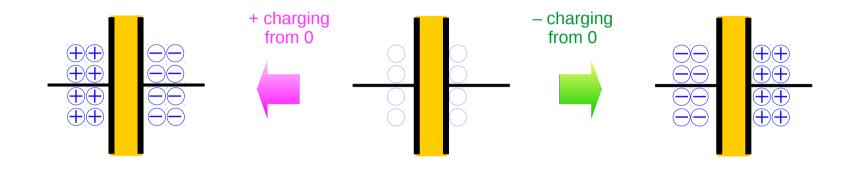
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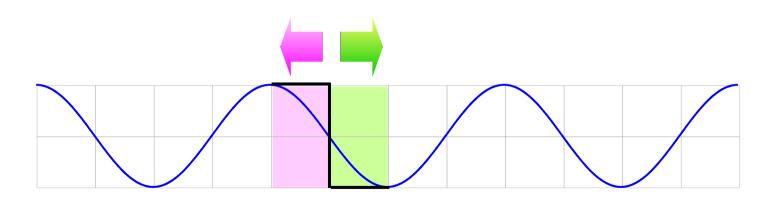


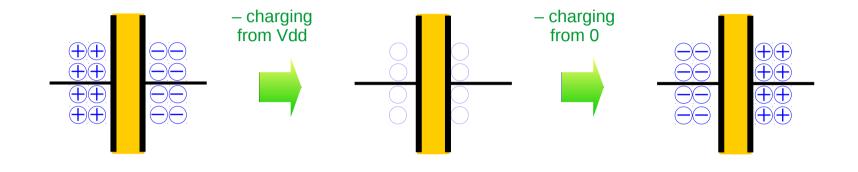


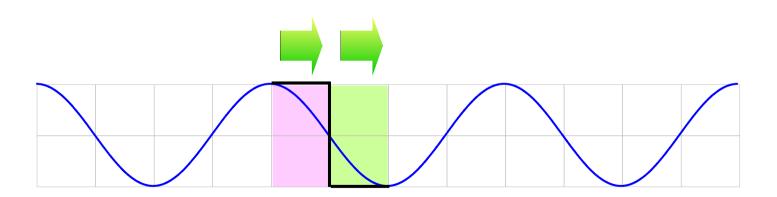


7

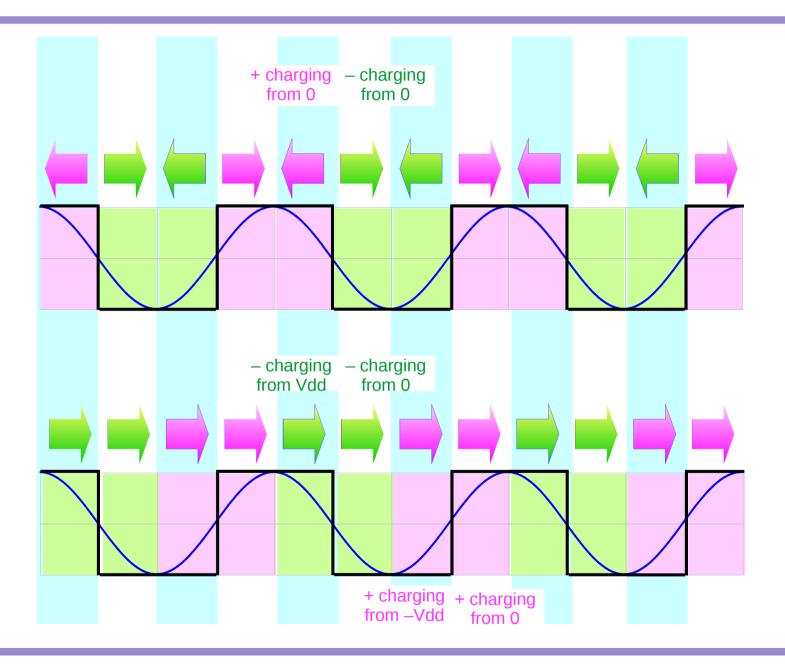


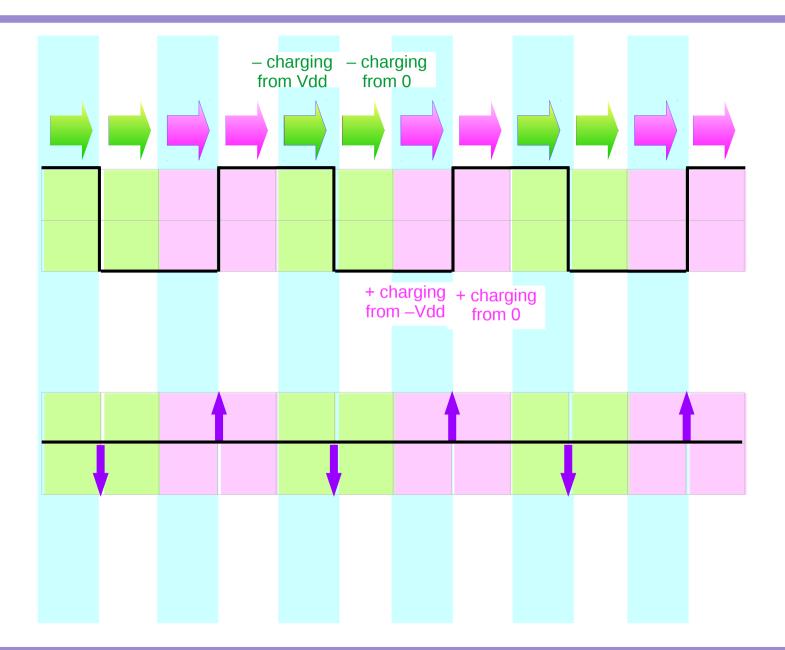


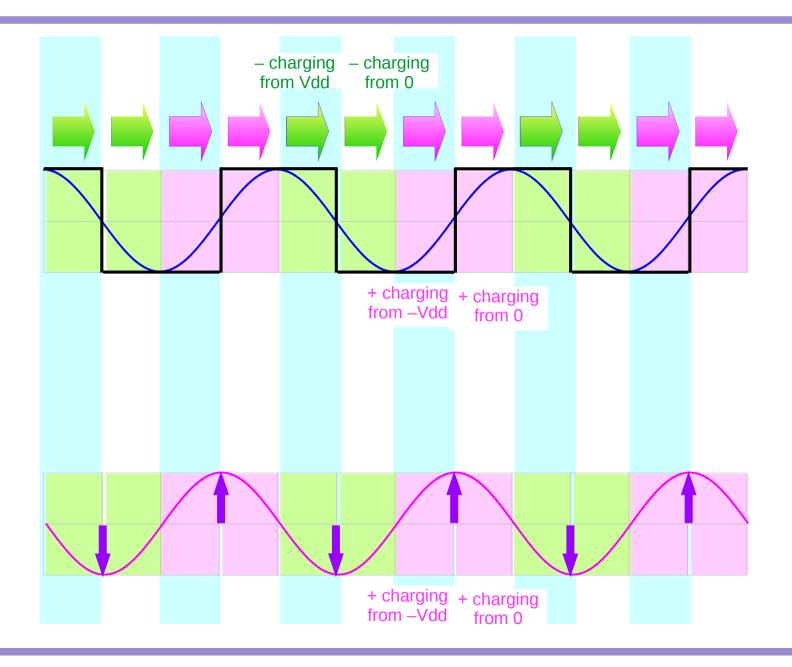


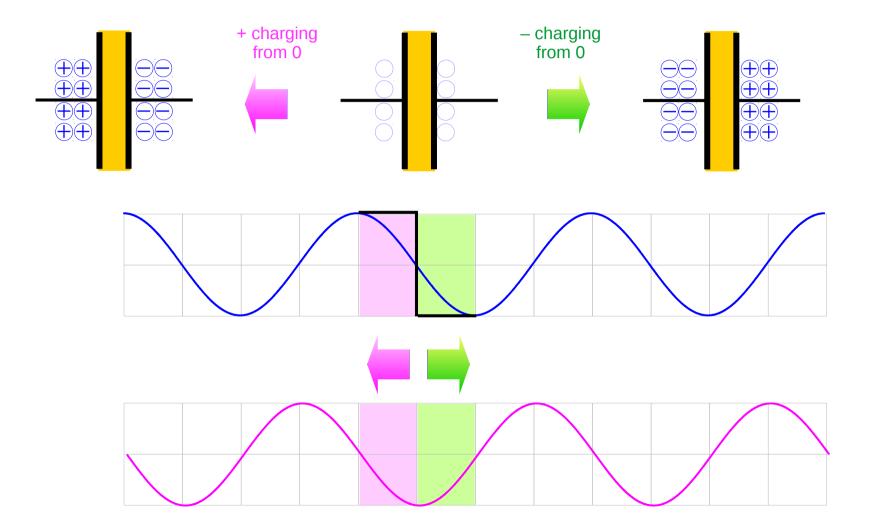


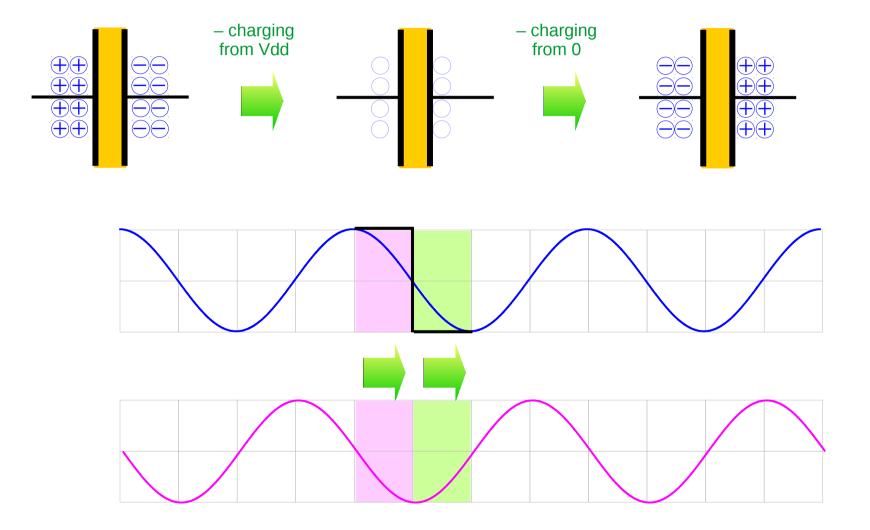
Capacitor – AC



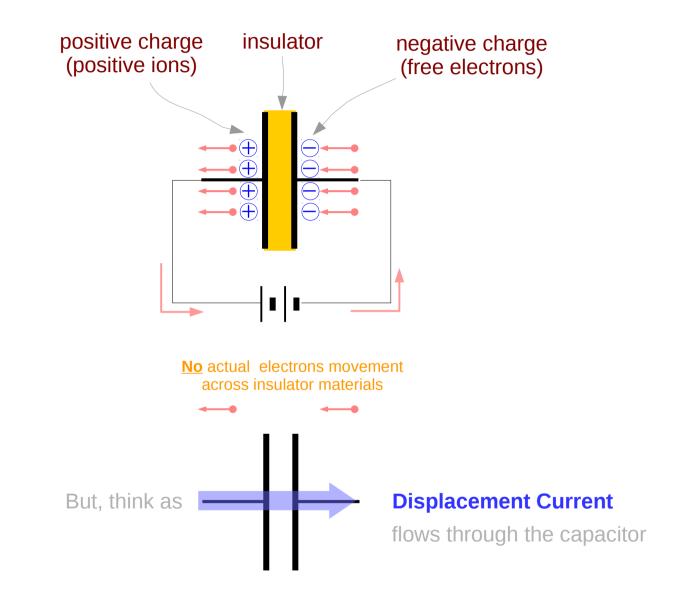






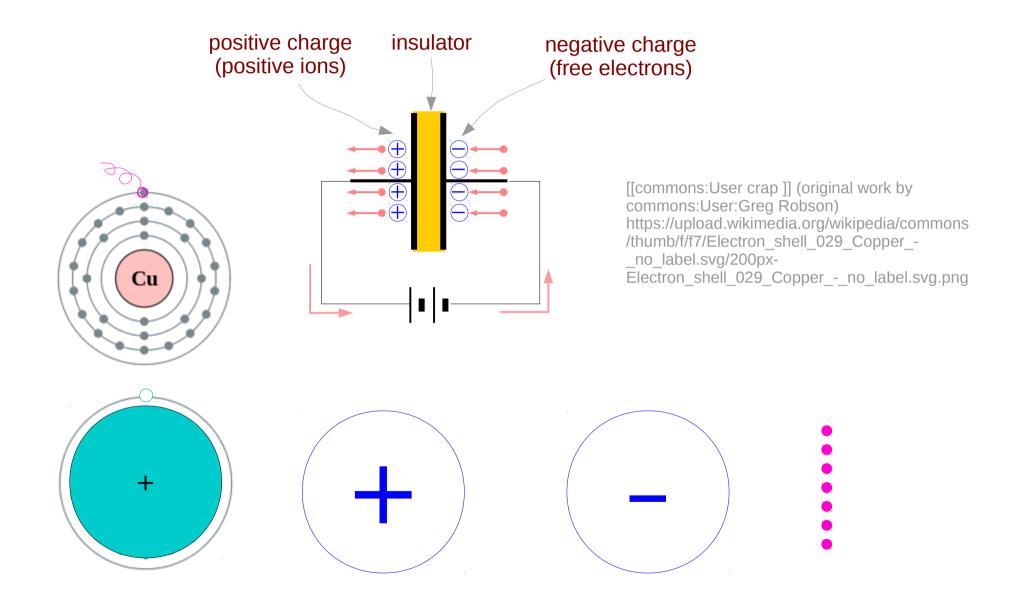


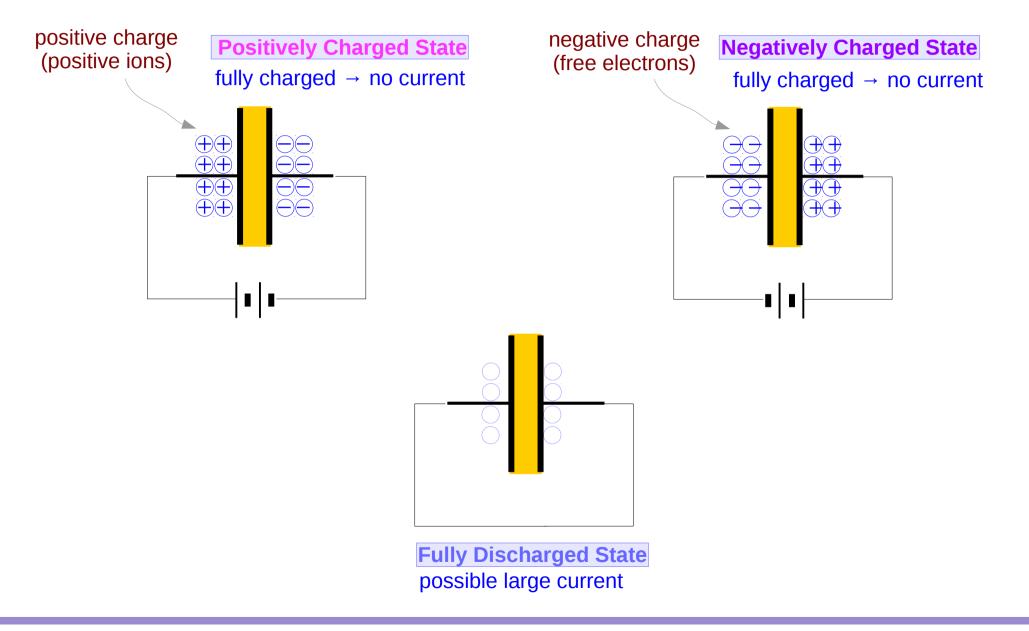
Capacitor Current



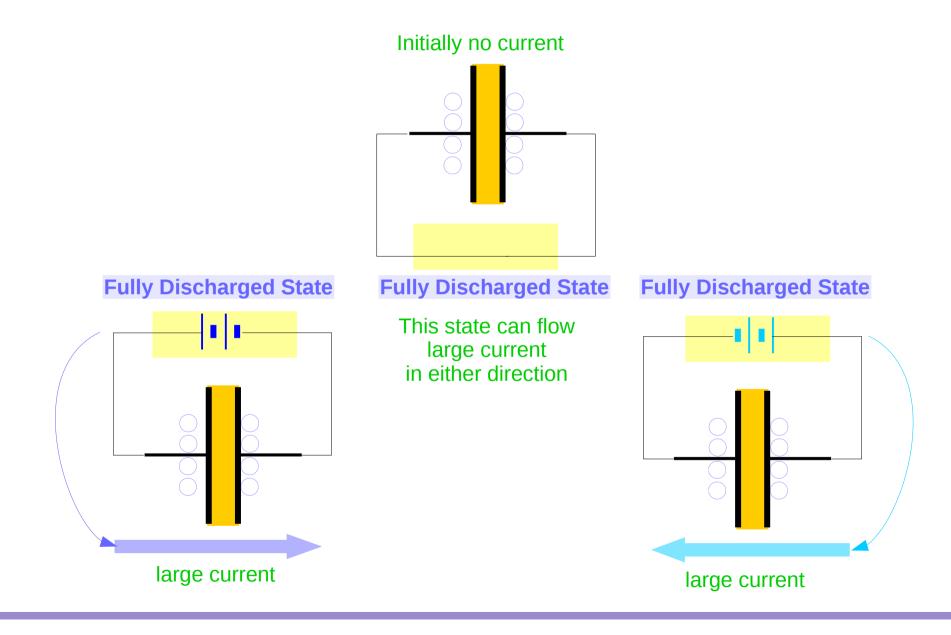
Capacitor – AC

Positive ions and free electrons

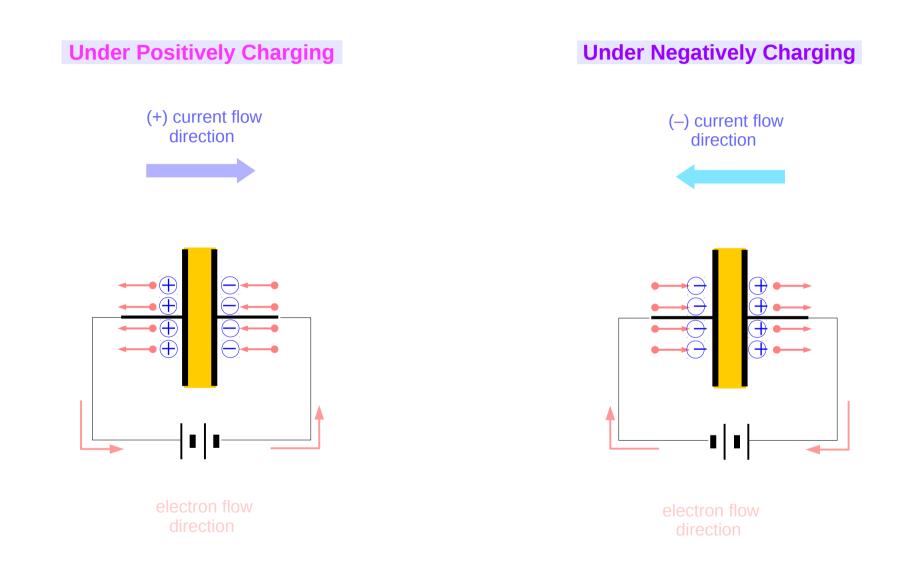




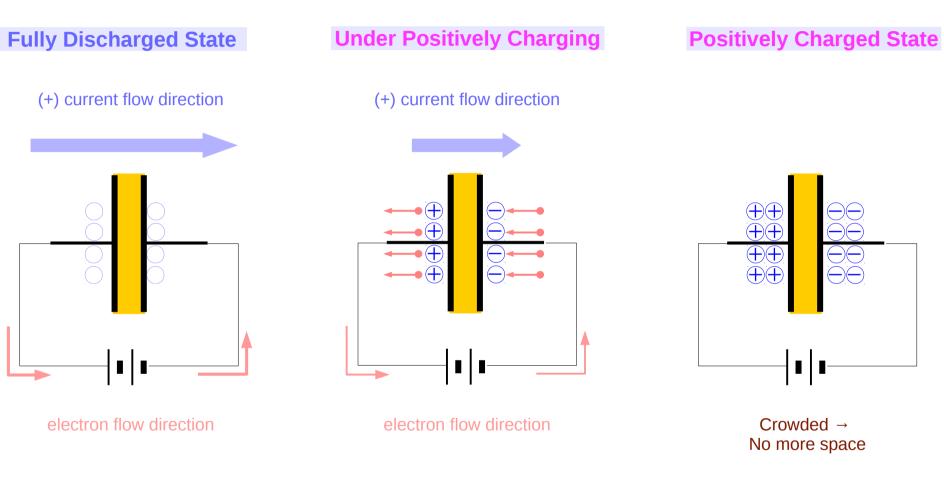
Currents in the Fully Discharged State



Inter-State Current Flowing



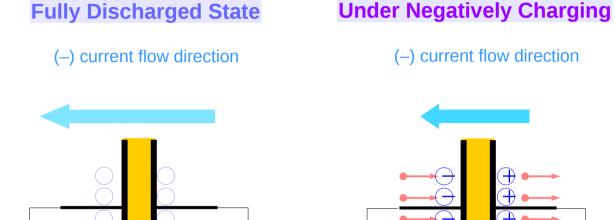
Inter-State Current Flowing

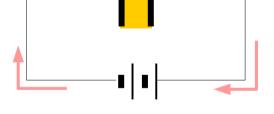


Initial large current

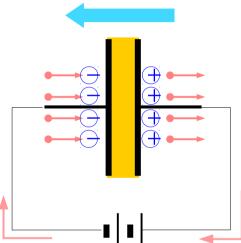
no current

Inter-State Current Flowing





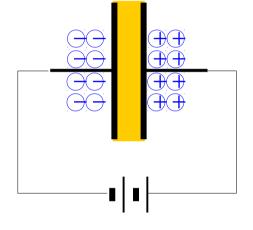
electron flow direction



(-) current flow direction

electron flow direction



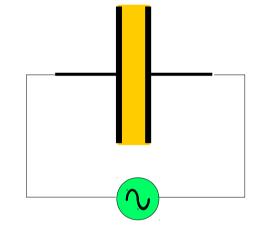


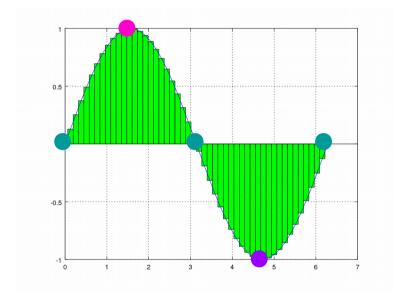
Crowded \rightarrow No more space

Initial large current

no current

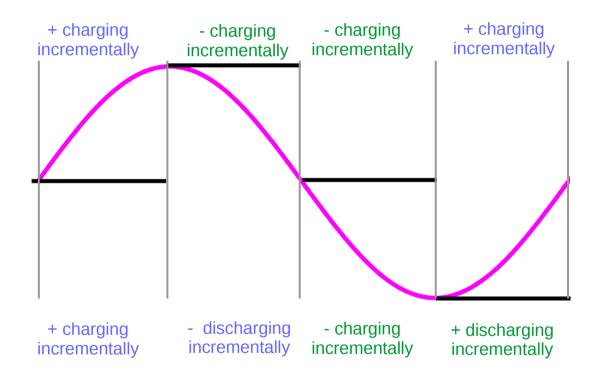
An AC Voltage Source



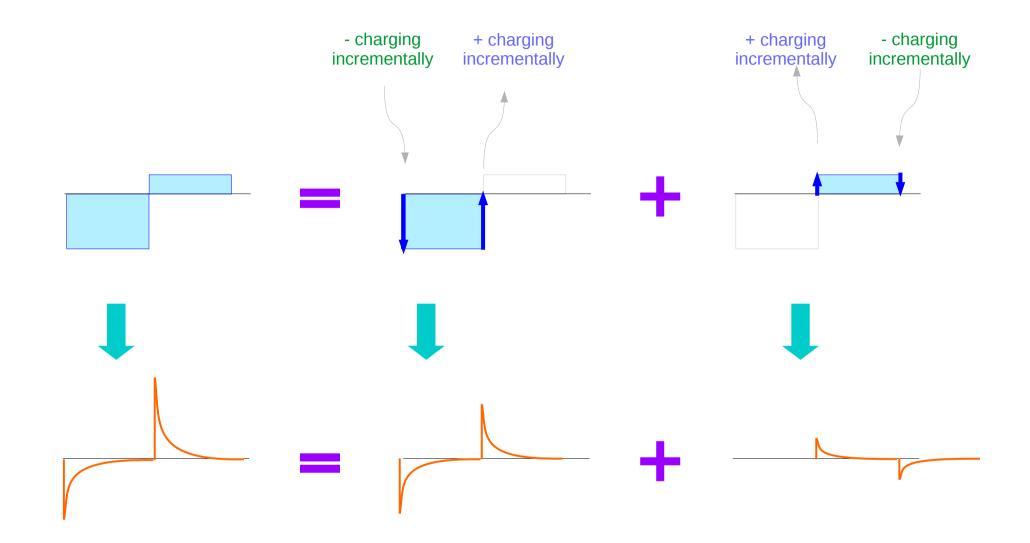


Continuous (Ever-) Charing Operations

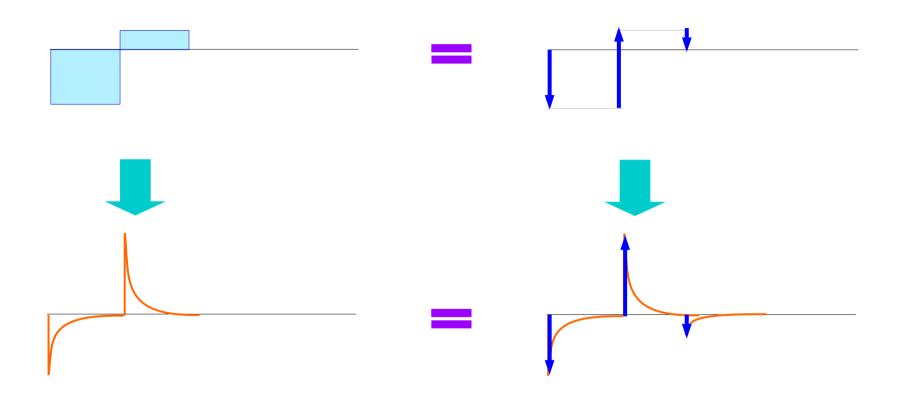
Incremental Voltage Increment➡ + Charging incrementallyIncremental Voltage Decrement➡ - Charging incrementally



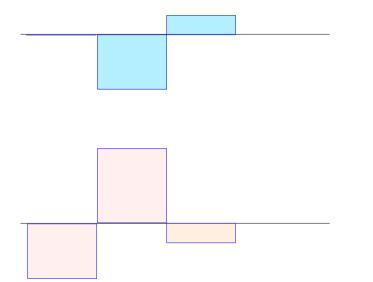
Superposition



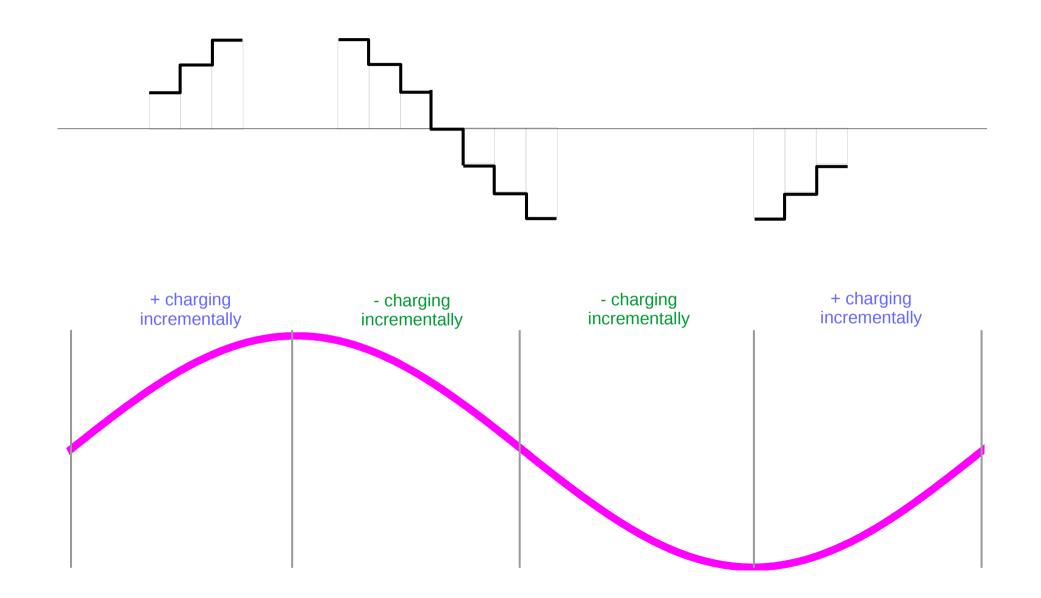
Superposition - Small Time Constant



Difference, Differentiation



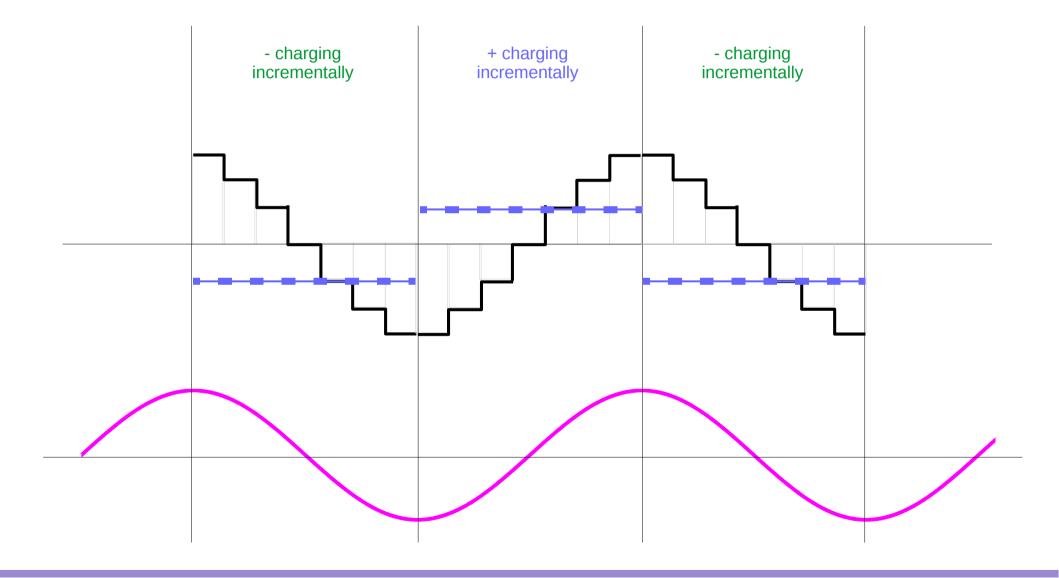
Continuous Charing and Discharging Operations



Capacitor – AC

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Incrementally Charging

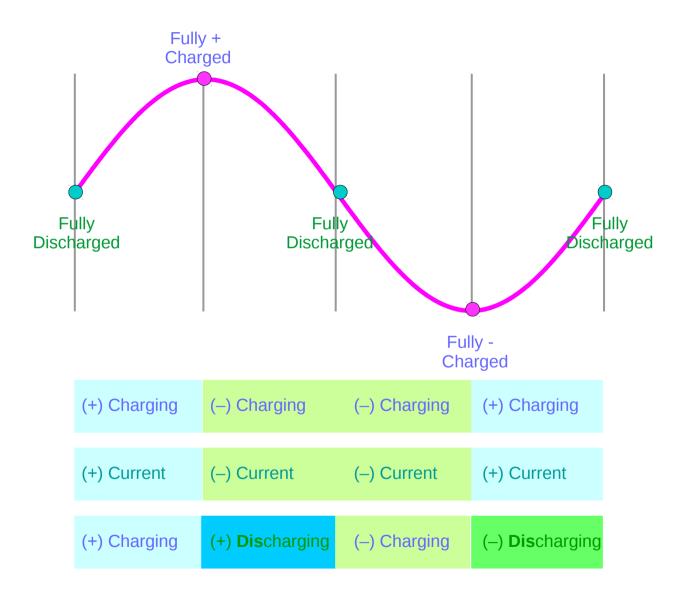


Capacitor – AC

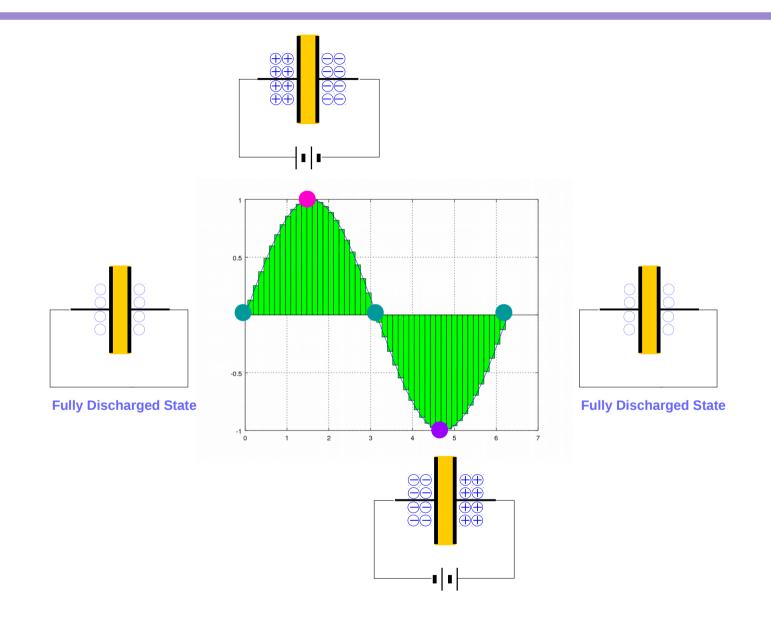
An AC Voltage Source



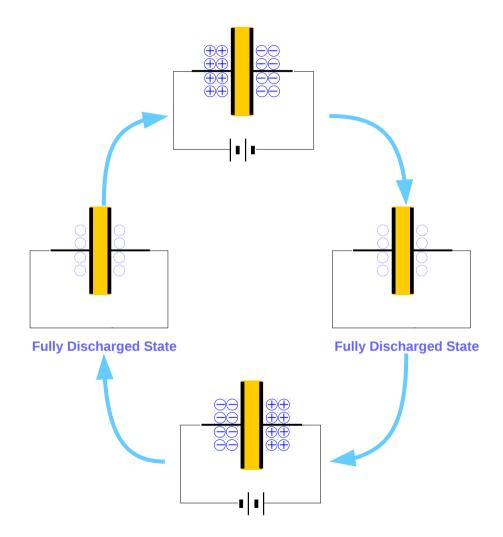
Fully Charged and Fully Discharged

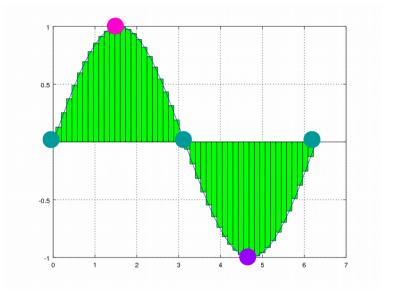


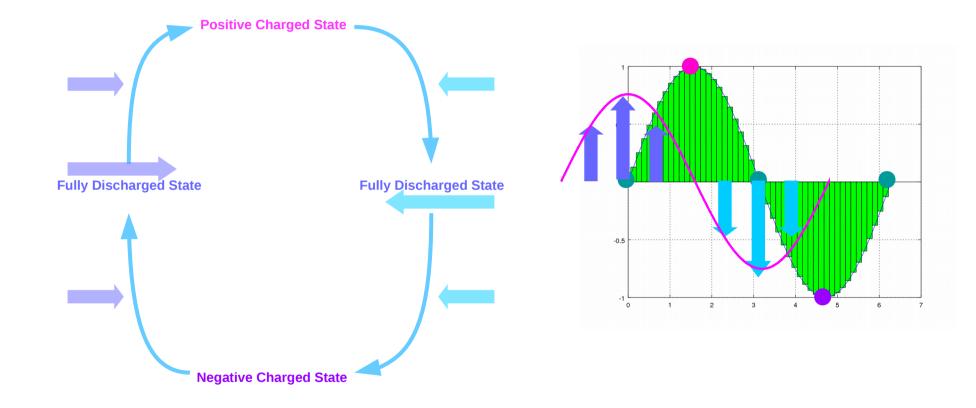
A Cycle



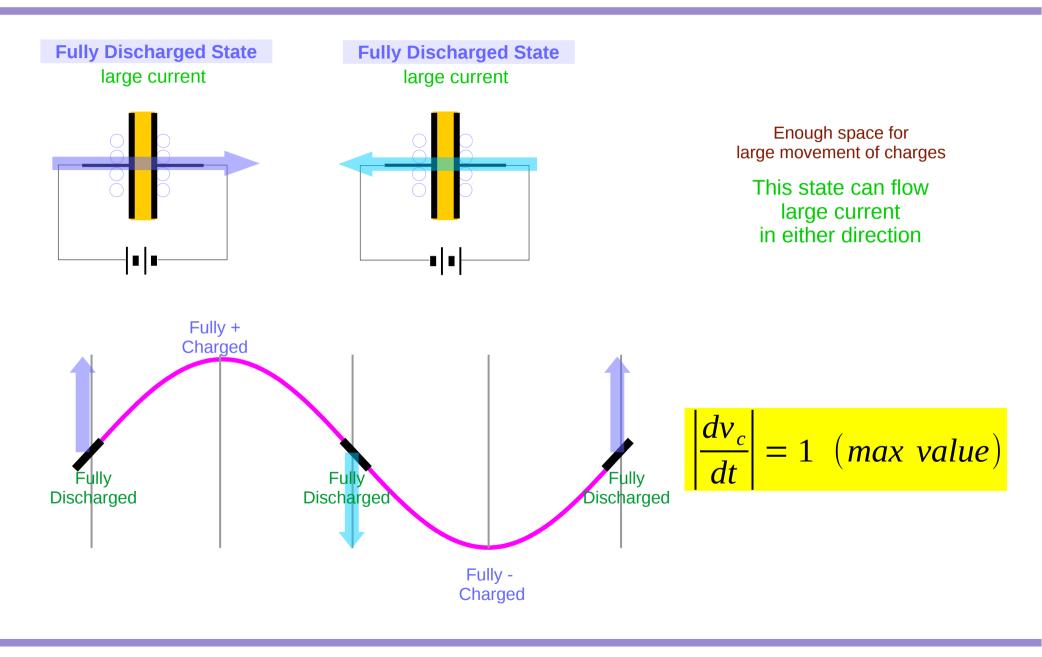
State Transition Diagram



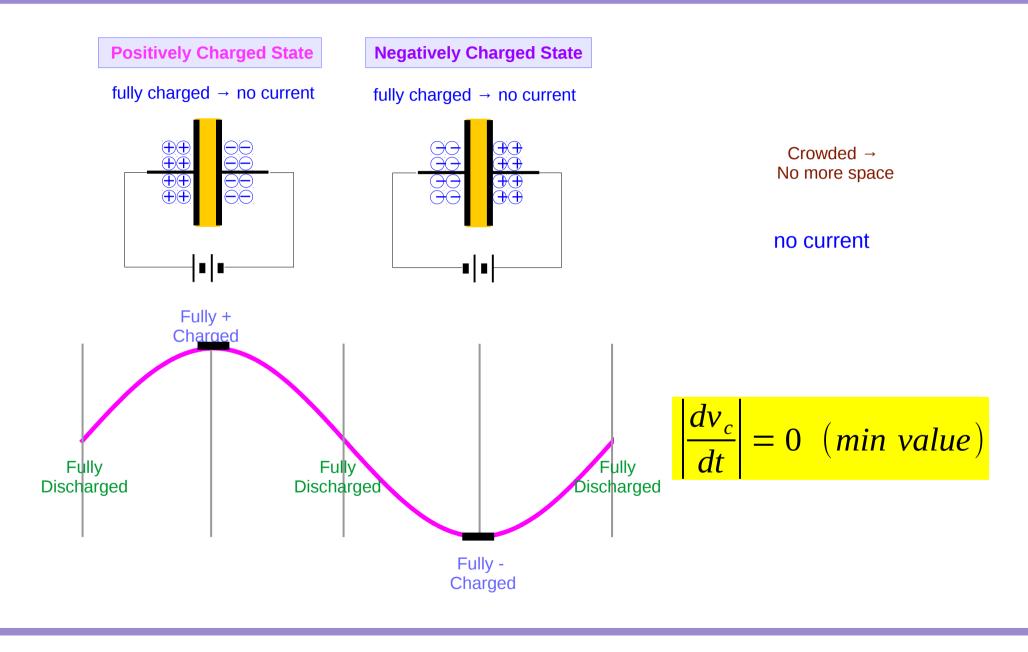




Fully Discharged : Large Current



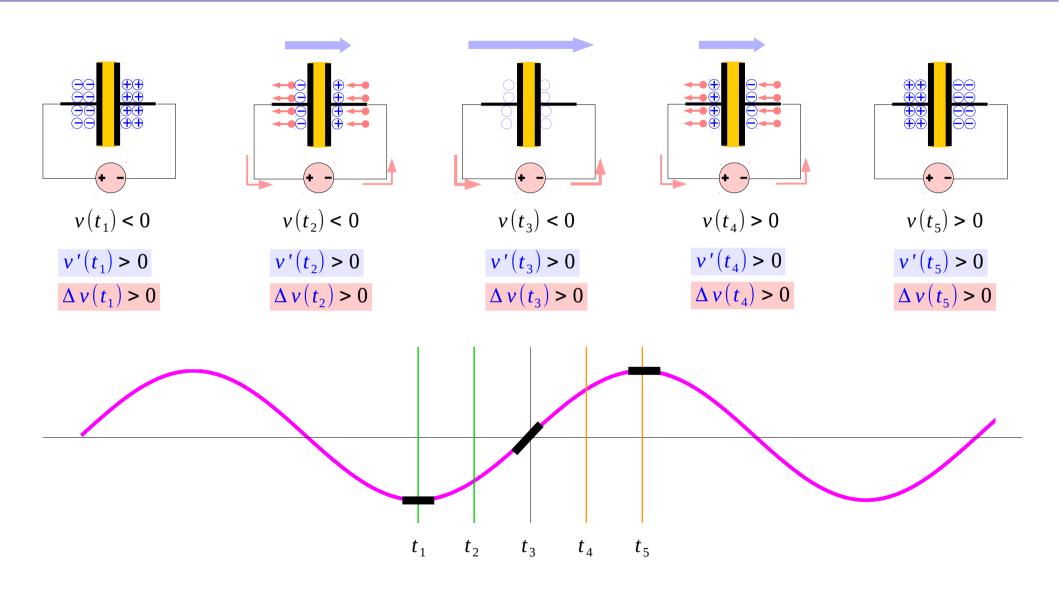
Fully Charged : Zero Current



Capacitor – AC

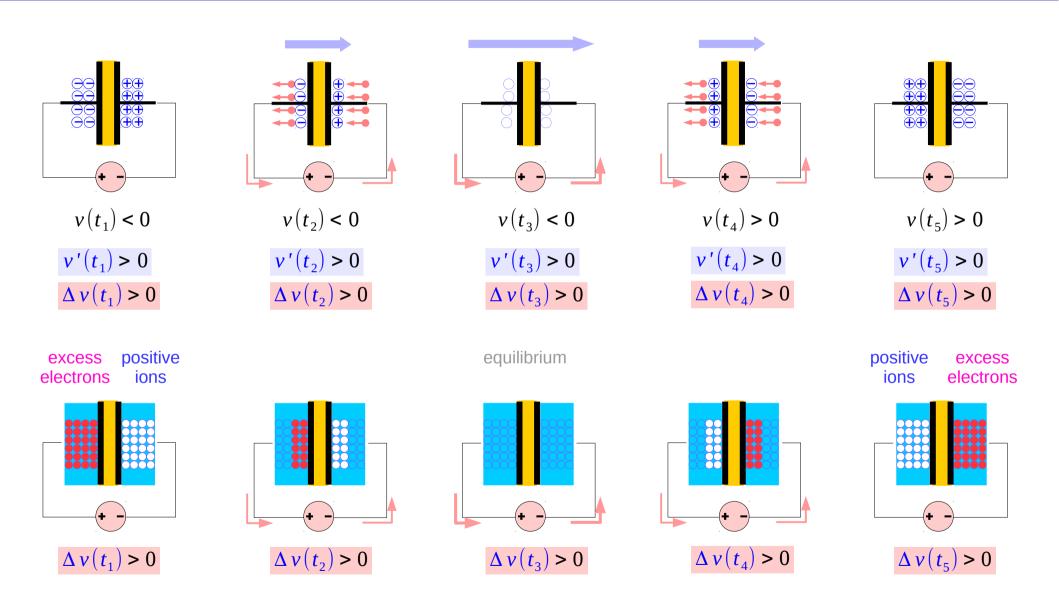
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Incrementally, Charging Positively

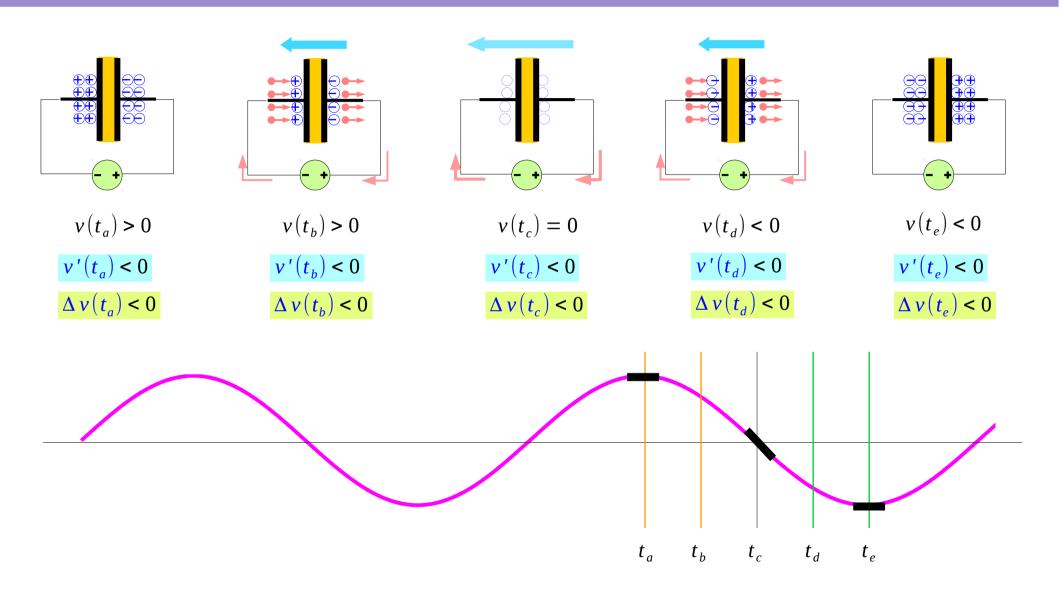


Capacitor – AC

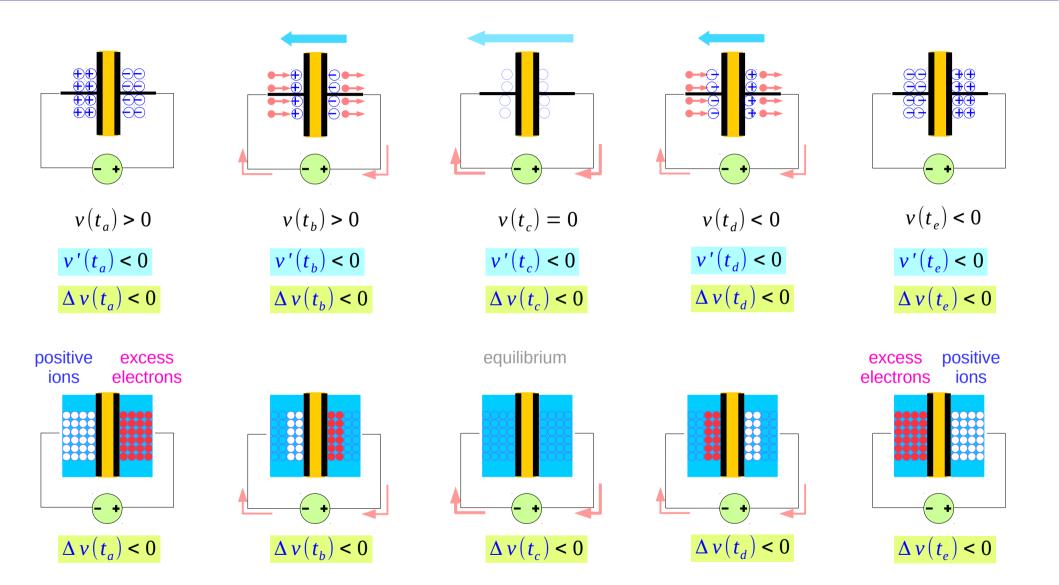
Incrementally, Charging Positively



Incrementally, Charging Negatively



Incrementally, Charging Negatively



Difference of Samples

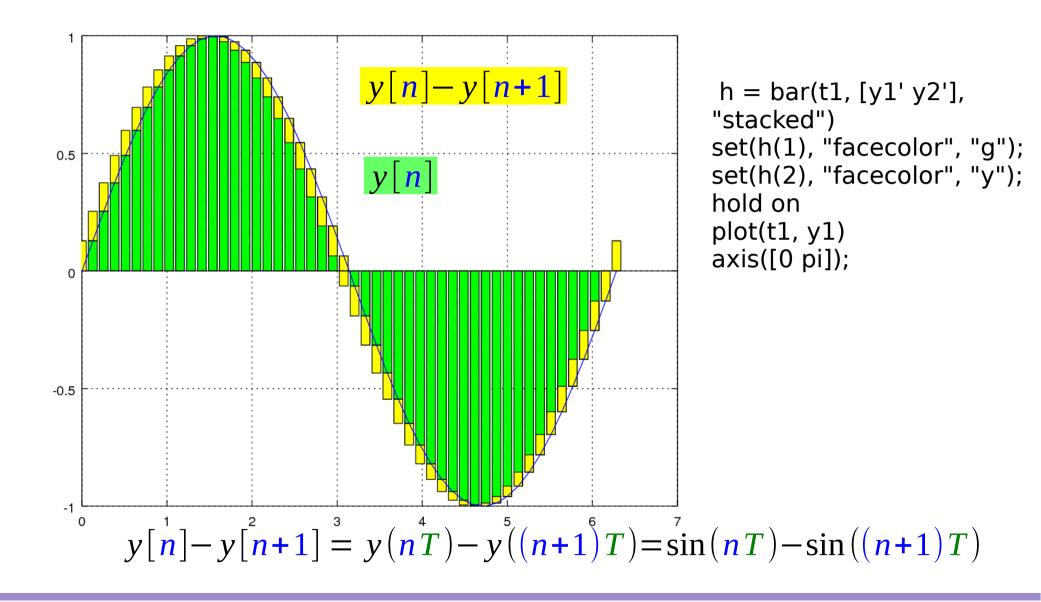
$$y(t) = \sin(t)$$

$$y[n] = \sin(nT)$$

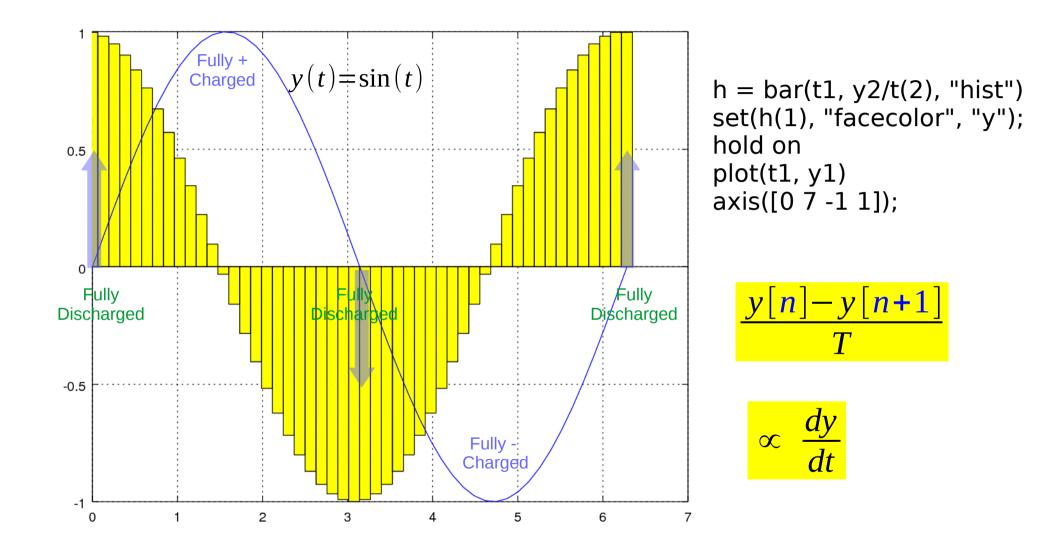
$$\frac{y[n] - y[n+1]}{y[n+1]} = \sin(nT) - \sin((n+1)T)$$

$$\frac{y[n] - y[n+1]}{T} \propto \frac{dy}{dt}$$

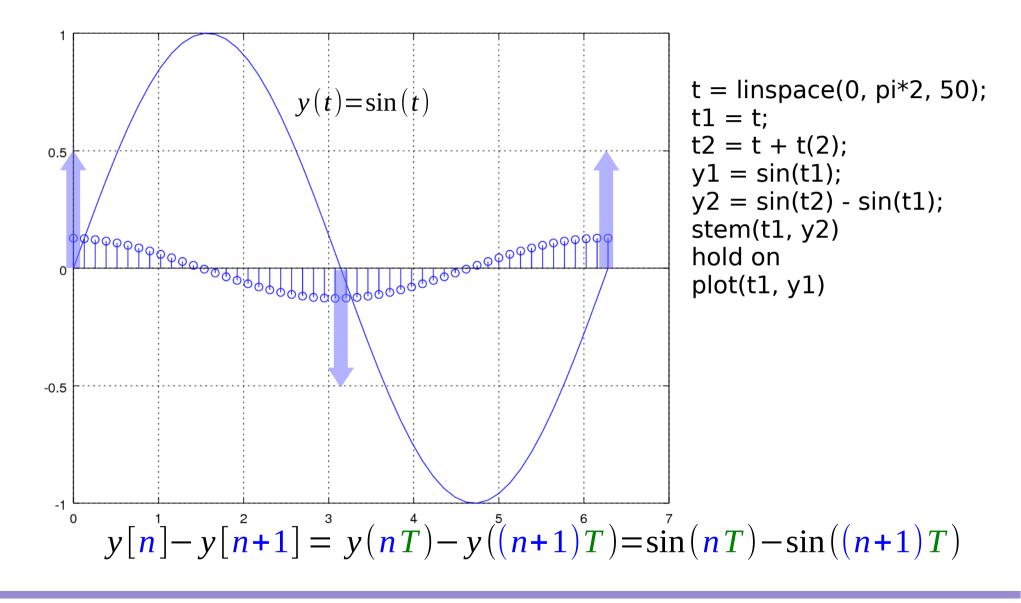
Fully Charged and Fully Discharged



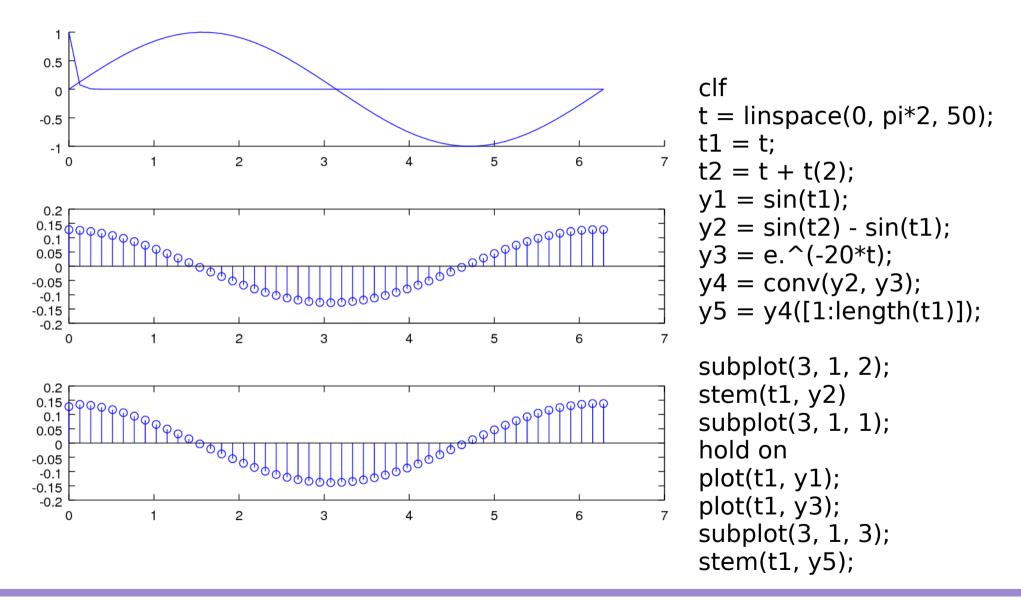
Fully Charged and Fully Discharged



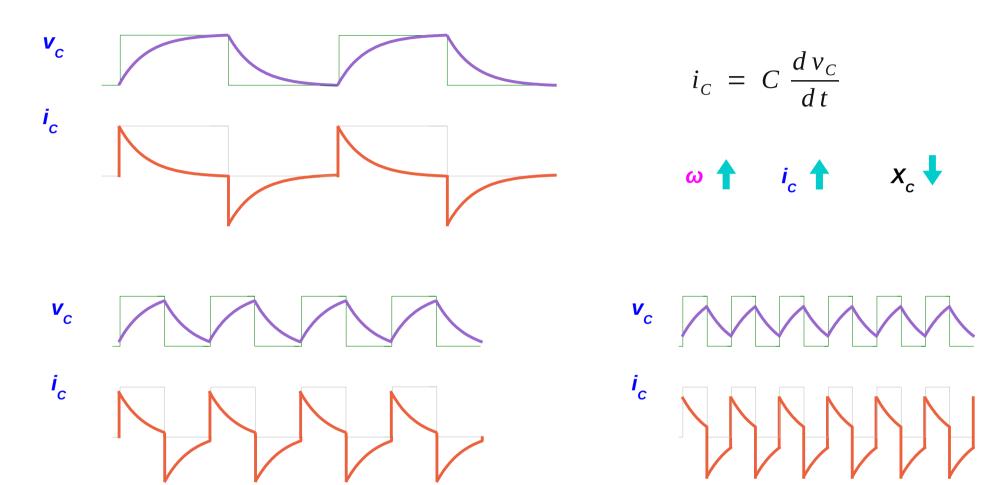
Capacitor – AC

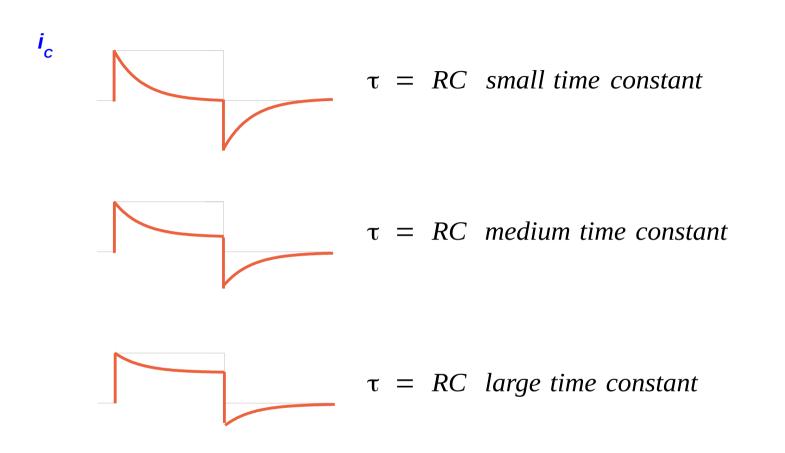


Fully Charged and Fully Discharged

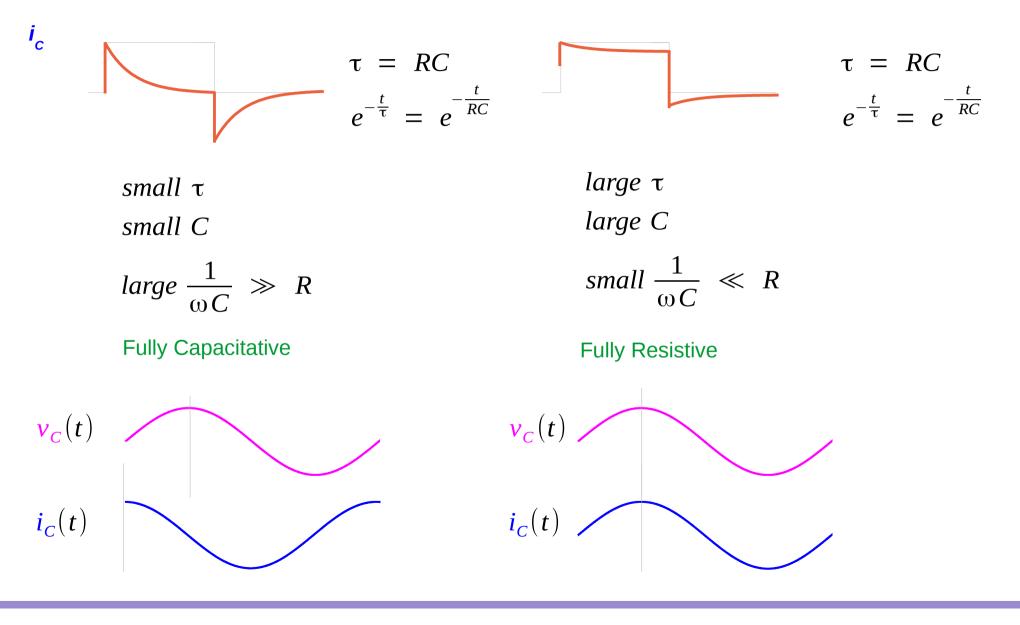


Pulse

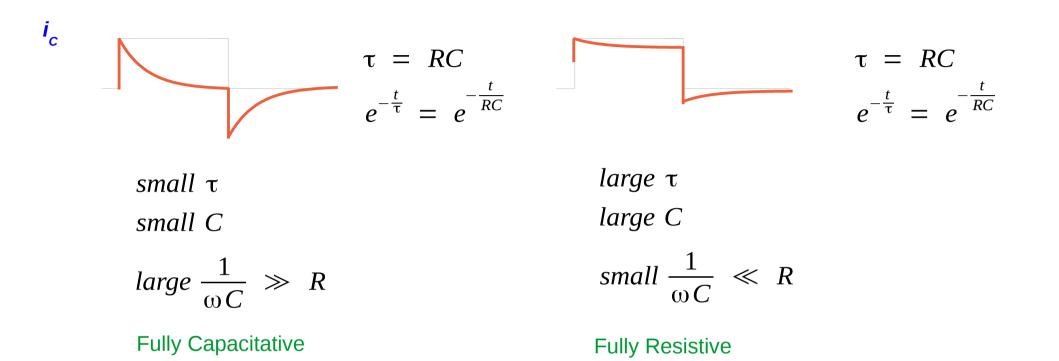




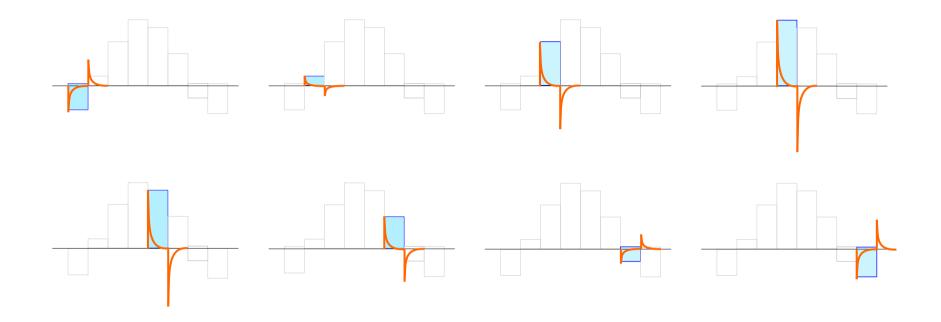
i_c $\tau_1 < \tau_2 < \tau_3$ $a_1 > a_2 > a_3$ $e^{-\frac{t}{\tau}} = e^{-\frac{t}{RC}} = e^{-at}$ $\tau = RC = \frac{1}{a}$



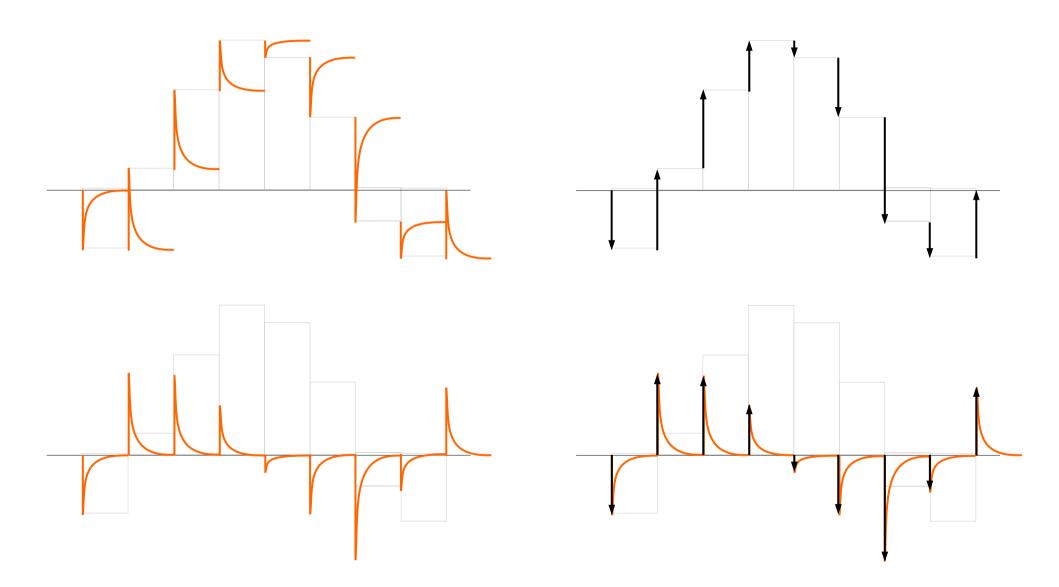
Capacitor – AC



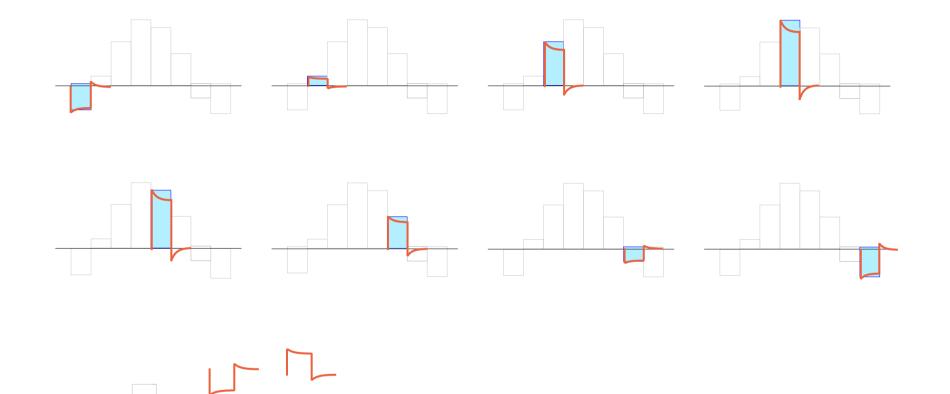
Superposition - Small Time Constant



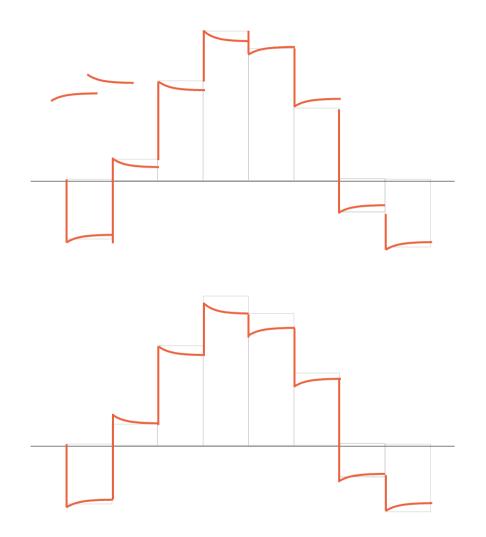
Small Time Constants

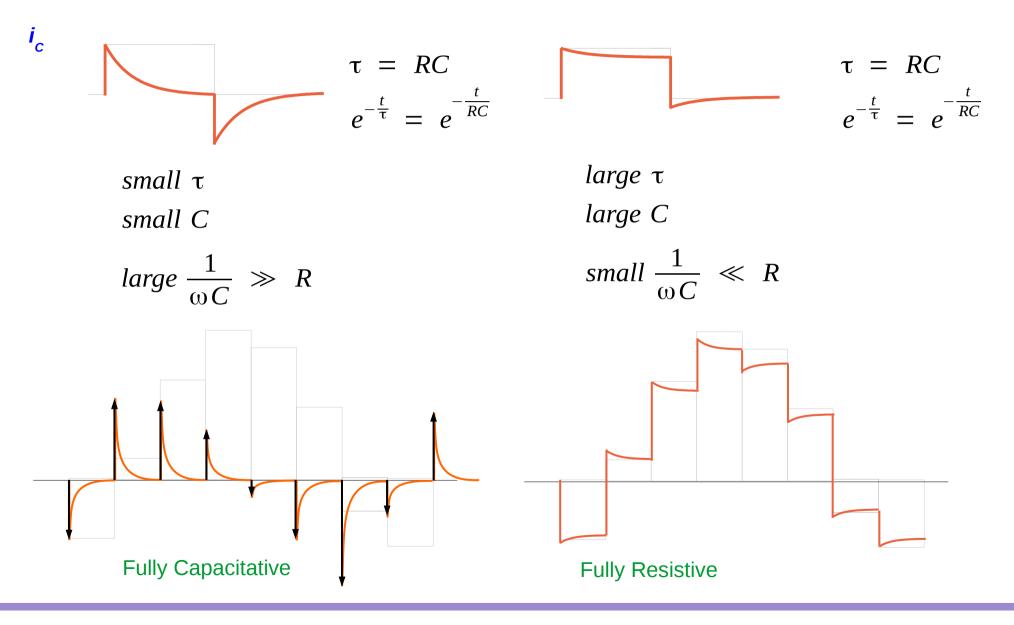


Superposition – Large Time Constant



Large Time Constants





Plotting superposition results

```
clf
t = linspace(0, pi*2, 50);
tt= linspace(0, pi*2, 500);
N = length(t);
NN= length(tt);
```

```
t1 = t;

t2 = [t(2:N), t(N)];

y1 = sin(t1);

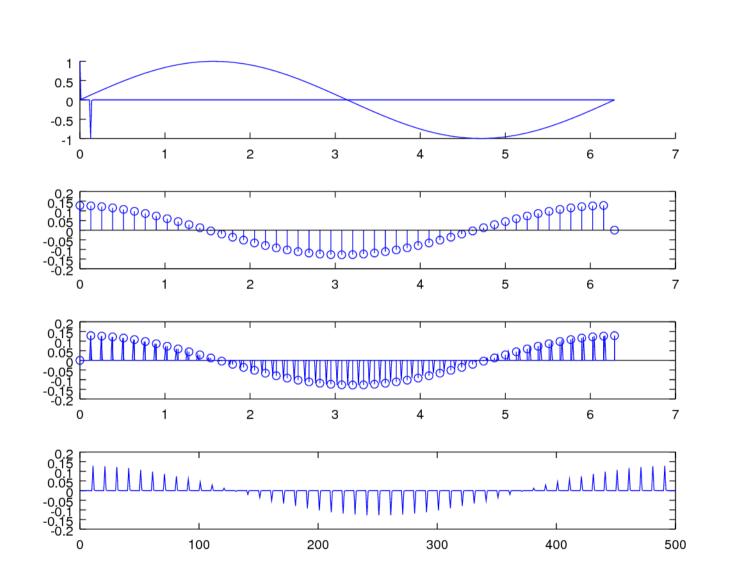
y2 = sin(t2) - sin(t1);
```

```
yy = [y1; zeros(NN/N-1, N)];
yy2= yy(:)';
a = 1/300;
yy3= e.^(-a*tt);
yy3 = yy3 - [zeros(1, NN/N),
e.^(-a*tt)](1:NN);
```

```
svec = zeros(1, NN);
for i = 1:NN;
  tvec = zeros(1, NN);
  tvec = [zeros(1, i-1), yy3];
  tvec = yy2(i) * tvec(1:NN);
  svec = svec + tvec;
endfor
yy4 = svec;
% yy4 = conv(yy2, yy3);
y5 = yy4([1:NN/N:NN]);
yy5 = yy4([1:NN]);
```

```
subplot(4, 1, 2);
stem(t1, y2)
subplot(4, 1, 1);
hold on
plot(t1, y1);
plot(tt, yy3);
subplot(4, 1, 3);
stem(t1, y5); hold on
plot(tt, yy5)
subplot(4, 1, 4);
plot(yy4);
```

Small Time Constant



$$\tau = RC$$
$$e^{-\frac{t}{\tau}} = e^{-\frac{t}{RC}}$$

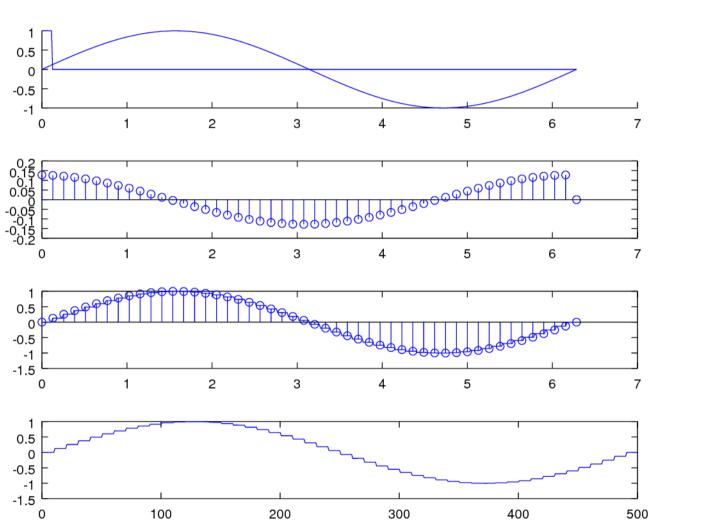
small τ small C 1

large
$$\frac{1}{\omega C}$$

Capacitor – AC

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Large Time Constant



yy = [y1; zeros(NN/N-1, N)]; yy2= yy(:)'; a = 1/300; yy3= e.^(-a*tt); yy3 = yy3 -[zeros(1, NN/N), e.^(-a*tt)](1:NN);

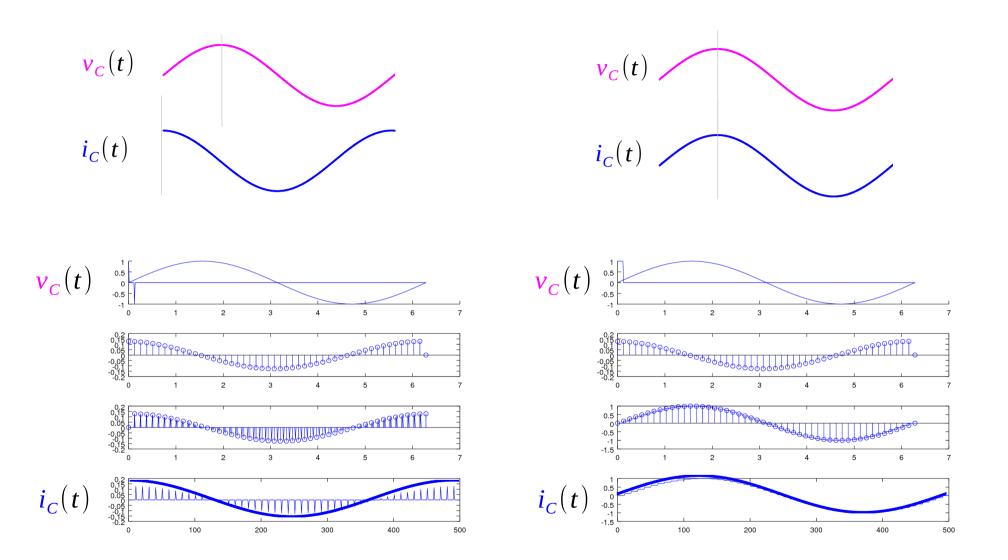
$$\tau = RC$$
$$e^{-\frac{t}{\tau}} = e^{-\frac{t}{RC}}$$

large τ large Csmall $\frac{1}{\omega C}$

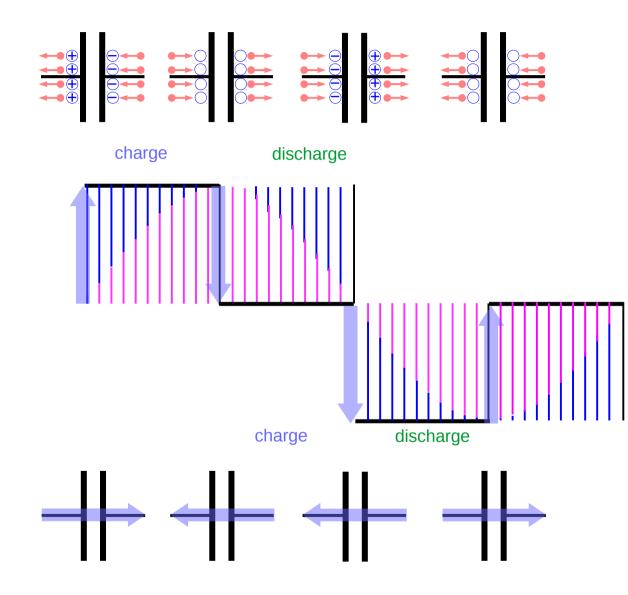
Capacitor – AC

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Envelope of the samples

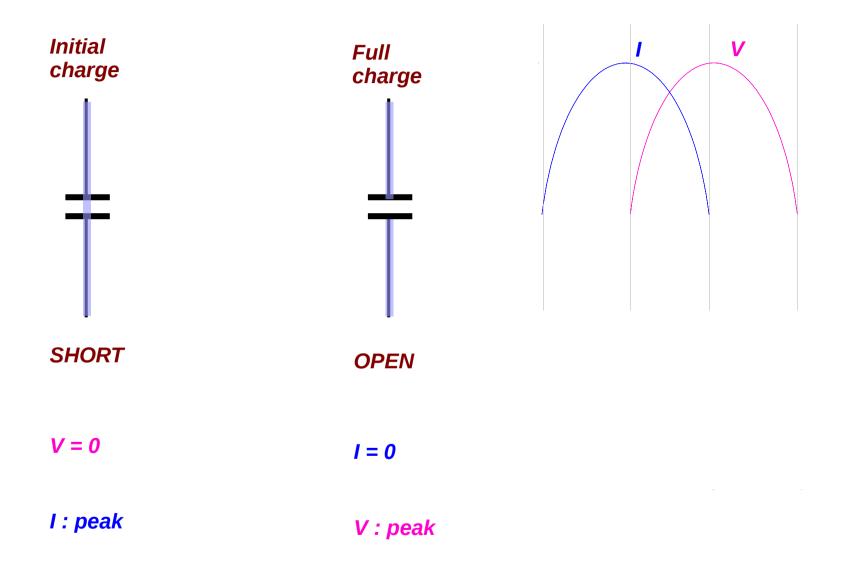


Capacitor – AC

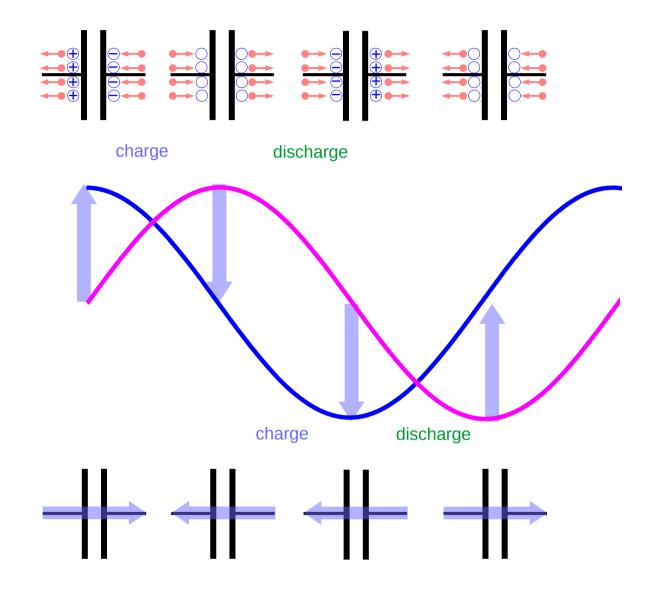


Capacitor – AC

I leads V by 90°

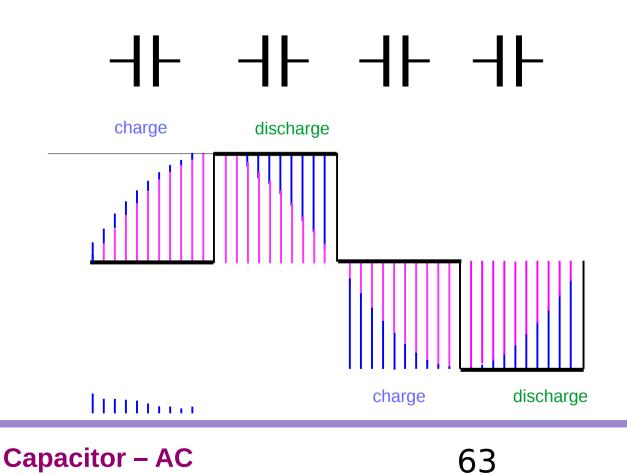


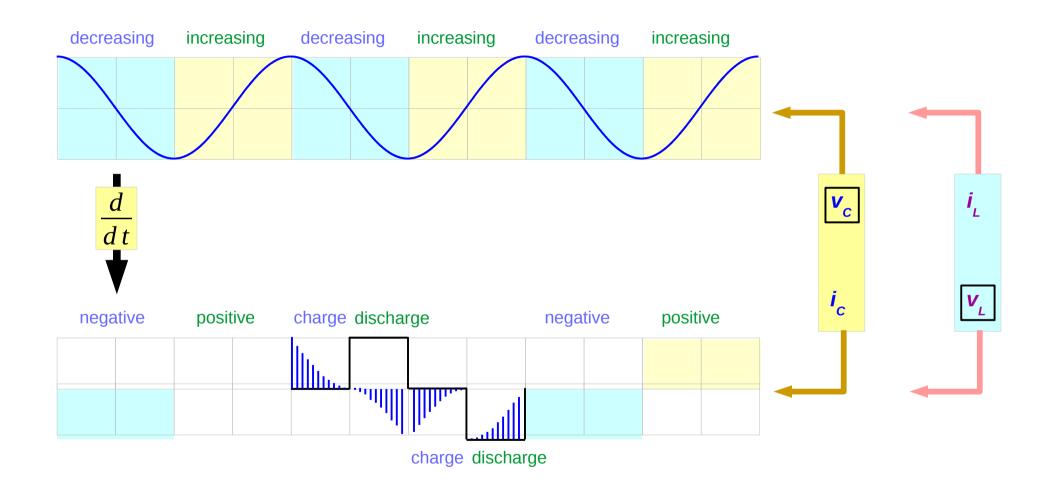
Capacitor – AC



Capacitor – AC

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References

- [1] http://en.wikipedia.org/
- [2] J.H. McClellan, et al., Signal Processing First, Pearson Prentice Hall, 2003