

Software Engineering [\[edit \]](#)

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Software Modeling [\[edit \]](#)

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Events and Triggers [\[edit \]](#)

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1. Events and Triggers

[mcuoneclipse/Event.c at master · ErichStyger/mcuoneclipse ...](#)

<https://github.com/ErichStyger/.../blob/.../Event.c> ▾ 이 페이지 번역하기

\file. * \brief Event driver implementation. * \author Erich Styger, erich.styger@hslu.ch.
*. * This module implements a generic event driver. We are using ...

[mcuoneclipse/Trigger.c at master · ErichStyger ...](#)

<https://github.com/ErichStyger/.../Trigger.c> ▾ 이 페이지 번역하기

\file. * \brief Implementation of generic triggers. * \author Erich Styger, erich.styger@hslu.ch. *. * This module implements a trigger module. * Triggers are special ...

2. Interrupt

[\[PDF\] Lecture 19: Interrupt Basics](#)

https://paws.kettering.edu/.../19-Interrupt_Basics_Ha... ▾ 이 페이지 번역하기

Lecture 19: Interrupt Basics. Page 2. Today's Goals. • Understand fundamental concepts of interrupts. • Learn about components of an interrupt-capable device.

3. Critical Section, Reentrant Code

[\[PDF\] Simple task synchronization.pdf](#)

eecs.vanderbilt.edu/.../ee276/.../11%20Simple%20tas... ▾ 이 페이지 번역하기

EECE 276 – Embedded Systems. 1. EECE 276. Embedded Systems. Simple task synchronization. Semaphores and shared data ...

4. Control & Status Register

<http://asics.chuckbenz.com/>

csrGen - generate verilog RTL code for processor memory maps in ASIC/FPGA designs

[csrGen perl script](#)

csrGen is a tool to automatically build verilog RTL for the CSRs in processor interfaces of many ASIC/FPGA designs. csrGen was [described in EE Times on Sept 9, 2002](#), and presented in [my paper](#) at SNUG San Jose, 2003 (SyNopsys Users Group). An [updated user's manual is now available](#), with additions and corrections to the info in the SNUG paper. An older, briefer description of csrGen is [csrGen.txt](#). [example.csrs](#) is a very simple example of how to use csrGen and [example_lp.v](#) is [the resulting verilog code](#). Also, I recently wrote [some notes](#) on how to adapt 2 interfaces (separate asynchronous clock domains) to one set of CSRs.

Updated: Aug '04 csrGen now produces HTML documentation of the register map. Any info contained between double quotes will be put into the description column for the address or register field. The HTML can then be copied into other document editors, for example it works great with MS Word. [example!](#)

5. ARM Assembly

[PDF] [Experiment 1 - Introduction to the Laboratory - The ZAP G...](#)

www.zap.org.au/elec2041-cdrom/.../experiment1.pdf - 이 페이지 번역하기

2003. 3. 6. - 1 -. Experiment 1: Introduction to the Laboratory. This first ... this site at <http://subjects.ee.unsw.edu.au/~elec2041/>. 8 See the excellent Jargon File on the Web at <http://www.catb.org/~esr/jargon/html/> for more informa-.

[PDF] [Experiment 2 - Introduction to Assembly ... - The ZAP Group](#)

www.zap.org.au/elec2041-cdrom/.../experiment2.pdf ▾ 이 페이지 번역하기

Introduction to Assembly Language Programming ... introduce simple ARM assembly language instructions, in Experiment 1) to read these files on-line:.

6. ELF

[A Whirlwind Tutorial on Creating Really Teensy ELF ...](#)

cseweb.ucsd.edu/~ricko/CSE131/teensyELF.htm ▾ 이 페이지 번역하기

(Of course, the more practical purpose of this document is to describe a few of the inner workings of the ELF file format and the Linux operating system.

<https://upload.wikimedia.org/wikiversity/en/d/d4/ELF.20160328.pdf>

https://en.wikiversity.org/wiki/C_programming_in_plain_view

Compilations [\[edit \]](#)

- [Preprocessor \(pdf\)](#)
- [Compiler \(pdf\)](#)
- [Assembler \(pdf\)](#)
- [Library \(pdf\)](#)
- [Linker \(\)](#)
- [Binutil \(pdf\)](#)
- [ELF \(pdf\)](#)

→ elf