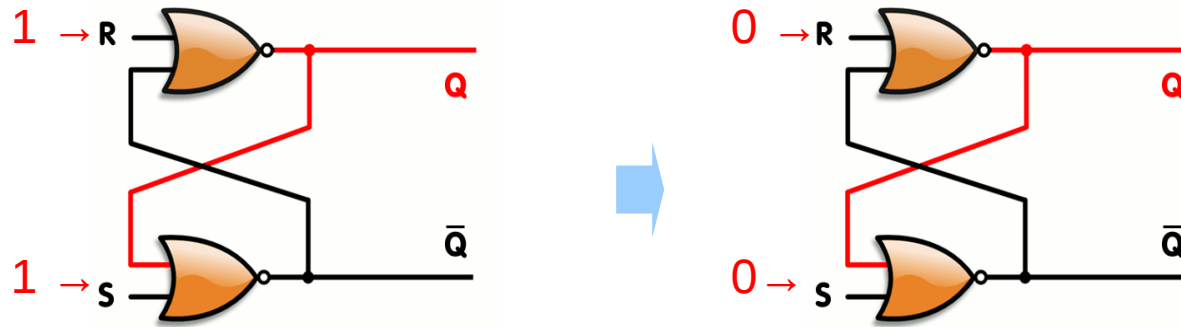
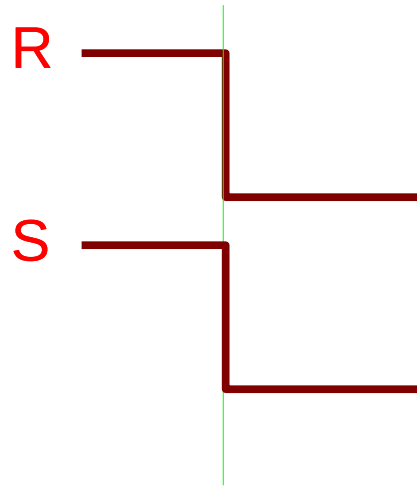


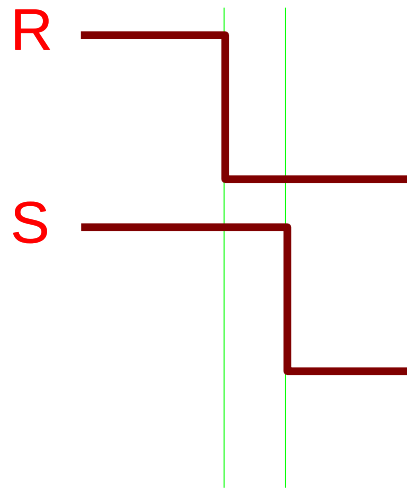
# #1 NOR RS Latch – Forbidden State



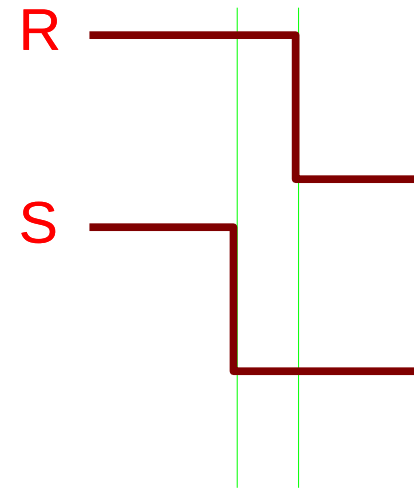
Case I



Case II

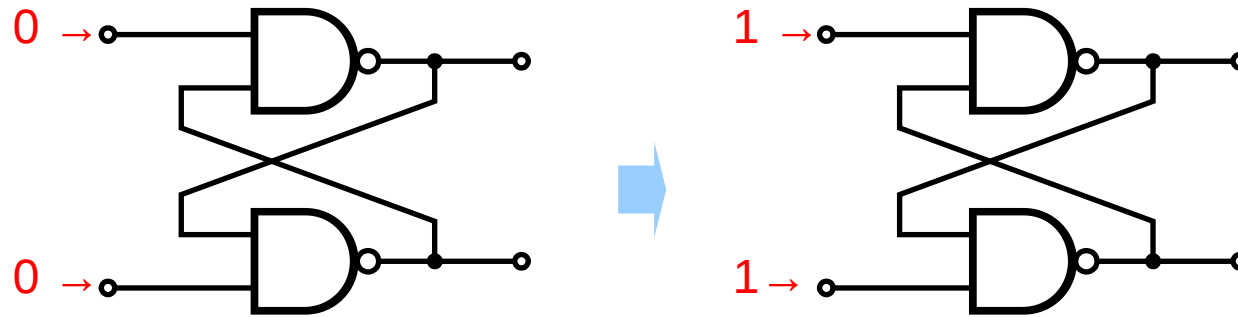


Case III

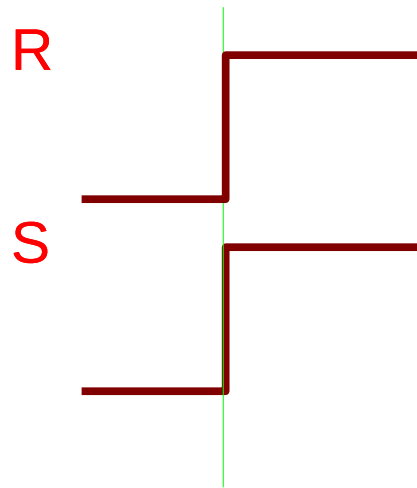


In real life, two edges can hardly be aligned. → Case II, III  
Complete the waveforms for Case I, II, III.

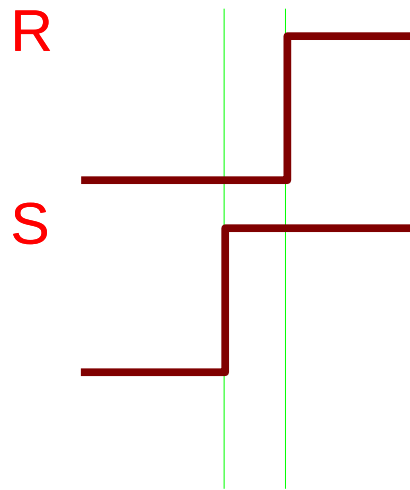
## #2 NAND RS Latch – Forbidden State



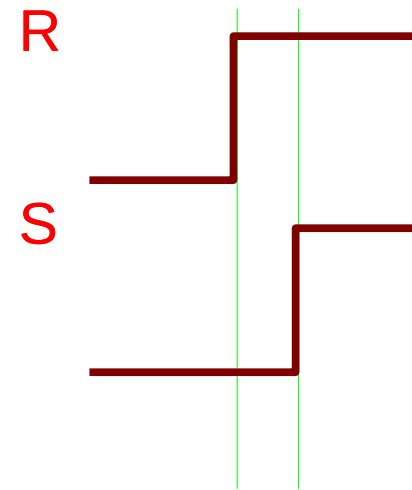
Case I



Case II



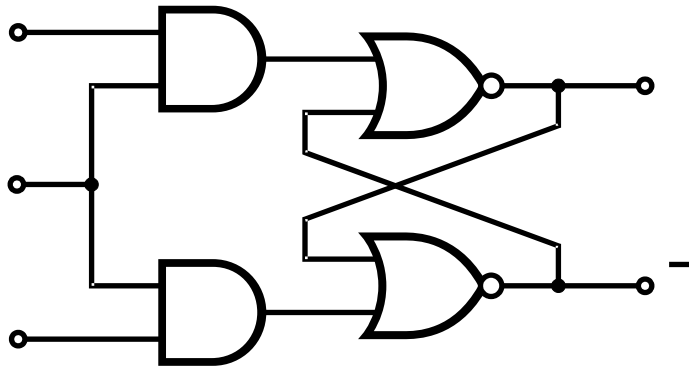
Case III



In real life, two edges can hardly be aligned. → Case II, III  
Complete the waveforms for Case I, II, III.

### #3 Indeterminate State in SR latch with control input

Case I



Case II

cf) Fig 5.5

Describe the indeterminate state in detail  
(as in previous problems)