## BJT Topologies -Analysis (H.3)

20170609-2

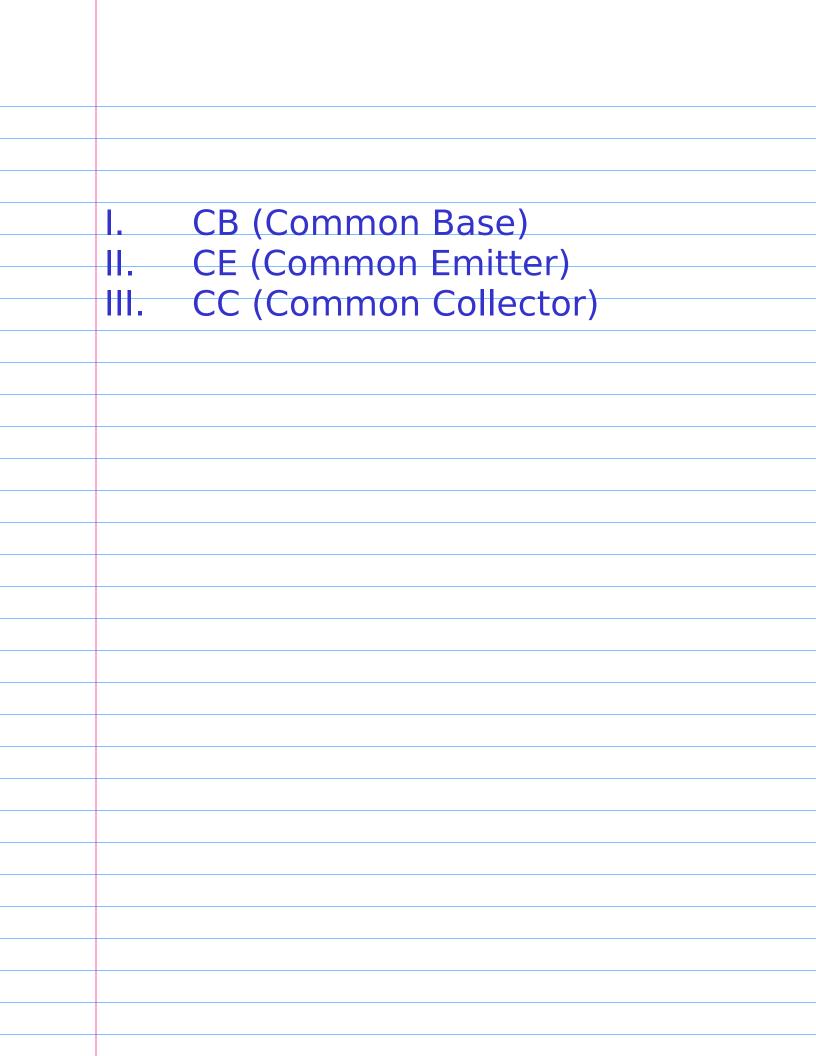
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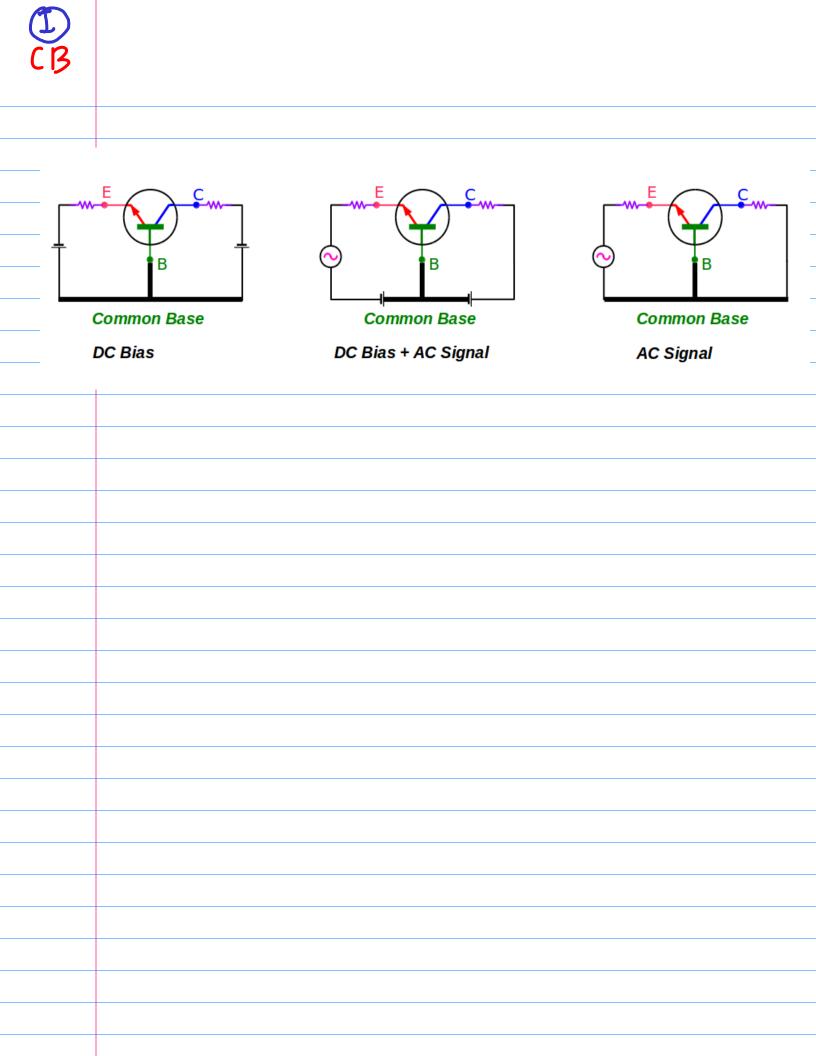
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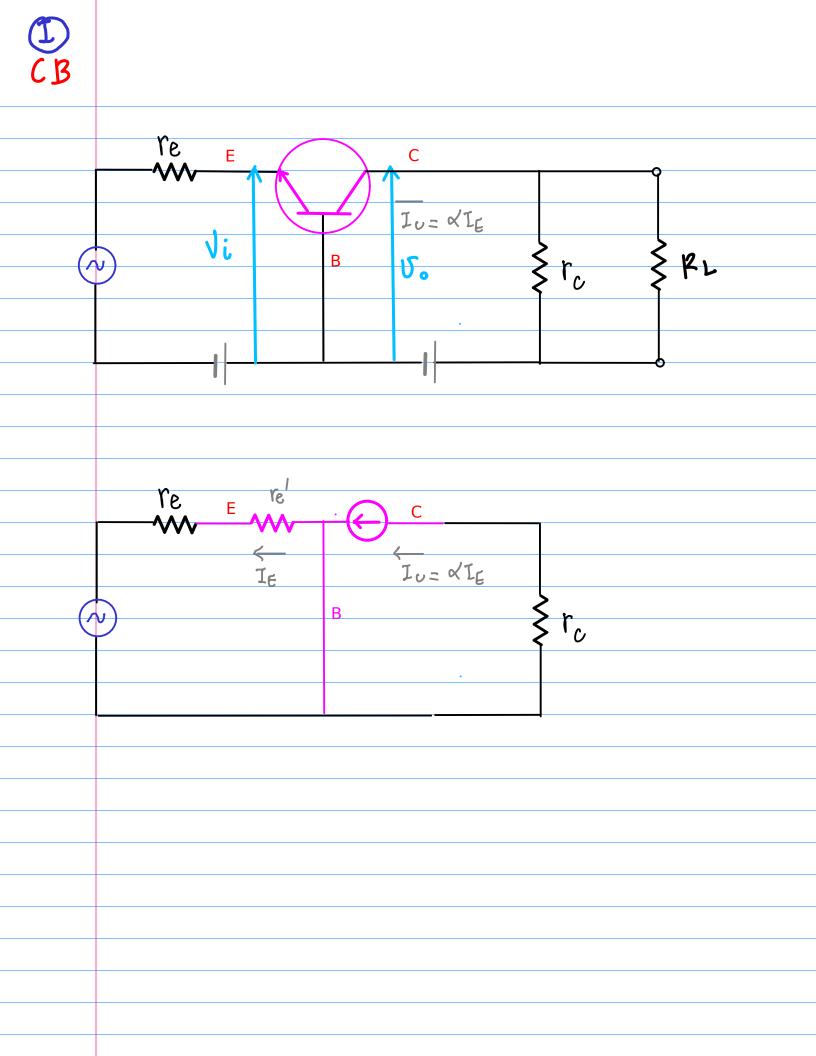
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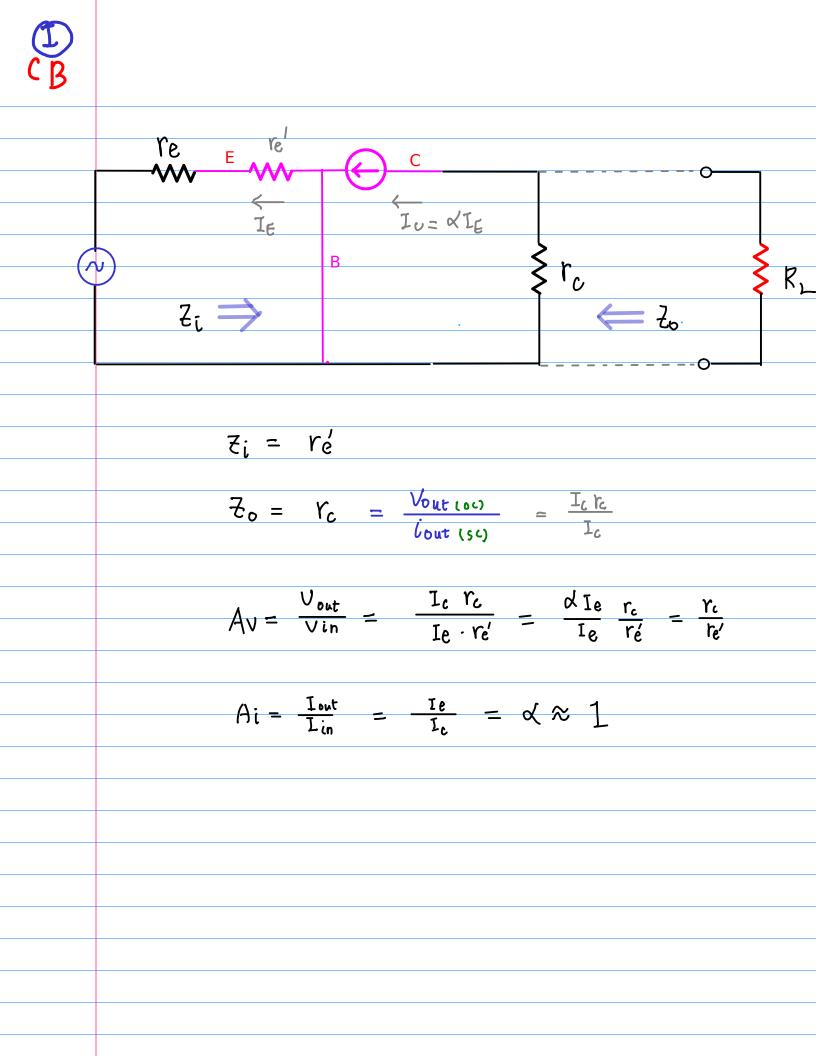
## References

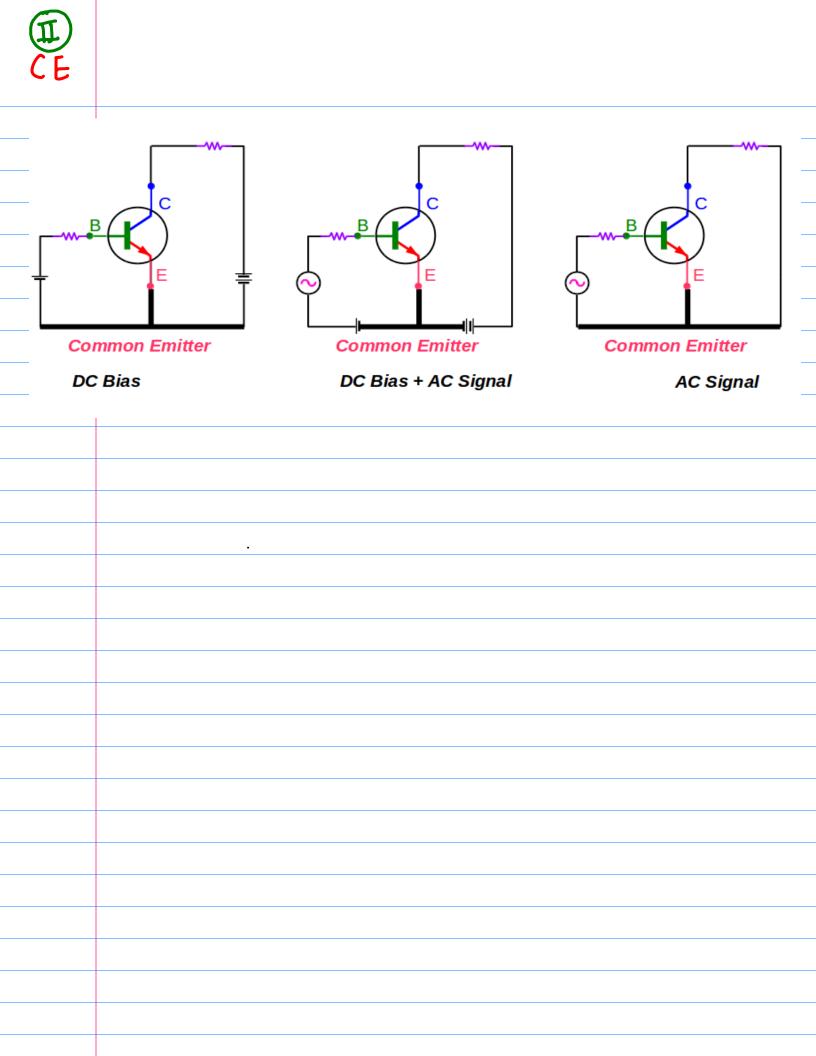
Based
[1] Floyd Electronic Devices 7th ed
[1] Floyd, Electronic Devices 7th ed [2] Cook,
[2] en.wikipedia.org
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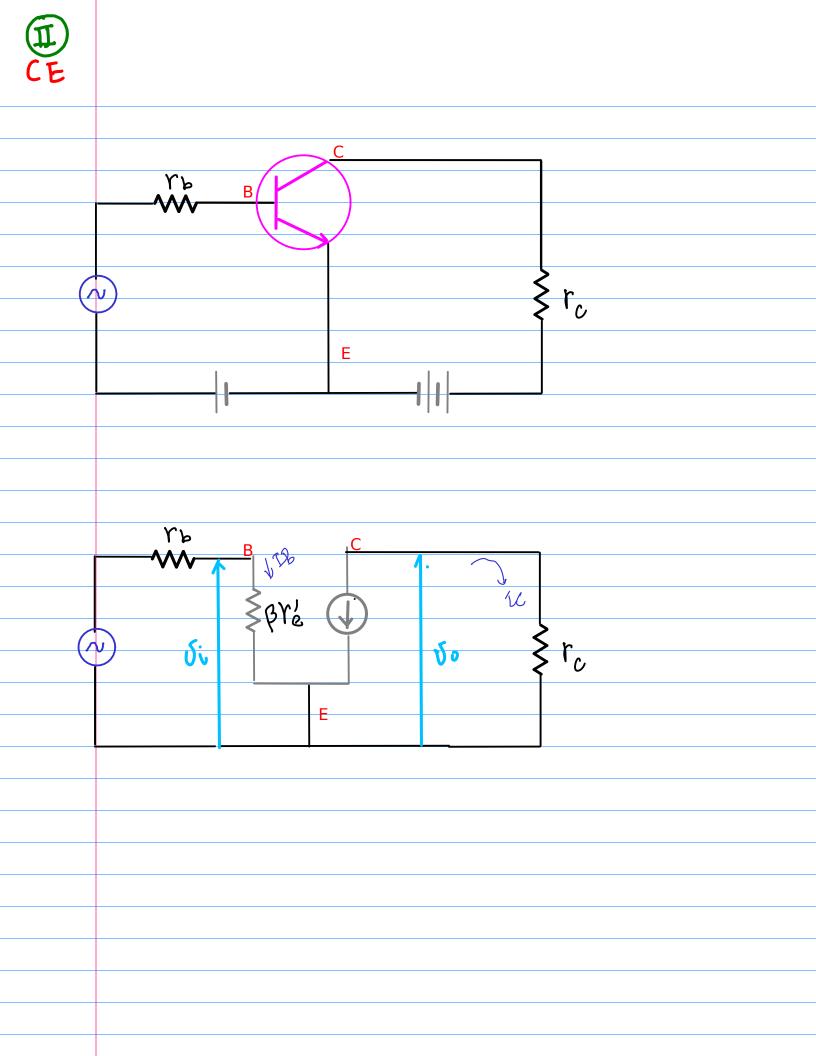


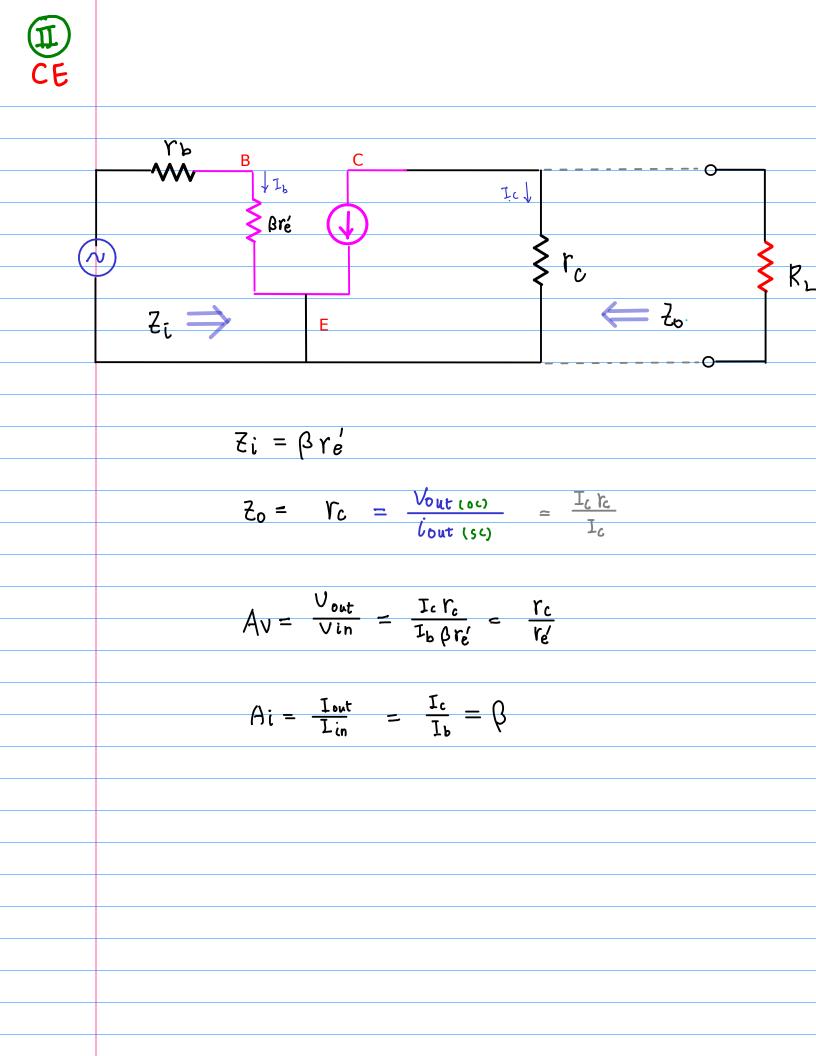


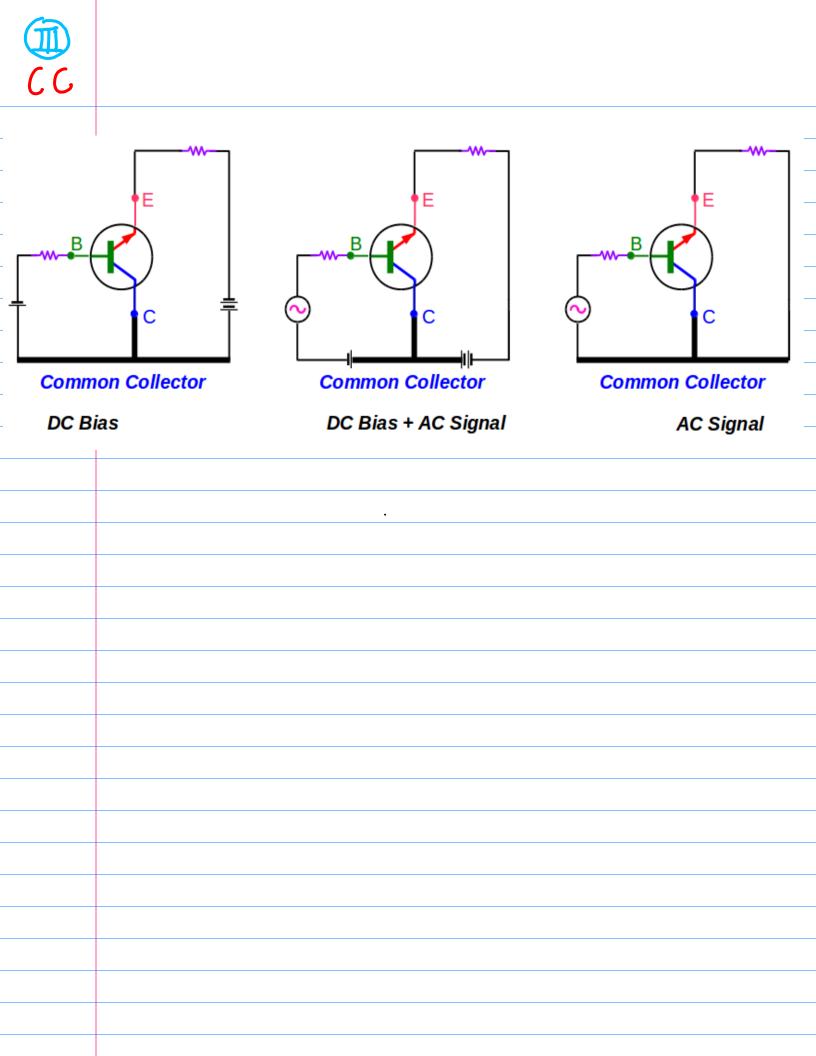


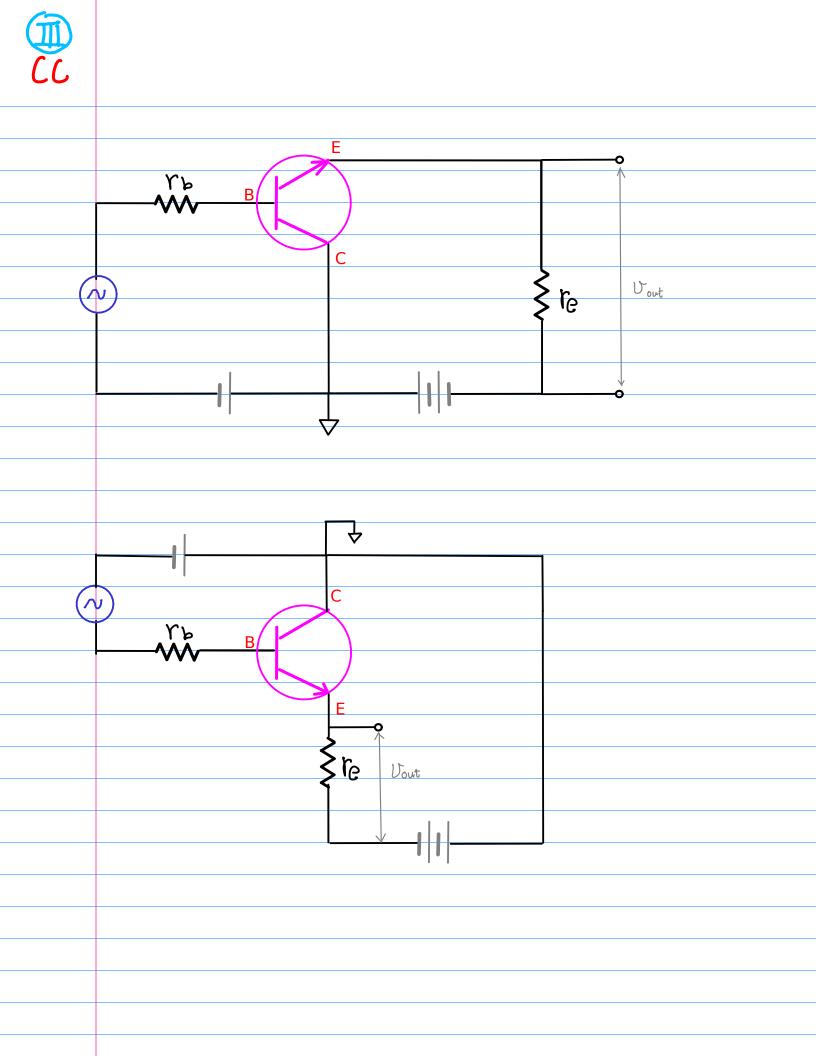


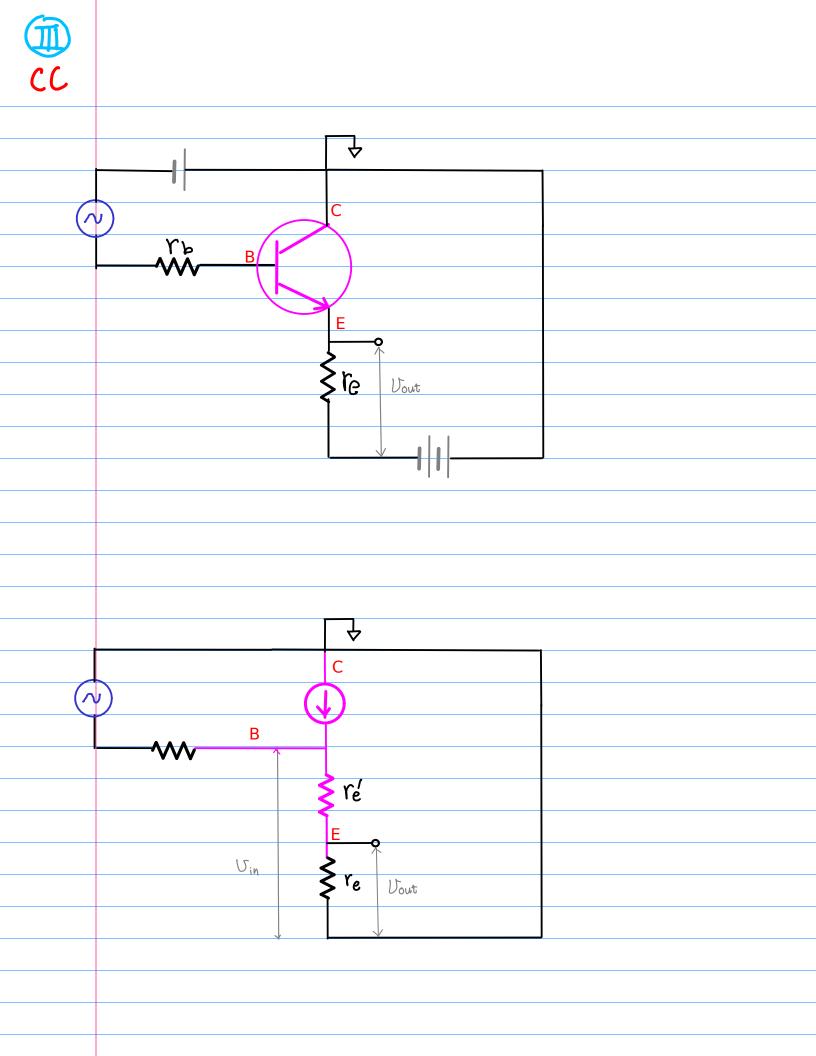


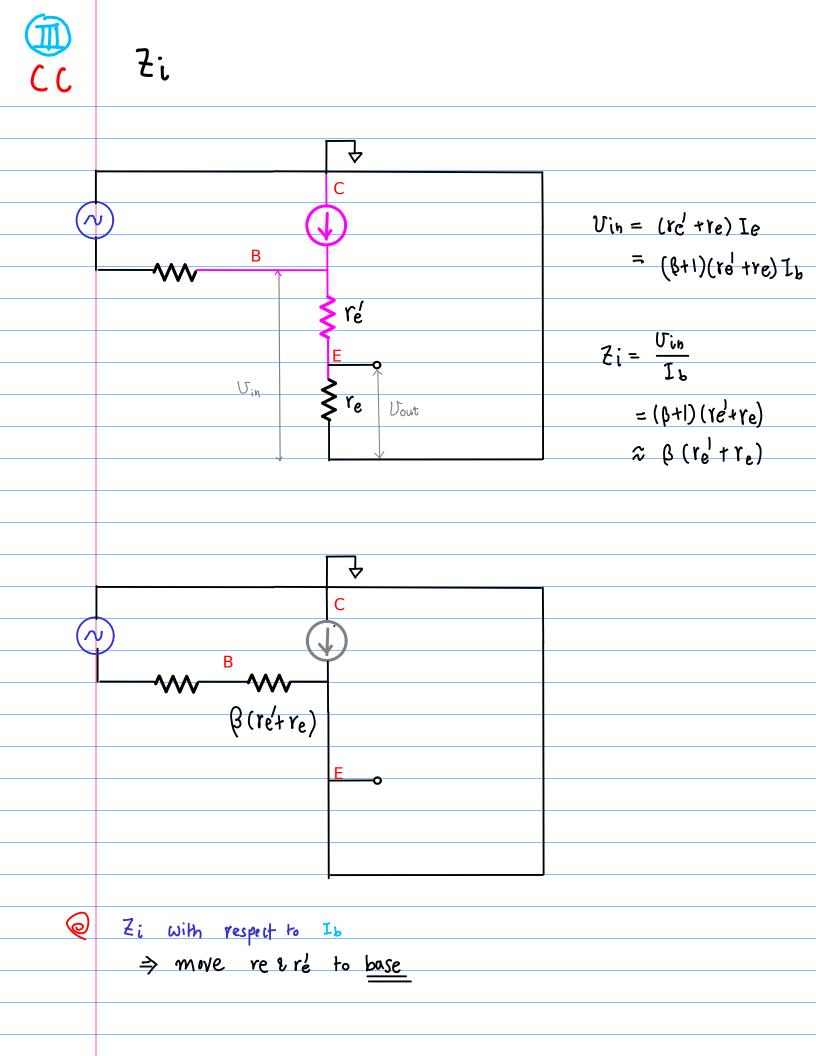


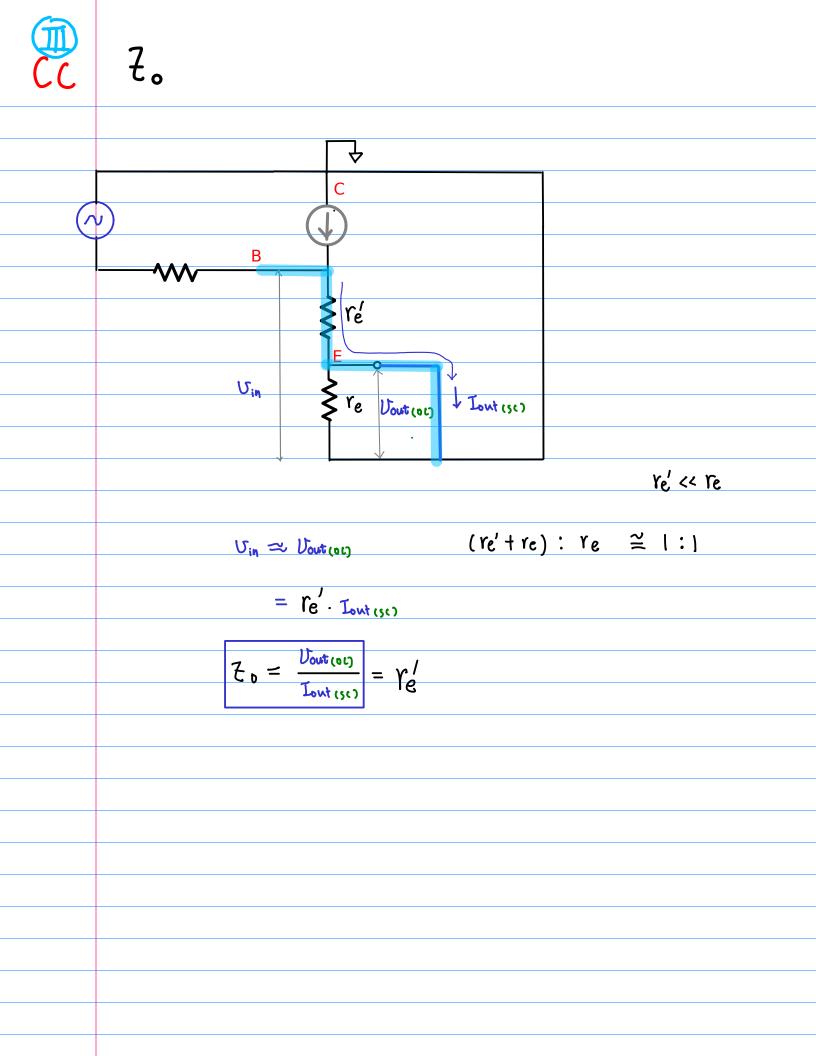












Ŷ С В **ř**ré re' **≶** ₹r<sub>e</sub> s re Zo (base) = re' || re Vout ≃ re' Q Zo with respect to Ie ⇒ move re & ré to <u>emitter</u>

